

new/usr/src/uts/common/io/rwn/rt2860.c

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*****
85647 Tue Jan 15 10:21:08 2019
new/usr/src/uts/common/io/rwn/rt2860.c
10090 rt2860_free_rx_ring doesn't need to check for NULL array
*****
1 /*
2  * Copyright 2017 Gary Mills
3  * Copyright 2010 Sun Microsystems, Inc. All rights reserved.
4  * Use is subject to license terms.
5  * Copyright (c) 2018, Joyent, Inc.
6  */

8 /*
9  * Copyright (c) 2007, 2008
10 * Damien Bergamini <damien.bergamini@free.fr>
11 *
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21 * ACTION OF CONTRACT, NEGLIGENCE OR OTHER TORTIOUS ACTION, ARISING OUT OF
22 * OR IN CONNECTION WITH THE USE OR PERFORMANCE OF THIS SOFTWARE.
23 */

25 /*
26  * Ralink Technology RT2860 chipset driver
27  * http://www.ralinktech.com/
28  */

30 #include <sys/types.h>
31 #include <sys/byteorder.h>
32 #include <sys/conf.h>
33 #include <sys/cmn_err.h>
34 #include <sys/stat.h>
35 #include <sys/ddi.h>
36 #include <sys/sunddi.h>
37 #include <sys/strsubr.h>
38 #include <inet/common.h>
39 #include <sys/note.h>
40 #include <sys/stream.h>
41 #include <sys/strsun.h>
42 #include <sys/modctl.h>
43 #include <sys/devops.h>
44 #include <sys/mac_provider.h>
45 #include <sys/mac_wifi.h>
46 #include <sys/net80211.h>
47 #include <sys/net80211_proto.h>
48 #include <sys/varargs.h>
49 #include <sys/pci.h>
50 #include <sys/crypto/common.h>
51 #include <sys/crypto/api.h>
52 #include <inet/wifi_ioctl.h>

54 #include "rt2860_reg.h"
55 #include "rt2860_var.h"

57 #define RT2860_DBG_80211      (1 << 0)
58 #define RT2860_DBG_DMA      (1 << 1)
59 #define RT2860_DBG_EEPROM   (1 << 2)
60 #define RT2860_DBG_FW       (1 << 3)
61 #define RT2860_DBG_HW       (1 << 4)
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62 #define RT2860_DBG_INTR      (1 << 5)
63 #define RT2860_DBG_RX       (1 << 6)
64 #define RT2860_DBG_SCAN     (1 << 7)
65 #define RT2860_DBG_TX       (1 << 8)
66 #define RT2860_DBG_RADIO    (1 << 9)
67 #define RT2860_DBG_RESUME   (1 << 10)
68 #define RT2860_DBG_MSG      (1 << 11)

70 uint32_t rt2860_dbg_flags = 0x0;

72 #ifdef DEBUG
73 #define RWN_DEBUG \
74     rt2860_debug
75 #else
76 #define RWN_DEBUG
77 #endif

79 static void *rt2860_soft_state_p = NULL;
80 static uint8_t rt2860_fw_bin [] = {
81 #include "fw-rt2860/rt2860.unicode"
82 };
unchanged portion omitted

893 /*ARGSUSED*/
894 static void
895 rt2860_free_rx_ring(struct rt2860_softc *sc, struct rt2860_rx_ring *ring)
896 {
897     struct rt2860_rx_data *data;
898     int i, count;

900     if (ring->rxdesc != NULL)
901         rt2860_free_dma_mem(&ring->rxdesc_dma);

903     count = RT2860_RX_RING_COUNT;

903     if (ring->data != NULL) {
905         for (i = 0; i < count; i++) {
906             data = &ring->data[i];
907             rt2860_free_dma_mem(&data->rxbuf_dma);
908         }
909     }
unchanged portion omitted
```