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4879 Wed Feb 11 23:21:00 2015
new/usr/src/uts/intel/ia32/ml/desctbls_asm.s
5606 support build with binutils 2.25
Reviewed by: Josef 'Jeff' Sipek <josef.sipek@nexenta.com>
Reviewed by: Marcel Telka <marcel.telka@nexenta.com>
Reviewed by: Piotr Jasiukajtis <estibi@me.com>
Reviewed by: Richard Lowe <richlowe@richlowe.net>
*****
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26 #pragma ident      "%Z%M% %I%      %E% SMI"

26 #include <sys/asm_linkage.h>
27 #include <sys/asm_misc.h>
28 #include <sys/regset.h>
29 #include <sys/panic.h>
30 #include <sys/ontrap.h>
31 #include <sys/privregs.h>
32 #include <sys/segments.h>
33 #include <sys/trap.h>

35 #if defined(__lint)
36 #include <sys/types.h>
37 #include <sys/system.h>
38 #include <sys/thread.h>
39 #include <sys/archsystem.h>
40 #include <sys/byteorder.h>
41 #include <sys/dtrace.h>
42 #include <sys/x86_archext.h>
43 #else /* __lint */
44 #include "assym.h"
45 #endif /* __lint */

47 #if defined(__lint)

49 /*ARGSUSED*/
50 void
51 rd_idtr(desctbr_t *idtr)
52 {}

54 /*ARGSUSED*/
55 void

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56 wr_idtr(desctbr_t *idtr)
57 {}

59 #else /* __lint */

61 #if defined(__amd64)

63     ENTRY_NP(rd_idtr)
64     sidt    (%rdi)
65     ret
66     SET_SIZE(rd_idtr)
_____ unchanged_portion_omitted _____

198     ENTRY_NP(get_cs_register)
199     movq    %cs, %rax
201     movq    $0, %rax
202     movw    %cs, %rax
200     ret
201     SET_SIZE(get_cs_register)
_____ unchanged_portion_omitted _____

```