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*****
28786 Mon Jan 28 16:02:58 2013
new/usr/src/uts/common/io/usb/usb/hcdi.c
3503 usb doesn't initialize cc_no_resources, causes gibberish output in kstat:
*****
_____unchanged_portion_omitted_____

266 /*
267  * Allocate the hotplug kstats structure
268  */
269 void
270 usb_hcdi_create_stats(usb_hcdi_t *hcdi, int instance)
271 {
272     char          kstatname[KSTAT_STRLEN];
273     const char    *dname = ddi_driver_name(hcdi->hcdi_dip);
274     hcdi_hotplug_stats_t *hsp;
275     hcdi_error_stats_t *esp;

277     if (HCDI_HOTPLUG_STATS(hcdi) == NULL) {
278         (void) snprintf(kstatname, KSTAT_STRLEN, "%s%d,hotplug",
279             dname, instance);
280         HCDI_HOTPLUG_STATS(hcdi) = kstat_create("usba", instance,
281             kstatname, "usb_hotplug", KSTAT_TYPE_NAMED,
282             sizeof (hcdi_hotplug_stats_t) / sizeof (kstat_named_t),
283             KSTAT_FLAG_PERSISTENT);

285         if (HCDI_HOTPLUG_STATS(hcdi) == NULL) {

287             return;
288         }

290         hsp = HCDI_HOTPLUG_STATS_DATA(hcdi);
291         kstat_named_init(&hsp->hcdi_hotplug_total_success,
292             "Total Hotplug Successes", KSTAT_DATA_UINT64);
293         kstat_named_init(&hsp->hcdi_hotplug_success,
294             "Hotplug Successes", KSTAT_DATA_UINT64);
295         kstat_named_init(&hsp->hcdi_hotplug_total_failure,
296             "Hotplug Total Failures", KSTAT_DATA_UINT64);
297         kstat_named_init(&hsp->hcdi_hotplug_failure,
298             "Hotplug Failures", KSTAT_DATA_UINT64);
299         kstat_named_init(&hsp->hcdi_device_count,
300             "Device Count", KSTAT_DATA_UINT64);

302         HCDI_HOTPLUG_STATS(hcdi)->ks_private = hcdi;
303         HCDI_HOTPLUG_STATS(hcdi)->ks_update = nulldev;
304         kstat_install(HCDI_HOTPLUG_STATS(hcdi));
305     }

307     if (HCDI_ERROR_STATS(hcdi) == NULL) {
308         (void) snprintf(kstatname, KSTAT_STRLEN, "%s%d,error",
309             dname, instance);
310         HCDI_ERROR_STATS(hcdi) = kstat_create("usba", instance,
311             kstatname, "usb_errors", KSTAT_TYPE_NAMED,
312             sizeof (hcdi_error_stats_t) / sizeof (kstat_named_t),
313             KSTAT_FLAG_PERSISTENT);

315         if (HCDI_ERROR_STATS(hcdi) == NULL) {

317             return;
318         }

320         esp = HCDI_ERROR_STATS_DATA(hcdi);
321         kstat_named_init(&esp->cc_crc, "CRC Errors", KSTAT_DATA_UINT64);
322         kstat_named_init(&esp->cc_bitstuffing,
323             "Bit Stuffing Violations", KSTAT_DATA_UINT64);

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324         kstat_named_init(&esp->cc_data_toggle_mm,
325             "Data Toggle PID Errors", KSTAT_DATA_UINT64);
326         kstat_named_init(&esp->cc_stall,
327             "Endpoint Stalls", KSTAT_DATA_UINT64);
328         kstat_named_init(&esp->cc_dev_not_resp,
329             "Device Not Responding", KSTAT_DATA_UINT64);
330         kstat_named_init(&esp->cc_pid_checkfailure,
331             "PID Check Bit Errors", KSTAT_DATA_UINT64);
332         kstat_named_init(&esp->cc_unexp_pid,
333             "Invalid PID Errors", KSTAT_DATA_UINT64);
334         kstat_named_init(&esp->cc_data_omitted,
335             "Data Omissions", KSTAT_DATA_UINT64);
336         kstat_named_init(&esp->cc_data_underrun,
337             "Data Underruns", KSTAT_DATA_UINT64);
338         kstat_named_init(&esp->cc_buffer_omitted,
339             "Buffer Omissions", KSTAT_DATA_UINT64);
340         kstat_named_init(&esp->cc_buffer_underrun,
341             "Buffer Underruns", KSTAT_DATA_UINT64);
342         kstat_named_init(&esp->cc_timeout,
343             "Command Timed Out", KSTAT_DATA_UINT64);
344         kstat_named_init(&esp->cc_not_accessed,
345             "Not Accessed By Hardware", KSTAT_DATA_UINT64);
346         kstat_named_init(&esp->cc_no_resources,
347             "No Resources", KSTAT_DATA_UINT64);
348     #endif /* ! codereview */
349     kstat_named_init(&esp->cc_unspecified_err,
350         "Unspecified Error", KSTAT_DATA_UINT64);
351     kstat_named_init(&esp->cc_stopped_polling,
352         "Stopped Polling", KSTAT_DATA_UINT64);
353     kstat_named_init(&esp->cc_pipe_closing,
354         "Pipe Closing", KSTAT_DATA_UINT64);
355     kstat_named_init(&esp->cc_pipe_reset,
356         "Pipe Reset", KSTAT_DATA_UINT64);
357     kstat_named_init(&esp->cc_not_supported,
358         "Command Not Supported", KSTAT_DATA_UINT64);
359     kstat_named_init(&esp->cc_flushed,
360         "Request Flushed", KSTAT_DATA_UINT64);
361 #endif /* ! codereview */
362 #ifndef NOTYETNEEDED
363     kstat_named_init(&esp->hcdi_usb_failure,
364         "USB Failure", KSTAT_DATA_UINT64);
365     kstat_named_init(&esp->hcdi_usb_no_resources,
366         "No Resources", KSTAT_DATA_UINT64);
367     kstat_named_init(&esp->hcdi_usb_no_bandwidth,
368         "No Bandwidth", KSTAT_DATA_UINT64);
369     kstat_named_init(&esp->hcdi_usb_pipe_reserved,
370         "Pipe Reserved", KSTAT_DATA_UINT64);
371     kstat_named_init(&esp->hcdi_usb_pipe_unshareable,
372         "Pipe Unshareable", KSTAT_DATA_UINT64);
373     kstat_named_init(&esp->hcdi_usb_not_supported,
374         "Function Not Supported", KSTAT_DATA_UINT64);
375     kstat_named_init(&esp->hcdi_usb_pipe_error,
376         "Pipe Error", KSTAT_DATA_UINT64);
377     kstat_named_init(&esp->hcdi_usb_pipe_busy,
378         "Pipe Busy", KSTAT_DATA_UINT64);
379 #endif

381     HCDI_ERROR_STATS(hcdi)->ks_private = hcdi;
382     HCDI_ERROR_STATS(hcdi)->ks_update = nulldev;
383     kstat_install(HCDI_ERROR_STATS(hcdi));
384 }
385 }

388 /*
389  * Do actual error stats

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390 */
391 void
392 usba_hcdi_update_error_stats(usba_hcdi_t *hcdi, usb_cr_t completion_reason)
393 {
394     if (HCDI_ERROR_STATS(hcdi) == NULL) {
395
396         return;
397     }
398
399     switch (completion_reason) {
400     case USB_CR_OK:
401         break;
402     case USB_CR_CRC:
403         HCDI_ERROR_STATS_DATA(hcdi)->cc_crc.value.ui64++;
404         break;
405     case USB_CR_BITSTUFFING:
406         HCDI_ERROR_STATS_DATA(hcdi)->cc_bitstuffing.value.ui64++;
407         break;
408     case USB_CR_DATA_TOGGLE_MM:
409         HCDI_ERROR_STATS_DATA(hcdi)->cc_data_toggle_mm.value.ui64++;
410         break;
411     case USB_CR_STALL:
412         HCDI_ERROR_STATS_DATA(hcdi)->cc_stall.value.ui64++;
413         break;
414     case USB_CR_DEV_NOT_RESP:
415         HCDI_ERROR_STATS_DATA(hcdi)->cc_dev_not_resp.value.ui64++;
416         break;
417     case USB_CR_PID_CHECKFAILURE:
418         HCDI_ERROR_STATS_DATA(hcdi)->cc_pid_checkfailure.value.ui64++;
419         break;
420     case USB_CR_UNEXP_PID:
421         HCDI_ERROR_STATS_DATA(hcdi)->cc_unexp_pid.value.ui64++;
422         break;
423     case USB_CR_DATA_OVERRUN:
424         HCDI_ERROR_STATS_DATA(hcdi)->cc_data_overrun.value.ui64++;
425         break;
426     case USB_CR_DATA_UNDERRUN:
427         HCDI_ERROR_STATS_DATA(hcdi)->cc_data_underrun.value.ui64++;
428         break;
429     case USB_CR_BUFFER_OVERRUN:
430         HCDI_ERROR_STATS_DATA(hcdi)->cc_buffer_overrun.value.ui64++;
431         break;
432     case USB_CR_BUFFER_UNDERRUN:
433         HCDI_ERROR_STATS_DATA(hcdi)->cc_buffer_underrun.value.ui64++;
434         break;
435     case USB_CR_TIMEOUT:
436         HCDI_ERROR_STATS_DATA(hcdi)->cc_timeout.value.ui64++;
437         break;
438     case USB_CR_NOT_ACCESSED:
439         HCDI_ERROR_STATS_DATA(hcdi)->cc_not_accessed.value.ui64++;
440         break;
441     case USB_CR_NO_RESOURCES:
442         HCDI_ERROR_STATS_DATA(hcdi)->cc_no_resources.value.ui64++;
443         break;
444     case USB_CR_UNSPECIFIED_ERR:
445         HCDI_ERROR_STATS_DATA(hcdi)->cc_unspecified_err.value.ui64++;
446         break;
447     case USB_CR_STOPPED_POLLING:
448         HCDI_ERROR_STATS_DATA(hcdi)->cc_stopped_polling.value.ui64++;
449         break;
450     case USB_CR_PIPE_CLOSING:
451         HCDI_ERROR_STATS_DATA(hcdi)->cc_pipe_closing.value.ui64++;
452         break;
453     case USB_CR_PIPE_RESET:
454         HCDI_ERROR_STATS_DATA(hcdi)->cc_pipe_reset.value.ui64++;
455         break;

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456     case USB_CR_NOT_SUPPORTED:
457         HCDI_ERROR_STATS_DATA(hcdi)->cc_not_supported.value.ui64++;
458         break;
459     case USB_CR_FLUSHED:
460         HCDI_ERROR_STATS_DATA(hcdi)->cc_flushed.value.ui64++;
461         break;
462     default:
463         break;
464     }
465 }

466 /*
467  * Destroy the hotplug kstats structure
468  */
469 static void
470 usba_hcdi_destroy_stats(usba_hcdi_t *hcdi)
471 {
472     if (HCDI_HOTPLUG_STATS(hcdi)) {
473         kstat_delete(HCDI_HOTPLUG_STATS(hcdi));
474         HCDI_HOTPLUG_STATS(hcdi) = NULL;
475     }
476
477     if (HCDI_ERROR_STATS(hcdi)) {
478         kstat_delete(HCDI_ERROR_STATS(hcdi));
479         HCDI_ERROR_STATS(hcdi) = NULL;
480     }
481 }

482 /*
483  * HCD callback handling
484  */
485 void
486 usba_hcdi_cb(usba_pipe_handle_data_t *ph_data,
487             usb_opaque_t req,
488             usb_cr_t completion_reason)
489 {
490     usba_device_t *usba_device = ph_data->p_usba_device;
491     usba_hcdi_t *hcdi = usba_hcdi_get_hcdi(
492         usba_device->usb_root_hub_dip);
493     usba_req_wrapper_t *req_wrp = USB_REQ2WRP(req);
494     usb_ep_descr_t *eptd = &ph_data->p_ep;

495     mutex_enter(&ph_data->p_mutex);

496     #ifdef DEBUG
497     mutex_enter(&ph_data->p_ph_impl->usba_ph_mutex);

498     USB_DPRINTF_L4(DPRINT_MASK_HCDI, hcdi->hcdi_log_handle,
499         "usba_hcdi_cb: "
500         "ph_data=0x%p req=0x%p state=%d ref=%d cnt=%d cr=%d",
501         (void *)ph_data, (void *)req, ph_data->p_ph_impl->usba_ph_state,
502         ph_data->p_ph_impl->usba_ph_ref_count, ph_data->p_req_count,
503         completion_reason);

504     mutex_exit(&ph_data->p_ph_impl->usba_ph_mutex);
505     #endif

506     /* Set the completion reason */
507     switch (eptd->bmAttributes & USB_EP_ATTR_MASK) {
508     case USB_EP_ATTR_CONTROL:
509         ((usb_ctrl_req_t *)req)->
510             ctrl_completion_reason = completion_reason;
511         break;

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522     case USB_EP_ATTR_BULK:
523         ((usb_bulk_req_t *)req)->
524             bulk_completion_reason = completion_reason;
525         break;
526     case USB_EP_ATTR_INTR:
527         ((usb_intr_req_t *)req)->
528             intr_completion_reason = completion_reason;
529         break;
530     case USB_EP_ATTR_ISOCH:
531         ((usb_isoc_req_t *)req)->
532             isoc_completion_reason = completion_reason;
533         break;
534 }
535
536 /*
537  * exception callbacks will still go thru a taskq thread
538  * but should occur after the soft interrupt callback
539  * By design of periodic pipes, polling will stop on any
540  * exception
541  */
542 if ((ph_data->p_spec_flag & USBA_PH_FLAG_USE_SOFT_INTR) &&
543     (completion_reason == USB_CR_OK)) {
544     ph_data->p_soft_intr++;
545     mutex_exit(&ph_data->p_mutex);
546
547     usba_add_to_list(&hcdi->hcdi_cb_queue, &req_wrp->wr_queue);
548
549     if (ddi_intr_trigger_softint(hcdi->hcdi_softint_hdl, NULL) !=
550         DDI_SUCCESS)
551         USB_DPRINTF_L2(DPRINT_MASK_HCDI, hcdi->hcdi_log_handle,
552             "usba_hcdi_cb: ddi_intr_trigger_softint failed");
553
554     return;
555 }
556
557 /*
558  * USBA_PH_FLAG_TQ_SHARE is for bulk and intr requests,
559  * USBA_PH_FLAG_USE_SOFT_INTR is only for isoch,
560  * so there are no conflicts.
561  */
562 if (ph_data->p_spec_flag & USBA_PH_FLAG_TQ_SHARE) {
563     int iface;
564
565     mutex_exit(&ph_data->p_mutex);
566     iface = usb_get_if_number(ph_data->p_dip);
567     if (iface < 0) {
568         /* we own the device, use the first taskq */
569         iface = 0;
570     }
571     if (taskq_dispatch(usba_device->usb_shared_taskq[iface],
572         hcdi_shared_cb_thread, req_wrp, TQ_NOSLEEP) ==
573         NULL) {
574         usba_req_exc_cb(req_wrp,
575             USB_CR_NO_RESOURCES, USB_CB_ASYNC_REQ_FAILED);
576     }
577
578     return;
579 }
580
581 /* Add the callback to the pipehandles callback list */
582 usba_add_to_list(&ph_data->p_cb_queue, &req_wrp->wr_queue);
583
584 /* only dispatch if there is no thread running */
585 if (ph_data->p_thread_id == 0) {
586     if (usba_async_ph_req(ph_data, hcdi_cb_thread,
587         ph_data, USB_FLAGS_NOSLEEP) != USB_SUCCESS) {

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588         USB_DPRINTF_L2(DPRINT_MASK_HCDI, hcdi->hcdi_log_handle,
589             "usba_hcdi_cb: taskq_dispatch failed");
590         if (usba_rm_from_list(&ph_data->p_cb_queue,
591             &req_wrp->wr_queue) == USB_SUCCESS) {
592             mutex_exit(&ph_data->p_mutex);
593             usba_req_exc_cb(req_wrp,
594                 USB_CR_NO_RESOURCES,
595                 USB_CB_ASYNC_REQ_FAILED);
596
597             return;
598         } else {
599             ph_data->p_thread_id = (kthread_t *)1;
600         }
601     }
602     mutex_exit(&ph_data->p_mutex);
603 }
604 }
605
606 /*
607  * thread to perform the callbacks
608  */
609 static void
610 hcdi_cb_thread(void *arg)
611 {
612     usba_pipe_handle_data_t *ph_data =
613         (usba_pipe_handle_data_t *)arg;
614     usba_ph_impl_t *ph_impl = ph_data->p_ph_impl;
615     usba_hcdi_t *hcdi = usba_hcdi_get_hcdi(ph_data->
616         p_usba_device->usb_root_hub_dip);
617     usba_req_wrapper_t *req_wrp;
618
619     mutex_enter(&ph_data->p_mutex);
620     ASSERT(ph_data->p_thread_id == (kthread_t *)1);
621     ph_data->p_thread_id = curthread;
622
623     /*
624      * hold the ph_data. we can't use usba_hold_ph_data() since
625      * it will return NULL if we are closing the pipe which would
626      * then leave all requests stuck in the cb_queue
627      */
628     mutex_enter(&ph_impl->usba_ph_mutex);
629     ph_impl->usba_ph_ref_count++;
630
631     USB_DPRINTF_L4(DPRINT_MASK_HCDI, hcdi->hcdi_log_handle,
632         "hcdi_cb_thread: ph_data=0x%p ref=%d", (void *)ph_data,
633         ph_impl->usba_ph_ref_count);
634
635     mutex_exit(&ph_impl->usba_ph_mutex);
636
637     /*
638      * wait till soft interrupt callbacks are taken care of
639      */
640     while (ph_data->p_soft_intr) {
641         mutex_exit(&ph_data->p_mutex);
642         delay(1);
643         mutex_enter(&ph_data->p_mutex);
644     }
645
646     while ((req_wrp = (usba_req_wrapper_t *)
647         usba_rm_first_pvt_from_list(&ph_data->p_cb_queue)) != NULL) {
648         hcdi_do_cb(ph_data, req_wrp, hcdi);
649     }
650
651     ph_data->p_thread_id = 0;
652     mutex_exit(&ph_data->p_mutex);

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655     USB_DPRINTF_L4(DPRINT_MASK_HCDI, hcdi->hcdi_log_handle,
656     "hcdi_cb_thread done: ph_data=0x%p", (void *)ph_data);

658     usba_release_ph_data(ph_impl);
659 }

662 static void
663 hcdi_do_cb(usba_pipe_handle_data_t *ph_data, usba_req_wrapper_t *req_wrp,
664     usba_hcdi_t *hcdi)
665 {
666     usb_cr_t      completion_reason;
667     usb_req_attr_t attrs = req_wrp->wr_attr;

669     switch (req_wrp->wr_ph_data->p_ep.bmAttributes &
670         USB_EP_ATTR_MASK) {
671     case USB_EP_ATTR_CONTROL:
672         completion_reason =
673             USB_WRP2CTRL_REQ(req_wrp)->ctrl_completion_reason;
674         break;
675     case USB_EP_ATTR_INTR:
676         completion_reason =
677             USB_WRP2INTR_REQ(req_wrp)->intr_completion_reason;
678         break;
679     case USB_EP_ATTR_BULK:
680         completion_reason =
681             USB_WRP2BULK_REQ(req_wrp)->bulk_completion_reason;
682         break;
683     case USB_EP_ATTR_ISOCH:
684         completion_reason =
685             USB_WRP2ISOC_REQ(req_wrp)->isoc_completion_reason;
686         break;
687     }
688     req_wrp->wr_cr = completion_reason;

690     USB_DPRINTF_L4(DPRINT_MASK_HCDI, hcdi->hcdi_log_handle,
691     "hcdi_do_cb: wrp=0x%p cr=0x%x", (void *)req_wrp, completion_reason);

693     /*
694     * Normal callbacks:
695     */
696     if (completion_reason == USB_CR_OK) {
697         mutex_exit(&ph_data->p_mutex);
698         usba_req_normal_cb(req_wrp);
699         mutex_enter(&ph_data->p_mutex);
700     } else {
701         usb_pipe_state_t pipe_state;

703         USB_DPRINTF_L4(DPRINT_MASK_HCDI, hcdi->hcdi_log_handle,
704         "exception callback handling: attrs=0x%x", attrs);

706         /*
707         * In exception callback handling, if we were
708         * not able to clear stall, we need to modify
709         * pipe state. Also if auto-clearing is not set
710         * pipe state needs to be modified.
711         */
712         pipe_state = usba_get_ph_state(ph_data);

714         if (!USB_PIPE_CLOSING(pipe_state)) {
715             switch (completion_reason) {
716             case USB_CR_STOPPED_POLLING:
717                 if (pipe_state ==
718                     USB_PIPE_STATE_ACTIVE) {
719                     usba_pipe_new_state(ph_data,

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720         USB_PIPE_STATE_IDLE);
721     }
722     break;
723     case USB_CR_NOT_SUPPORTED:
724         usba_pipe_new_state(ph_data,
725         USB_PIPE_STATE_IDLE);
726     break;
727     case USB_CR_PIPE_RESET:
728     case USB_CR_FLUSHED:
729         break;
730     default:
731         usba_pipe_new_state(ph_data,
732         USB_PIPE_STATE_ERROR);
733     break;
734 }
735 }

737     pipe_state = usba_get_ph_state(ph_data);

739     mutex_exit(&ph_data->p_mutex);
740     if (attrs & USB_ATTRS_PIPE_RESET) {
741         if ((completion_reason != USB_CR_PIPE_RESET) &&
742             (pipe_state == USB_PIPE_STATE_ERROR)) {
743             hcdi_autoclearing(req_wrp);
744         }
745     }
746 }

748     usba_req_exc_cb(req_wrp, 0, 0);
749     mutex_enter(&ph_data->p_mutex);
750 }

752     /* Update the hcdi error kstats */
753     if (completion_reason) {
754         mutex_enter(&hcdi->hcdi_mutex);
755         usba_hcdi_update_error_stats(hcdi, completion_reason);
756         mutex_exit(&hcdi->hcdi_mutex);
757     }

759     /*
760     * Once the callback is finished, release the pipe handle
761     * we start the next request first to avoid that the
762     * pipe gets closed while starting the next request
763     */
764     mutex_exit(&ph_data->p_mutex);
765     usba_start_next_req(ph_data);

767     mutex_enter(&ph_data->p_mutex);
768 }

771 /*
772 * thread to perform callbacks on the shared queue
773 */
774 static void
775 hcdi_shared_cb_thread(void *arg)
776 {
777     usba_req_wrapper_t *req_wrp = (usba_req_wrapper_t *)arg;
778     usba_pipe_handle_data_t *ph_data = req_wrp->wr_ph_data;
779     usba_ph_impl_t *ph_impl = ph_data->p_ph_impl;
780     usba_hcdi_t *hcdi = usba_hcdi_get_hcdi(ph_data->
781         p_usba_device->usb_root_hub_dip);
782     /*
783     * hold the ph_data. we can't use usba_hold_ph_data() since
784     * it will return NULL if we are closing the pipe which would
785     * then leave all requests stuck in the cb_queue

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848 /*
849  * hcdi_autoclearing:
850  *     This function is called under the taskq context. It
851  *     resets the pipe, and clears the stall, if necessary

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852 */
853 static void
854 hcdi_autoclearing(usba_req_wrapper_t *req_wrp)
855 {
856     usb_cr_t          cr = req_wrp->wr_cr;
857     usb_pipe_handle_t pipe_handle, def_pipe_handle;
858     usb_cr_t          completion_reason;
859     usb_cb_flags_t    cb_flags;
860     int               rval;
861     usba_device_t     *usba_device =
862         req_wrp->wr_ph_data->p_usba_device;
863     usba_hcdi_t       *hcdi = usba_hcdi_get_hcdi(
864         usba_device->usb_root_hub_dip);
865     usb_req_attrs_t    attrs = req_wrp->wr_attrs;
866
867     USB_DPRINTF_L4(DPRINT_MASK_HCDI, hcdi->hcdi_log_handle,
868         "hcdi_autoclearing: wrp=0x%p", (void *)req_wrp);
869
870     pipe_handle = usba_get_pipe_handle(req_wrp->wr_ph_data);
871     def_pipe_handle = usba_get_dflt_pipe_handle(req_wrp->wr_ph_data->p_dip);
872
873     /*
874      * first reset the pipe synchronously
875      */
876     if ((attrs & USB_ATTRS_PIPE_RESET) == USB_ATTRS_PIPE_RESET) {
877         usba_pipe_clear(pipe_handle);
878         usba_req_set_cb_flags(req_wrp, USB_CB_RESET_PIPE);
879     }
880
881     ASSERT(def_pipe_handle);
882
883     /* Do not clear if this request was a usb_get_status request */
884     if ((pipe_handle == def_pipe_handle) &&
885         (USBA_WRP2CTRL_REQ(req_wrp)->ctrl_bRequest ==
886          USB_REQ_GET_STATUS)) {
887         USB_DPRINTF_L2(DPRINT_MASK_USBAI, hcdi->hcdi_log_handle,
888             "hcdi_autoclearing: usb_get_status failed, no clearing");
889
890         /* if default pipe and stall no auto clearing */
891     } else if ((pipe_handle == def_pipe_handle) && (cr == USB_CR_STALL)) {
892         USB_DPRINTF_L2(DPRINT_MASK_USBAI, hcdi->hcdi_log_handle,
893             "hcdi_autoclearing: default pipe stalled, no clearing");
894
895         usba_req_set_cb_flags(req_wrp, USB_CB_PROTOCOL_STALL);
896
897         /* else do auto clearing */
898     } else if (((attrs & USB_ATTRS_AUTOCLEARING) ==
899         USB_ATTRS_AUTOCLEARING) && (cr == USB_CR_STALL)) {
900         ushort_t status = 0;
901
902         rval = usb_get_status(req_wrp->wr_dip, def_pipe_handle,
903             USB_DEV_REQ_DEV_TO_HOST | USB_DEV_REQ_RCPT_EP,
904             req_wrp->wr_ph_data->p_ep.bEndpointAddress,
905             &status, USB_FLAGS_SLEEP);
906         if (rval != USB_SUCCESS) {
907             USB_DPRINTF_L2(DPRINT_MASK_USBAI, hcdi->hcdi_log_handle,
908                 "get status (STALL) failed: rval=%d", rval);
909
910             usba_pipe_clear(def_pipe_handle);
911         }
912
913         if ((rval != USB_SUCCESS) ||
914             (status & USB_EP_HALT_STATUS)) {
915             usba_req_set_cb_flags(req_wrp, USB_CB_FUNCTIONAL_STALL);
916
917             if ((rval = usb_pipe_sync_ctrl_xfer(

```

```

918         req_wrp->wr_dip, def_pipe_handle,
919         USB_DEV_REQ_HOST_TO_DEV |
920         USB_DEV_REQ_RCPT_EP,
921         USB_REQ_CLEAR_FEATURE,
922         0,
923         req_wrp->wr_ph_data->p_ep.bEndpointAddress,
924         0,
925         NULL, 0,
926         &completion_reason,
927         &cb_flags, USB_FLAGS_SLEEP)) != USB_SUCCESS) {
928         USB_DPRINTF_L2(DPRINT_MASK_USBAI,
929         hcdi->hcdi_log_handle,
930         "auto clearing (STALL) failed: "
931         "rval=%d, cr=0x%x cb=0x%x",
932         rval, completion_reason, cb_flags);

934         usba_pipe_clear(def_pipe_handle);
935     } else {
936         usba_req_set_cb_flags(req_wrp,
937         USB_CB_STALL_CLEARED);
938     }
939 } else {
940     usba_req_set_cb_flags(req_wrp, USB_CB_PROTOCOL_STALL);
941 }
942 }
943 }

946 /*
947 * usba_hcdi_get_req_private:
948 * This function is used to get the HCD private field
949 * maintained by USBA. HCD calls this function.
950 *
951 * Arguments:
952 *     req          - pointer to usb_*_req_t
953 *
954 * Return Values:
955 *     wr_hcd_private field from wrapper
956 */
957 usb_opaque_t
958 usba_hcdi_get_req_private(usb_opaque_t req)
959 {
960     usba_req_wrapper_t *wrp = USBA_REQ2WRP(req);

962     return (wrp->wr_hcd_private);
963 }

966 /*
967 * usba_hcdi_set_req_private:
968 * This function is used to set the HCD private field
969 * maintained by USBA. HCD calls this function.
970 *
971 * Arguments:
972 *     req          - pointer to usb_*_req_t
973 *     hcd_private  - wr_hcd_private field from wrapper
974 */
975 void
976 usba_hcdi_set_req_private(usb_opaque_t req,
977         usb_opaque_t hcd_private)
978 {
979     usba_req_wrapper_t *wrp = USBA_REQ2WRP(req);

981     wrp->wr_hcd_private = hcd_private;
982 }

```

```

985 /* get data toggle information for this endpoint */
986 uchar_t
987 usba_hcdi_get_data_toggle(usba_device_t *usba_device, uint8_t ep_addr)
988 {
989     uchar_t toggle;
990     usba_ph_impl_t *ph_impl;
991     int ep_index;

993     ep_index = usb_get_ep_index(ep_addr);
994     mutex_enter(&usba_device->usb_mutex);
995     ph_impl = &usba_device->usb_ph_list[ep_index];
996     mutex_enter(&ph_impl->usba_ph_mutex);
997     toggle = (uchar_t)(ph_impl->usba_ph_flags & USBA_PH_DATA_TOGGLE);
998     mutex_exit(&ph_impl->usba_ph_mutex);
999     mutex_exit(&usba_device->usb_mutex);

1001     return (toggle);
1002 }

1005 /* set data toggle information for this endpoint */
1006 void
1007 usba_hcdi_set_data_toggle(usba_device_t *usba_device, uint8_t ep_addr,
1008         uchar_t toggle)
1009 {
1010     usba_ph_impl_t *ph_impl;
1011     int ep_index;

1013     ep_index = usb_get_ep_index(ep_addr);
1014     mutex_enter(&usba_device->usb_mutex);
1015     ph_impl = &usba_device->usb_ph_list[ep_index];
1016     mutex_enter(&ph_impl->usba_ph_mutex);
1017     ph_impl->usba_ph_flags &= ~USBA_PH_DATA_TOGGLE;
1018     ph_impl->usba_ph_flags |= (USBA_PH_DATA_TOGGLE & toggle);
1019     mutex_exit(&ph_impl->usba_ph_mutex);
1020     mutex_exit(&usba_device->usb_mutex);
1021 }

1024 /* get pipe_handle_impl ptr for this ep */
1025 usba_pipe_handle_data_t *
1026 usba_hcdi_get_ph_data(usba_device_t *usba_device, uint8_t ep_addr)
1027 {
1028     return (usba_device->usb_ph_list[usb_get_ep_index(ep_addr)].
1029         usba_ph_data);
1030 }

```