new/usr/src/uts/common/sys/ib/clients/of/rdma/rdma_cm.h                    1

**********************************************************
   12155 Fri Aug  8 18:40:37 2014
new/usr/src/uts/common/sys/ib/clients/of/rdma/rdma_cm.h
5072 all rdma cm events should be in the enum
**********************************************************
     1  /*
     2   * This file contains definitions used in OFED defined user/kernel
     3   * interfaces. These are imported from the OFED header rdma_cm.h. Oracle
     4   * elects to have and use the contents of rdma_cm.h under and governed
     5   * by the OpenIB.org BSD license (see below for details). However,
     6   * the following notice accompanied the original version of this file:
     7   */

     9  /*
    10   * Copyright (c) 2005 Voltaire Inc.  All rights reserved.
    11   * Copyright (c) 2005 Intel Corporation.  All rights reserved.
    12   *
    13   * This Software is licensed under one of the following licenses:
    14   *
    15   * 1) under the terms of the "Common Public License 1.0" a copy of which is
    16   *    available from the Open Source Initiative, see
    17   *    http://www.opensource.org/licenses/cpl.php.
    18   *
    19   * 2) under the terms of the "The BSD License" a copy of which is
    20   *    available from the Open Source Initiative, see
    21   *    http://www.opensource.org/licenses/bsd-license.php.
    22   *
    23   * 3) under the terms of the "GNU General Public License (GPL) Version 2" a
    24   *    copy of which is available from the Open Source Initiative, see
    25   *    http://www.opensource.org/licenses/gpl-license.php.
    26   *
    27   * Licensee has the right to choose one of the above licenses.
    28   *
    29   * Redistributions of source code must retain the above copyright
    30   * notice and one of the license notices.
    31   *
    32   * Redistributions in binary form must reproduce both the above copyright
    33   * notice, one of the license notices in the documentation
    34   * and/or other materials provided with the distribution.
    35   *
    36   */

    38  #ifndef _SYS_IB_CLIENTS_OF_RDMA_RDMA_CM_H
    39  #define _SYS_IB_CLIENTS_OF_RDMA_RDMA_CM_H

    41  #ifdef __cplusplus
    42  extern "C" {
    43  #endif

    45  #include <sys/socket.h>

    47  /*
    48   * Upon receiving a device removal event, users must destroy the associated
    49   * RDMA identifier and release all resources allocated with the device.
    50   */
    51  enum rdma_cm_event_type {
    52          RDMA_CM_EVENT_ADDR_RESOLVED,
    53          RDMA_CM_EVENT_ADDR_ERROR,
    54          RDMA_CM_EVENT_ROUTE_RESOLVED,
    55          RDMA_CM_EVENT_ROUTE_ERROR,
    56          RDMA_CM_EVENT_CONNECT_REQUEST,
    57          RDMA_CM_EVENT_CONNECT_RESPONSE,
    58          RDMA_CM_EVENT_CONNECT_ERROR,
    59          RDMA_CM_EVENT_UNREACHABLE,
    60          RDMA_CM_EVENT_REJECTED,
    61          RDMA_CM_EVENT_ESTABLISHED,

new/usr/src/uts/common/sys/ib/clients/of/rdma/rdma_cm.h                    2

    62          RDMA_CM_EVENT_DISCONNECTED,
    63          RDMA_CM_EVENT_DEVICE_REMOVAL,
    64          RDMA_CM_EVENT_MULTICAST_JOIN,
    65          **RDMA_CM_EVENT_MULTICAST_ERROR,**
    66          **RDMA_CM_EVENT_ADDR_CHANGE**
    65          *RDMA_CM_EVENT_MULTICAST_ERROR*
    67  };
_____unchanged_portion_omitted_

**********************************************************
    12047 Fri Aug  8 18:40:38 2014
**new/usr/src/uts/common/sys/ib/clients/rdsv3/rdsv3_impl.h**
**5072 all rdma cm events should be in the enum**
**********************************************************
```
   1 /*
   2  * CDDL HEADER START
   3  *
   4  * The contents of this file are subject to the terms of the
   5  * Common Development and Distribution License (the "License").
   6  * You may not use this file except in compliance with the License.
   7  *
   8  * You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
   9  * or http://www.opensolaris.org/os/licensing.
  10  * See the License for the specific language governing permissions
  11  * and limitations under the License.
  12  *
  13  * When distributing Covered Code, include this CDDL HEADER in each
  14  * file and include the License file at usr/src/OPENSOLARIS.LICENSE.
  15  * If applicable, add the following below this CDDL HEADER, with the
  16  * fields enclosed by brackets "[]" replaced with your own identifying
  17  * information: Portions Copyright [yyyy] [name of copyright owner]
  18  *
  19  * CDDL HEADER END
  20  */
  21 /*
  22  * Copyright (c) 2010, Oracle and/or its affiliates. All rights reserved.
  23  * Copyright 2012 Nexenta Systems, Inc.  All rights reserved.
  24  */

  26 #ifndef _RDSV3_IMPL_H
  27 #define _RDSV3_IMPL_H

  29 #include <sys/atomic.h>

  31 /*
  32  * This file is only present in Solaris
  33  */

  35 #ifdef __cplusplus
  36 extern "C" {
  37 #endif

  39 extern dev_info_t        *rdsv3_dev_info;

  41 #define uint16_be_t     uint16_t
  42 #define uint32_be_t     uint32_t
  43 #define uint64_be_t     uint64_t

  45 /*
  46  * RDS Well known service id
  47  * Format: 0x1h00144Fhhhhhhhh
  48  *         "00144F" is the Sun OUI
  49  * 'h' can be any hex-decimal digit.
  50  */
  51 #define RDS_SERVICE_ID            0x1000144F00000001ULL

  53 /*
  54  * Atomic operations
  55  */
  56 typedef unsigned int    atomic_t;
  57 #define ATOMIC_INIT(a)  a

  59 #define atomic_get(p)   (*(p))

  61 #define atomic_cmpset_long(p, c, n) \
```

```
  62          ((c == atomic_cas_uint(p, c, n)) ? c : -1)

  64 #define atomic_dec_and_test(a)                  \
  65          (atomic_dec_uint_nv((a)) == 0)

  67 #define atomic_cmpxchg(a, o, n)                 \
  68          atomic_cas_uint(a, o, n)

  70 #ifdef _LP64
  71 #define set_bit(b, p) \
  72          atomic_or_ulong(((volatile ulong_t *)(void *)(p)) + ((b) >> 6), \
  73          1ul << ((b) & 0x3f))

  75 #define clear_bit(b, p) \
  76          atomic_and_ulong(((volatile ulong_t *)(void *)(p)) + ((b) >> 6), \
  77          ~(1ul << ((b) & 0x3f)))

  79 #define test_bit(b, p) \
  80          (((volatile ulong_t *)(void *)(p))[(b) >> 6] & (1ul << ((b) & 0x3f)))

  82 #define test_and_set_bit(b, p) \
  83          atomic_set_long_excl(((ulong_t *)(void *)(p)) +         \
  84              ((b) >> 6), ((b) & 0x3f))
  85 #define test_and_clear_bit(b, p) \
  86          !atomic_clear_long_excl(((ulong_t *)(void *)(p)) + ((b) >> 6), \
  87          ((b) & 0x3f))
  88 #else
  89 #define set_bit(b, p) \
  90          atomic_or_uint(((volatile uint_t *)(void *)p) + (b >> 5), \
  91          1ul << (b & 0x1f))

  93 #define clear_bit(b, p) \
  94          atomic_and_uint(((volatile uint_t *)(void *)p) + (b >> 5), \
  95          ~(1ul << (b & 0x1f)))

  97 #define test_bit(b, p) \
  98          (((volatile uint_t *)(void *)p)[b >> 5] & (1ul << (b & 0x1f)))

 100 #define test_and_set_bit(b, p) \
 101          atomic_set_long_excl(((ulong_t *)(void *)p) + (b >> 5), (b & 0x1f))
 102 #define test_and_clear_bit(b, p) \
 103          !atomic_clear_long_excl(((ulong_t *)(void *)p) + (b >> 5), (b & 0x1f))
 104 #endif

 106 /*
 107  * These macros and/or constants are used instead of Linux
 108  * generic_{test,__{clear,set}}_le_bit().
 109  */
 110 #if defined(sparc)
 111 #define LE_BIT_XOR       ((BITS_PER_LONG-1) & ~0x7)
 112 #else
 113 #define LE_BIT_XOR       0
 114 #endif

 116 #define set_le_bit(b, p)        set_bit(b ^ LE_BIT_XOR, p)
 117 #define clear_le_bit(b, p)      clear_bit(b ^ LE_BIT_XOR, p)
 118 #define test_le_bit(b, p)       test_bit(b ^ LE_BIT_XOR, p)

 120 uint_t  rdsv3_one_sec_in_hz;

 122 #define jiffies 100
 123 #define HZ      (drv_hztousec(1))
 124 /* setting this to PAGESIZE throws build errors */
 125 #define PAGE_SIZE       4096 /* xxx - fix this */
 126 #define BITS_PER_LONG   (sizeof (unsigned long) * 8)
```

```
128 /* debug */
129 #define RDSV3_PANIC()            cmn_err(CE_PANIC, "Panic forced by RDSV3");

131 /* ERR */
132 #define MAX_ERRNO        4095
133 #define ERR_PTR(x)       ((void *)(uintptr_t)x)
134 #define IS_ERR(ptr)      (((uintptr_t)ptr) >= (uintptr_t)-MAX_ERRNO)
135 #define PTR_ERR(ptr)     (int)(uintptr_t)ptr

137 #define MAX_SCHEDULE_TIMEOUT    (~0UL>>1)

139 #define RDMA_CM_EVENT_ADDR_CHANGE        14

139 /* list */
140 /* copied and modified list_remove_node */
141 #define list_remove_node(node)                                       \
142         if ((node)->list_next != NULL) {                             \
143                 (node)->list_prev->list_next = (node)->list_next;    \
144                 (node)->list_next->list_prev = (node)->list_prev;    \
145                 (node)->list_next = (node)->list_prev = NULL;        \
146         }

148 #define list_splice(src, dst)    {                                   \
149         list_create(dst, (src)->list_size, (src)->list_offset);      \
150         list_move_tail(dst, src);                                    \
151         }

153 #define RDSV3_FOR_EACH_LIST_NODE(objp, listp, member)    \
154         for (objp = list_head(listp); objp; objp = list_next(listp, objp))
155 #define RDSV3_FOR_EACH_LIST_NODE_SAFE(objp, tmp, listp, member) \
156         for (objp = list_head(listp), tmp = (objp != NULL) ?     \
157                 list_next(listp, objp) : NULL;                   \
158                 objp;                                            \
159                 objp = tmp, tmp = (objp != NULL) ?               \
160                 list_next(listp, objp) : NULL)

162 /* simulate wait_queue_head_t */
163 typedef struct rdsv3_wait_queue_s {
164         kmutex_t        waitq_mutex;
165         kcondvar_t      waitq_cv;
166         uint_t          waitq_waiters;
167 } rdsv3_wait_queue_t;
_____unchanged_portion_omitted_
```