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*****
54095 Wed Jun  5 03:13:14 2019
new/usr/src/tools/cpcgen/cpcgen.c
11200 cpcgen needs smatch fixes again
*****
1 /*
2  * This file and its contents are supplied under the terms of the
3  * Common Development and Distribution License ("CDDL"), version 1.0.
4  * You may only use this file in accordance with the terms of version
5  * 1.0 of the CDDL.
6  *
7  * A full copy of the text of the CDDL should have accompanied this
8  * source.  A copy of the CDDL is also available via the Internet at
9  * http://www.illumos.org/license/CDDL.
10 */

12 /*
13  * Copyright 2019, Joyent, Inc.
14  * Copyright (c) 2019, Joyent, Inc.
15 */

16 /*
17  * This program transforms Intel perfmon and AMD PMC data files into C files and
18  * manual pages.
19  */

21 #include <stdio.h>
22 #include <stdarg.h>
23 #include <unistd.h>
24 #include <err.h>
25 #include <libgen.h>
26 #include <libnvpair.h>
27 #include <strings.h>
28 #include <errno.h>
29 #include <limits.h>
30 #include <sys/mman.h>
31 #include <sys/param.h>
32 #include <assert.h>
33 #include <ctype.h>
34 #include <sys/types.h>
35 #include <sys/stat.h>
36 #include <fcntl.h>
37 #include <dirent.h>

39 #include <json_nvlist.h>

41 #define EXIT_USAGE      2
42 #define CPROC_MAX_STEPPINGS    16

44 typedef enum {
45     CPCGEN_MODE_UNKNOWN = 0,
46     CPCGEN_MODE_INTEL,
47     CPCGEN_MODE_AMD
48 } cpc_mode_t;
unchanged_portion omitted

389 /*
390  * Parse a string of the form 'GenuineIntel-6-2E' and get out the family and
391  * model.
392  */
393 static void
394 cpcgen_parse_model(char *fsr, uint_t *family, uint_t *model, uint_t *nstepp,
395     uint_t *steppings)
396 {
397     const char *bstr = "GenuineIntel";
398     const char *brand, *fam, *mod, *step;

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399     char *last;
400     long l;
401     uint_t nstep = 0;

403     /*
404      * Tokenize the string. There may be an optional stepping portion,
405      * which has a range of steppings enclosed by '[' and ']' characters.
406      * While the other parts are required, the stepping may be missing.
407      */
408     if ((brand = strtok_r(fsr, "-", &last)) == NULL ||
409         (fam = strtok_r(NULL, "-", &last)) == NULL ||
410         (mod = strtok_r(NULL, "-", &last)) == NULL) {
411         errx(EXIT_FAILURE, "failed to parse processor id \"%s\"", fsr);
412     }
413     step = strtok_r(NULL, "-", &last);

415     if (strcmp(bstr, brand) != 0) {
416         errx(EXIT_FAILURE, "brand string \"%s\" did not match \"%s\"",
417             brand, bstr);
418     }

420     errno = 0;
421     l = strtol(fam, &last, 16);
422     if (errno != 0 || l < 0 || l >= INT_MAX || *last != '\0') {
423         errx(EXIT_FAILURE, "failed to parse family \"%s\"", fam);
424     }
425     *family = (uint_t)l;

427     l = strtol(mod, &last, 16);
428     if (errno != 0 || l < 0 || l >= INT_MAX || *last != '\0') {
429         errx(EXIT_FAILURE, "failed to parse model \"%s\"", mod);
430     }
431     *model = (uint_t)l;

433     if (step == NULL) {
434         *nstepp = 0;
435         return;
436     }

438     if (*step != '[' || ((last = strchr(step, ']')) == NULL)) {
439         errx(EXIT_FAILURE, "failed to parse stepping \"%s\": missing "
440             "stepping range brackets", step);
441     }
442     step++;
443     *last = '\0';
444     while (*step != '\0') {
445         if (!isxdigit(*step)) {
446             errx(EXIT_FAILURE, "failed to parse stepping: invalid "
447                 "stepping identifier '0x%x'", *step);
448         }

450         if (nstep >= CPROC_MAX_STEPPINGS) {
451             errx(EXIT_FAILURE, "failed to parse stepping: "
452                 "encountered too many steppings");
453         }

455         switch (*step) {
456             case '0':
457                 steppings[nstep] = 0x0;
458                 break;
459             case '1':
460                 steppings[nstep] = 0x1;
461                 break;
462             case '2':

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463     steppings[nstep] = 0x2;
464     break;
465     case '3':
466         steppings[nstep] = 0x3;
467         break;
468     case '4':
469         steppings[nstep] = 0x4;
470         break;
471     case '5':
472         steppings[nstep] = 0x5;
473         break;
474     case '6':
475         steppings[nstep] = 0x6;
476         break;
477     case '7':
478         steppings[nstep] = 0x7;
479         break;
480     case '8':
481         steppings[nstep] = 0x8;
482         break;
483     case '9':
484         steppings[nstep] = 0x9;
485         break;
486     case 'a':
487     case 'A':
488         steppings[nstep] = 0xa;
489         break;
490     case 'b':
491     case 'B':
492         steppings[nstep] = 0xb;
493         break;
494     case 'c':
495     case 'C':
496         steppings[nstep] = 0xc;
497         break;
498     case 'd':
499     case 'D':
500         steppings[nstep] = 0xd;
501         break;
502     case 'e':
503     case 'E':
504         steppings[nstep] = 0xe;
505         break;
506     case 'f':
507     case 'F':
508         steppings[nstep] = 0xf;
509         break;
510     default:
511         errx(EXIT_FAILURE, "encountered non-hex stepping "
512             "character: '%c'", *step);
513     }
514     nstep++;
515     step++;
516 }

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518     *nstepp = nstep;
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519 }
unchanged_portion_omitted
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839 static boolean_t
840 cpcgen_manual_intel_file_before(FILE *f, cpc_map_t *map)
841 {
842     size_t i;
843     char *upper;
844     cpc_proc_t *proc;

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846     if ((upper = strdup(map->cmap_name)) == NULL) {
847         warn("failed to duplicate manual name for %s", map->cmap_name);
848         return (B_FALSE);
849     }

851     for (i = 0; upper[i] != '\0'; i++) {
852         upper[i] = toupper(upper[i]);
853     }

855     if (fprintf(f, cpcgen_manual_intel_intel_header, map->cmap_path, upper,
856         map->cmap_name) == -1) {
857         warn("failed to write out manual header for %s",
858             map->cmap_name);
859         free(upper);
860         return (B_FALSE);
861     }
862     free(upper);

864     for (proc = map->cmap_procs; proc != NULL; proc = proc->cproc_next) {
865         if (proc->cproc_nsteps > 0) {
866             uint_t step;

868             for (step = 0; step < proc->cproc_nsteps; step++) {
869                 if (fprintf(f, ".It\n.Sy Family 0x%x, Model "
870                     "0x%x, Stepping 0x%x\n",
871                     proc->cproc_family, proc->cproc_model,
872                     proc->cproc_steppings[step]) == -1) {
873                     warn("failed to write out model "
874                         "information for %s",
875                         map->cmap_name);
876                     return (B_FALSE);
877                 }
878             }
879         } else {
880             if (fprintf(f, ".It\n.Sy Family 0x%x, Model 0x%x\n",
881                 proc->cproc_family, proc->cproc_model) == -1) {
882                 warn("failed to write out model information "
883                     "for %s", map->cmap_name);
884                 return (B_FALSE);
885             }
886         }
887     }

889     if (fprintf(f, cpcgen_manual_intel_data) == -1) {
890         warn("failed to write out manual header for %s",
891             map->cmap_name);
892         return (B_FALSE);
893     }

895     free(upper);
895     return (B_TRUE);
896 }
unchanged_portion_omitted

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