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*****
25655 Mon Jan 14 13:17:17 2019
new/usr/src/uts/common/io/ixgbe/ixgbe_gld.c
10086 ixgbe_m_propinfo() is missing an 'else'
*****
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32 */

33 #include "ixgbe_sw.h"

35 /*
36  * Bring the device out of the reset/quiesced state that it
37  * was in when the interface was registered.
38  */
39 int
40 ixgbe_m_start(void *arg)
41 {
42     ixgbe_t *ixgbe = (ixgbe_t *)arg;

44     mutex_enter(&ixgbe->gen_lock);

46     if (ixgbe->ixgbe_state & IXGBE_SUSPENDED) {
47         mutex_exit(&ixgbe->gen_lock);
48         return (ECANCELED);
49     }

51     if (ixgbe_start(ixgbe, B_TRUE) != IXGBE_SUCCESS) {
52         mutex_exit(&ixgbe->gen_lock);
53         return (EIO);
54     }

56     atomic_or_32(&ixgbe->ixgbe_state, IXGBE_STARTED);

58     mutex_exit(&ixgbe->gen_lock);

60     /*

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61     * Enable and start the watchdog timer
62     */
63     ixgbe_enable_watchdog_timer(ixgbe);

65     return (0);
66 }
unchanged_portion_omitted

678 void
679 ixgbe_m_propinfo(void *arg, const char *pr_name, mac_prop_id_t pr_num,
680                 mac_prop_info_handle_t prh)
681 {
682     ixgbe_t *ixgbe = (ixgbe_t *)arg;
683     struct ixgbe_hw *hw = &ixgbe->hw;
684     uint_t perm;
685     uint8_t value;
686     ixgbe_link_speed speeds = 0;

688     /*
689      * We cannot always rely on the common code maintaining
690      * hw->phy.speeds_supported, therefore we fall back to use the
691      * recorded supported speeds which were obtained during instance init in
692      * ixgbe_init_params().
693      */
694     speeds = hw->phy.speeds_supported;
695     if (speeds == 0)
696         speeds = ixgbe->speeds_supported;

698     switch (pr_num) {
699     case MAC_PROP_DUPLEX:
700     case MAC_PROP_SPEED:
701         mac_prop_info_set_perm(prh, MAC_PROP_PERM_READ);
702         break;

704     case MAC_PROP_ADV_100FDX_CAP:
705         mac_prop_info_set_perm(prh, MAC_PROP_PERM_READ);
706         value = (speeds & IXGBE_LINK_SPEED_100_FULL) ? 1 : 0;
707         mac_prop_info_set_default_uint8(prh, value);
708         break;

710     case MAC_PROP_ADV_1000FDX_CAP:
711         mac_prop_info_set_perm(prh, MAC_PROP_PERM_READ);
712         value = (speeds & IXGBE_LINK_SPEED_1GB_FULL) ? 1 : 0;
713         mac_prop_info_set_default_uint8(prh, value);
714         break;

716     case MAC_PROP_ADV_2500FDX_CAP:
717         mac_prop_info_set_perm(prh, MAC_PROP_PERM_READ);
718         value = (speeds & IXGBE_LINK_SPEED_2_5GB_FULL) ? 1 : 0;
719         mac_prop_info_set_default_uint8(prh, value);
720         break;

722     case MAC_PROP_ADV_5000FDX_CAP:
723         mac_prop_info_set_perm(prh, MAC_PROP_PERM_READ);
724         value = (speeds & IXGBE_LINK_SPEED_5GB_FULL) ? 1 : 0;
725         mac_prop_info_set_default_uint8(prh, value);
726         break;

728     case MAC_PROP_ADV_10GFDX_CAP:
729         mac_prop_info_set_perm(prh, MAC_PROP_PERM_READ);
730         value = (speeds & IXGBE_LINK_SPEED_10GB_FULL) ? 1 : 0;
731         mac_prop_info_set_default_uint8(prh, value);
732         break;

734     /*
735      * We allow speed changes only on baseT PHYs. MAC_PROP_EN_* are marked

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736     * read-only on non-baseT (SFP) PHYs.
737     */
738 case MAC_PROP_AUTONEG:
739     perm = (hw->phy.media_type == ixgbe_media_type_copper) ?
740         MAC_PROP_PERM_RW : MAC_PROP_PERM_READ;
741     mac_prop_info_set_perm(prh, perm);
742     mac_prop_info_set_default_uint8(prh, 1);
743     break;

745 case MAC_PROP_EN_10GFDX_CAP:
746     if (speeds & IXGBE_LINK_SPEED_10GB_FULL) {
747         perm = (hw->phy.media_type == ixgbe_media_type_copper) ?
748             MAC_PROP_PERM_RW : MAC_PROP_PERM_READ;
749         mac_prop_info_set_perm(prh, perm);
750         mac_prop_info_set_default_uint8(prh, 1);
751     }
752     break;

754 case MAC_PROP_EN_5000FDX_CAP:
755     if (speeds & IXGBE_LINK_SPEED_5GB_FULL) {
756         perm = (hw->phy.media_type == ixgbe_media_type_copper) ?
757             MAC_PROP_PERM_RW : MAC_PROP_PERM_READ;
758         mac_prop_info_set_perm(prh, perm);
759         mac_prop_info_set_default_uint8(prh, 1);
760     }
761     break;

763 case MAC_PROP_EN_2500FDX_CAP:
764     if (speeds & IXGBE_LINK_SPEED_2_5GB_FULL) {
765         perm = (hw->phy.media_type == ixgbe_media_type_copper) ?
766             MAC_PROP_PERM_RW : MAC_PROP_PERM_READ;
767         mac_prop_info_set_perm(prh, perm);
768         mac_prop_info_set_default_uint8(prh, 1);
769     }
770     break;

772 case MAC_PROP_EN_1000FDX_CAP:
773     if (speeds & IXGBE_LINK_SPEED_1GB_FULL) {
774         perm = (hw->phy.media_type == ixgbe_media_type_copper) ?
775             MAC_PROP_PERM_RW : MAC_PROP_PERM_READ;
776         mac_prop_info_set_perm(prh, perm);
777         mac_prop_info_set_default_uint8(prh, 1);
778     }
779     break;

781 case MAC_PROP_EN_100FDX_CAP:
782     if (speeds & IXGBE_LINK_SPEED_100_FULL) {
783         perm = (hw->phy.media_type == ixgbe_media_type_copper) ?
784             MAC_PROP_PERM_RW : MAC_PROP_PERM_READ;
785         mac_prop_info_set_perm(prh, perm);
786         mac_prop_info_set_default_uint8(prh, 1);
787     }
788     break;

790 case MAC_PROP_FLOWCTRL:
791     mac_prop_info_set_default_link_flowctrl(prh,
792         LINK_FLOWCTRL_NONE);
793     break;

795 case MAC_PROP_MTU:
796     mac_prop_info_set_range_uint32(prh,
797         DEFAULT_MTU, ixgbe->capab->max_mtu);
798     break;

800 case MAC_PROP_PRIVATE: {
801     char valstr[64];

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802     int value;

804     bzero(valstr, sizeof (valstr));

806     if (strcmp(pr_name, "_adv_pause_cap") == 0 ||
807         strcmp(pr_name, "_adv_asym_pause_cap") == 0) {
808         mac_prop_info_set_perm(prh, MAC_PROP_PERM_READ);
809         return;
810     }

812     if (strcmp(pr_name, "_tx_copy_thresh") == 0) {
813         value = DEFAULT_TX_COPY_THRESHOLD;
814     } else if (strcmp(pr_name, "_tx_recycle_thresh") == 0) {
815         value = DEFAULT_TX_RECYCLE_THRESHOLD;
816     } else if (strcmp(pr_name, "_tx_overload_thresh") == 0) {
817         value = DEFAULT_TX_OVERLOAD_THRESHOLD;
818     } else if (strcmp(pr_name, "_tx_resched_thresh") == 0) {
819         value = DEFAULT_TX_RESCHED_THRESHOLD;
820     } else if (strcmp(pr_name, "_rx_copy_thresh") == 0) {
821         value = DEFAULT_RX_COPY_THRESHOLD;
822     } else if (strcmp(pr_name, "_rx_limit_per_intr") == 0) {
823         value = DEFAULT_RX_LIMIT_PER_INTR;
824     } else if (strcmp(pr_name, "_intr_throttling") == 0) {
824         if (strcmp(pr_name, "_intr_throttling") == 0) {
825             value = ixgbe->capab->def_intr_throttle;
826         } else {
827             return;
828         }
830     } else {
831         (void) snprintf(valstr, sizeof (valstr), "%x", value);
832     }
833 }

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unchanged portion omitted