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*****
8567 Mon Jan 14 13:12:18 2019
new/usr/src/uts/intel/io/intel_nhm/dimm_topo.c
10082 intel_nhm is checking for NULL arrays
*****
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27 /*
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29  */

31 #include <sys/types.h>
32 #include <sys/time.h>
33 #include <sys/nvpair.h>
34 #include <sys/cmn_err.h>
35 #include <sys/cred.h>
36 #include <sys/open.h>
37 #include <sys/ddi.h>
38 #include <sys/sunddi.h>
39 #include <sys/conf.h>
40 #include <sys/modctl.h>
41 #include <sys/cyclic.h>
42 #include <sys/errorq.h>
43 #include <sys/stat.h>
44 #include <sys/cpuvar.h>
45 #include <sys/mc_intel.h>
46 #include <sys/mc.h>
47 #include <sys/fm/protocol.h>
48 #include "nhm_log.h"
49 #include "intel_nhm.h"

51 extern nvlist_t *inhm_mc_nvlist[MAX_CPU_NODES];
52 extern char closed_page;
53 extern char ecc_enabled;
54 extern char lockstep[MAX_CPU_NODES];
55 extern char mirror_mode[MAX_CPU_NODES];
56 extern char spare_channel[MAX_CPU_NODES];

58 static void
59 inhm_vrank(nvlist_t *vrank, int num, uint64_t dimm_base, uint64_t limit,
60           uint32_t sinterleave, uint32_t cinterleave, uint32_t rinterleave,
61           uint32_t sway, uint32_t cway, uint32_t rway)

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62 {
63     char buf[128];

65     (void) snprintf(buf, sizeof (buf), "dimm-rank-base-%d", num);
66     (void) nvlist_add_uint64(vrank, buf, dimm_base);
67     (void) snprintf(buf, sizeof (buf), "dimm-rank-limit-%d", num);
68     (void) nvlist_add_uint64(vrank, buf, dimm_base + limit);
69     if (sinterleave > 1) {
70         (void) snprintf(buf, sizeof (buf), "dimm-socket-interleave-%d",
71             num);
72         (void) nvlist_add_uint32(vrank, buf, sinterleave);
73         (void) snprintf(buf, sizeof (buf),
74             "dimm-socket-interleave-way-%d", num);
75         (void) nvlist_add_uint32(vrank, buf, sway);
76     }
77     if (cinterleave > 1) {
78         (void) snprintf(buf, sizeof (buf), "dimm-channel-interleave-%d",
79             num);
80         (void) nvlist_add_uint32(vrank, buf, cinterleave);
81         (void) snprintf(buf, sizeof (buf),
82             "dimm-channel-interleave-way-%d", num);
83         (void) nvlist_add_uint32(vrank, buf, cway);
84     }
85     if (rinterleave > 1) {
86         (void) snprintf(buf, sizeof (buf), "dimm-rank-interleave-%d",
87             num);
88         (void) nvlist_add_uint32(vrank, buf, rinterleave);
89         (void) snprintf(buf, sizeof (buf),
90             "dimm-rank-interleave-way-%d", num);
91         (void) nvlist_add_uint32(vrank, buf, rway);
92     }
93 }
    unchanged_portion_omitted

136 static nvlist_t *
137 inhm_dimm(nhm_dimm_t *nhm_dimm, uint32_t node, uint8_t channel, uint32_t dimm)
138 {
139     nvlist_t *newdimm;
140     uint8_t t;
141     char sbuf[65];

143     (void) nvlist_alloc(&newdimm, NV_UNIQUE_NAME, KM_SLEEP);
144     (void) nvlist_add_uint32(newdimm, "dimm-number", dimm);

146     if (nhm_dimm->dimm_size >= 1024*1024*1024) {
147         (void) snprintf(sbuf, sizeof (sbuf), "%dG",
148             (int)(nhm_dimm->dimm_size / (1024*1024*1024)));
149     } else {
150         (void) snprintf(sbuf, sizeof (sbuf), "%dM",
151             (int)(nhm_dimm->dimm_size / (1024*1024)));
152     }
153     (void) nvlist_add_string(newdimm, "dimm-size", sbuf);
154     (void) nvlist_add_uint64(newdimm, "size", nhm_dimm->dimm_size);
155     (void) nvlist_add_uint32(newdimm, "nbanks", (uint32_t)nhm_dimm->nbanks);
156     (void) nvlist_add_uint32(newdimm, "ncolumn",
157         (uint32_t)nhm_dimm->ncolumn);
158     (void) nvlist_add_uint32(newdimm, "nrow", (uint32_t)nhm_dimm->nrow);
159     (void) nvlist_add_uint32(newdimm, "width", (uint32_t)nhm_dimm->width);
160     (void) nvlist_add_uint32(newdimm, "ranks", (uint32_t)nhm_dimm->nranks);
161     inhm_rank(newdimm, nhm_dimm, node, channel, dimm,
162         nhm_dimm->dimm_size / nhm_dimm->nranks);
163     if (nhm_dimm->manufacturer[0]) {
164         if (nhm_dimm->manufacturer && nhm_dimm->manufacturer[0]) {
165             t = sizeof (nhm_dimm->manufacturer);
166             (void) strncpy(sbuf, nhm_dimm->manufacturer, t);
167             sbuf[t] = 0;

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167         (void) nvlist_add_string(newdimm, "manufacturer", sbuf);
168     }
169     if (nhm_dimm->serial_number[0]) {
170         if (nhm_dimm->serial_number && nhm_dimm->serial_number[0]) {
171             t = sizeof (nhm_dimm->serial_number);
172             (void) strncpy(sbuf, nhm_dimm->serial_number, t);
173             sbuf[t] = 0;
174             (void) nvlist_add_string(newdimm, FM_FMRI_HC_SERIAL_ID, sbuf);
175         }
176     }
177     if (nhm_dimm->part_number[0]) {
178         if (nhm_dimm->part_number && nhm_dimm->part_number[0]) {
179             t = sizeof (nhm_dimm->part_number);
180             (void) strncpy(sbuf, nhm_dimm->part_number, t);
181             sbuf[t] = 0;
182             (void) nvlist_add_string(newdimm, FM_FMRI_HC_PART, sbuf);
183         }
184     }
185     if (nhm_dimm->revision[0]) {
186         if (nhm_dimm->revision && nhm_dimm->revision[0]) {
187             t = sizeof (nhm_dimm->revision);
188             (void) strncpy(sbuf, nhm_dimm->revision, t);
189             sbuf[t] = 0;
190             (void) nvlist_add_string(newdimm, FM_FMRI_HC_REVISION, sbuf);
191         }
192     }
193     t = sizeof (nhm_dimm->label);
194     (void) strncpy(sbuf, nhm_dimm->label, t);
195     sbuf[t] = 0;
196     (void) nvlist_add_string(newdimm, FM_FAULT_FRU_LABEL, sbuf);
197     return (newdimm);
198 }
```

unchanged portion omitted