

new/usr/src/uts/i86pc/acpi\_drv/Makefile

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2401 Mon Jan 7 13:06:56 2019

new/usr/src/uts/i86pc/acpi\_drv/Makefile

10077 usr/src/uts/Makefile changes for smatch

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```
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26 # Copyright 2016 Joyent, Inc.
27 #
28 #
29 # This makefile drives the production of the acpi_drv
30 # driver kernel module.
31 #
32 # i86pc architecture dependent
33 #
34 #
35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../
39 #
40 #
41 # Define the module and object file sets.
42 #
43 MODULE = acpi_drv
44 OBJECTS = $(ACPI_DRV_OBJS:%=$(OBJSDIR)/%)
45 LINTS = $(ACPI_DRV_OBJS:%.o=$(LINTSDIR)/%.ln)
46 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
47 CONF_SRCDIR = $(UTSBASE)/i86pc/io/acpi_drv
48 #
49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/i86pc/Makefile.i86pc
53 #
54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY) $(CONFMOD)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
```

new/usr/src/uts/i86pc/acpi\_drv/Makefile

2

```
61 DEBUG_FLGS =
62 $(NOT_RELEASE_BUILD)DEBUG_DEFS += $(DEBUG_FLGS)
63 #
64 CPPFLAGS += -DSUNDDI
65 #
66 CERRWARN += -_gcc=-Wno-unused-function
67 #
68 # needs work
69 $(OBJSDIR)/acpi_video.o := SMOFF += all_func_returns
70 #
71 LDFLAGS += -dy -N misc/acpica
72 #
73 #
74 # For now, disable these lint checks; maintainers should endeavor
75 # to investigate and remove these for maximum lint coverage.
76 # Please do not carry these forward to new Makefiles.
77 #
78 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
79 #
80 #
81 # Overrides
82 #
83 C99LMODE= -Xc99=%all
84 #
85 #
86 # Default build targets.
87 #
88 .KEEP_STATE:
89 #
90 def: $(DEF_DEPS)
91 #
92 all: $(ALL_DEPS)
93 #
94 clean: $(CLEAN_DEPS)
95 #
96 clobber: $(CLOBBER_DEPS)
97 #
98 lint: $(LINT_DEPS)
99 #
100 modlintlib: $(MODLINTLIB_DEPS)
101 #
102 clean.lint: $(CLEAN_LINT_DEPS)
103 #
104 install: $(INSTALL_DEPS)
105 #
106 #
107 # Include common targets.
108 #
109 include $(UTSBASE)/i86pc/Makefile.targ
```

new/usr/src/uts/i86pc/acpidev/Makefile

1

```
*****
2065 Mon Jan 7 13:06:59 2019
new/usr/src/uts/i86pc/acpidev/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # Copyright 2016, Joyent, Inc.
28 #
29 # This makefile drives the production of the ACPI device configuration
30 # kernel module.
31 #
32 # i86pc architecture dependent
33 #
34 #
35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../
39 #
40 #
41 # Define the module and object file sets.
42 #
43 MODULE = acpidev
44 OBJECTS = $(ACPIDEV_OBJS:%=$(OBJDIR)/%)
45 LINTS = $(ACPIDEV_OBJS:%.o=$(LINTS_DIR)/%.ln)
46 ROOTMODULE = $(ROOT_PSM_MISC_DIR)/$(MODULE)
47 #
48 #
49 # Include common rules.
50 #
51 include $(UTSBASE)/i86pc/Makefile.i86pc
52 #
53 #
54 # Define targets
55 #
56 ALL_TARGET = $(BINARY)
57 LINT_TARGET = $(MODULE).lint
58 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
59 #
60 #
```

new/usr/src/uts/i86pc/acpidev/Makefile

2

```
61 # Depends on acpica ACPI CA interpreter
62 #
63 LDFLAGS += -dy -N misc/acpica
64 #
65 CERRWARN += -_gcc=-Wno-uninitialized
66 CERRWARN += -_gcc=-Wno-unused-function
67 CERRWARN += -_gcc=-Wno-type-limits
68 #
69 # needs work
70 SMOFF += deref_check
71 #
72 #
73 # Default build targets.
74 #
75 .KEEP_STATE:
76 #
77 def: $(DEF_DEPS)
78 #
79 all: $(ALL_DEPS)
80 #
81 clean: $(CLEAN_DEPS)
82 #
83 clobber: $(CLOBBER_DEPS)
84 #
85 lint: $(LINT_DEPS)
86 #
87 modlintlib: $(MODLINTLIB_DEPS)
88 #
89 clean.lint: $(CLEAN_LINT_DEPS)
90 #
91 install: $(INSTALL_DEPS)
92 #
93 #
94 # Include common targets.
95 #
96 include $(UTSBASE)/i86pc/Makefile.targ
```

```

*****
1997 Mon Jan 7 13:07:01 2019
new/usr/src/uts/i86pc/acpinex/Makefile
10077 usr/src/uts/ Makefile changes for smatch
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22 # uts/i86pc/acpinex/Makefile
23 #
24 # Copyright (c) 2009, Intel Corporation.
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27 # Copyright (c) 2018, Joyent, Inc.
27 # Copyright 2016, Joyent, Inc.
28 #
29 # This makefile drives the production of the ACPI virtual nexus
30 # kernel module.
31 #
32 # intel platform dependent
33 #
34 #
35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../

40 #
41 # Define the module and object file sets.
42 #
43 MODULE = acpinex
44 OBJECTS = $(ACPINEX_OBJS:%=$(OBJDIR)/%)
45 LINTS = $(ACPINEX_OBJS:%.o=$(LINTS_DIR)/%.ln)
46 ROOTMODULE = $(ROOT_PSM_DRV_DIR)/$(MODULE)

48 #
49 # Include common rules.
50 #
51 include $(UTSBASE)/i86pc/Makefile.i86pc

53 #
54 # Define targets
55 #
56 ALL_TARGET = $(BINARY)
57 LINT_TARGET = $(MODULE).lint
58 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

60 #

```

```

61 # Depends on acpica ACPI CA interpreter
62 #
63 LDFLAGS += -dy -N misc/acpica -N misc/acpidev

65 CERRWARN += -_gcc=-Wno-unused-function

67 # needs work
68 SMOFF += deref_check

70 #
71 # Default build targets.
72 #
73 .KEEP_STATE:

75 def: $(DEF_DEPS)

77 all: $(ALL_DEPS)

79 clean: $(CLEAN_DEPS)

81 clobber: $(CLOBBER_DEPS)

83 lint: $(LINT_DEPS)

85 modlintlib: $(MODLINTLIB_DEPS)

87 clean.lint: $(CLEAN_LINT_DEPS)

89 install: $(INSTALL_DEPS)

91 #
92 # Include common targets.
93 #
94 include $(UTSBASE)/i86pc/Makefile.targ

```

new/usr/src/uts/i86pc/apix/Makefile

1

```
*****
2138 Mon Jan 7 13:07:02 2019
new/usr/src/uts/i86pc/apix/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
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22 # uts/i86pc/apix/Makefile
23 #
24 # Copyright (c) 2010, Oracle and/or its affiliates. All rights reserved.
25 # Copyright (c) 2018, Joyent, Inc.
25 # Copyright 2017, Joyent, Inc.
26 #
27 # This makefile drives the production of the apix "mach"
28 # kernel module.
29 #
30 # apix implementation architecture dependent
31 #
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../

38 #
39 # Define the module and object file sets.
40 #
41 MODULE = apix
42 OBJECTS = ${APIX_OBJS:%=${OBJS_DIR}/%}
43 LINTS = ${APIX_OBJS:%.o=${LINTS_DIR}/%.ln}
44 ROOTMODULE = ${ROOT_PSM_MACH_DIR}/${MODULE}

46 #
47 # Include common rules.
48 #
49 include ${UTSBASE}/i86pc/Makefile.i86pc

51 #
52 # Define targets
53 #
54 ALL_TARGET = ${BINARY}
55 LINT_TARGET = ${MODULE}.lint
56 INSTALL_TARGET = ${BINARY} ${ROOTMODULE}

58 DEBUG_FLGS =
59 $(NOT_RELEASE_BUILD)DEBUG_DEFS += ${DEBUG_FLGS}
```

new/usr/src/uts/i86pc/apix/Makefile

2

```
61 #
62 # Depends on ACPI CA interpreter
63 #
64 LDFLAGS += -dy -N misc/acpica

66 # needs work, real bug in apic_allocate_irq()
67 ${OBJS_DIR}/mp_platform_common.o := SMOFF += indenting
68 # needs work
69 ${OBJS_DIR}/psm_common.o := SMOFF += deref_check

71 #
72 # Default build targets.
73 #
74 .KEEP_STATE:

76 def: $(DEF_DEPS)

78 all: $(ALL_DEPS)

80 clean: $(CLEAN_DEPS)

82 clobber: $(CLOBBER_DEPS)

84 lint: $(LINT_DEPS)

86 modlintlib: $(MODLINTLIB_DEPS)

88 clean.lint: $(CLEAN_LINT_DEPS)

90 install: $(INSTALL_DEPS)

92 #
93 # Include common targets.
94 #
95 include ${UTSBASE}/i86pc/Makefile.targ
```

new/usr/src/uts/i86pc/dr/Makefile

1

```
*****
2274 Mon Jan 7 13:07:03 2019
new/usr/src/uts/i86pc/dr/Makefile
10077 usr/src/uts/ Makefile changes for smatch
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25 # Copyright (c) 2010, Intel Corporation.
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27 #
28 # Copyright (c) 2018, Joyent, Inc.
29 # This makefile drives the production of the dr driver module.
30 # i86pc architecture dependent
31 #
32 #
33 # Path to the base of the uts directory tree (usually /usr/src/uts).
34 UTSBASE = ../../
35 PLATFORM = i86pc
36 #
37 # Define the module and object file sets.
38 #
39 MODULE = dr
40 OBJECTS = $(DR_OBJS:%=$(OBJS_DIR)/%)
41 LINTS = $(DR_OBJS:%.o=$(LINTS_DIR)/%.ln)
42 ROOTMODULE = $(ROOT_PSM_DRV_DIR)/$(MODULE)
43 CONF_SRCDIR = $(UTSBASE)/i86pc/io/dr
44 #
45 # Include common rules.
46 #
47 #
48 include $(UTSBASE)/i86pc/Makefile.i86pc
49 #
50 # Define targets
51 #
52 #
53 ALL_TARGET = $(BINARY) $(SRC_CONFILE)
54 LINT_TARGET = $(MODULE).lint
55 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFILE)
56 #
```

new/usr/src/uts/i86pc/dr/Makefile

2

```
58 # Overrides
59 #
60 DEF_BUILDS = $(DEF_BUILDS64)
61 ALL_BUILDS = $(ALL_BUILDS64)
62 #
63 #
64 # lint pass one enforcement
65 #
66 CFLAGS += $(CCVERBOSE)
67 #
68 CERRWARN += -_gcc=-Wno-parentheses
69 CERRWARN += -_gcc=-Wno-uninitialized
70 CERRWARN += -_gcc=-Wno-empty-body
71 #
72 # needs work
73 SMOFF += index_overflow
74 #
75 #
76 # module dependencies
77 #
78 LDFLAGS += -dy -Nmisc/drmach_acpi
79 #
80 CLEANFILES += $(DR_GENERR)
81 CLEANFILES += $(DR_IO)/dr_err.c
82 #
83 #
84 # Default build targets.
85 #
86 .KEEP_STATE:
87 #
88 def: $(DEF_DEPS)
89 #
90 all: $(ALL_DEPS)
91 #
92 clean: $(CLEAN_DEPS)
93 #
94 clobber: $(CLOBBER_DEPS)
95 #
96 lint: $(LINT_DEPS)
97 #
98 modlintlib: $(MODLINTLIB_DEPS)
99 #
100 clean.lint: $(CLEAN_LINT_DEPS)
101 #
102 install: $(INSTALL_DEPS) $(CONF_INSTALL_DEPS)
103 #
104 #
105 # Include common targets.
106 #
107 include $(UTSBASE)/i86pc/Makefile.targ
```

```

*****
2010 Mon Jan 7 13:07:04 2019
new/usr/src/uts/i86pc/i86hvm/Makefile.i86hvm
10077 usr/src/uts/Makefile changes for smatch
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28 ROOT_HVM_DIR           = $(ROOT_PLAT_DIR)/i86hvm
29 ROOT_HVM_MOD_DIR       = $(ROOT_HVM_DIR)/kernel
30 ROOT_HVM_DRV_DIR_32    = $(ROOT_HVM_MOD_DIR)/drv
31 ROOT_HVM_DRV_DIR_64    = $(ROOT_HVM_MOD_DIR)/drv/$(MACH64)
32 ROOT_HVM_DRV_DIR       = $(ROOT_HVM_DRV_DIR_$(CLASS))
33 ROOT_HVM_MISC_DIR_32   = $(ROOT_HVM_MOD_DIR)/misc
34 ROOT_HVM_MISC_DIR_64   = $(ROOT_HVM_MOD_DIR)/misc/$(MACH64)
35 ROOT_HVM_MISC_DIR     = $(ROOT_HVM_MISC_DIR_$(CLASS))
36 USR_HVM_DIR            = $(USR_PLAT_DIR)/i86hvm

38 HVM_LINT_LIB_DIR= $(UTSBASE)/$(PLATFORM)/i86hvm/lint-libs/$(OBJS_DIR)

40 #
41 #   Define modules.
42 #
43 HVM_DRV_KMODS= cmdk rtls xdf xnf xpv xpvd
44 HVM_MISC_KMODS= hvm_bootstrap
45 HVM_KMODS=      $(HVM_DRV_KMODS) $(HVM_MISC_KMODS)

47 include $(UTSBASE)/i86pc/i86hvm/Makefile.files
48 #
49 #   Include common rules.
50 #
51 include $(UTSBASE)/i86pc/Makefile.i86pc

53 LINTS_DIR           = $(OBJS_DIR)
54 LINT_LIB_DIR        = $(UTSBASE)/$(PLATFORM)/lint-libs/$(OBJS_DIR)

56 #
57 # Indicate that we are building for the i86hvm semi-platform.
58 # Also use Solaris specific code in xen public header files.
59 #
60 CPPFLAGS            += -DXPV_HVM_DRIVER -D_SOLARIS
61 ASFLAGS              += -DXPV_HVM_DRIVER

```

```

63 # needs work
64 SMATCH=off

```

```

*****
4902 Mon Jan 7 13:07:05 2019
new/usr/src/uts/i86pc/isa/Makefile
10077 usr/src/uts/ Makefile changes for smatch
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26 # Copyright (c) 2018, Joyent, Inc.
27 # Copyright 2016 Joyent, Inc.
28 #
29 # This makefile drives the production of the ISA nexus driver
30 #
31 # i86pc implementation architecture dependent
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../

38 #
39 # Define the module and object file sets.
40 #
41 MODULE = isa
42 OBJECTS = $(ISANEXUS_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(ISANEXUS_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_PSM_DRV_DIR)/$(MODULE)
45 INC_PATH += -I$(UTSBASE)/i86pc/sys/acpi

47 #
48 # Include common rules.
49 #
50 include $(UTSBASE)/i86pc/Makefile.i86pc

52 #
53 # Define targets
54 #
55 ALL_TARGET = $(BINARY)
56 LINT_TARGET = $(MODULE).lint
57 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

59 $(OBJS_DIR)/isa.o := CERRWARN += -_gcc=-Wno-unused-function

```

```

61 # needs work
62 $(OBJS_DIR)/i8237A.o := SMOFF += indenting

64 #
65 # lint pass one enforcement
66 #
67 CFLAGS += $(CCVERBOSE)

69 #
70 # Dependency
71 LDFLAGS += -dy -Nmisc/acpica -Nmisc/busra -Nmisc/pci_autoconfig

73 #
74 # Define our version of dma_engine and i8237A interfaces
75 #
76 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_init=$(MODULE)_i_dmae_init
77 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_acquire=$(MODULE)_i_dmae_acquire
78 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_free=$(MODULE)_i_dmae_free
79 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_get_best_mode=$(MODULE)_i_dmae_g
80 $(OBJECTS) $(LINTS) := CPPFLAGS += -D_dmae_nxcookie=$(MODULE)_dmae_nxcookie
81 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_prog=$(MODULE)_i_dmae_prog
82 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_swsetup=$(MODULE)_i_dmae_swsetup
83 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_swstart=$(MODULE)_i_dmae_swstart
84 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_stop=$(MODULE)_i_dmae_stop
85 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_enable=$(MODULE)_i_dmae_enable
86 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_disable=$(MODULE)_i_dmae_disable
87 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_get_chan_stat=$(MODULE)_i_dmae_g
88 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_init=$(MODULE)_d37A_init
89 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_dma_valid=$(MODULE)_d37A_dma_valid
90 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_dma_release=$(MODULE)_d37A_dma_rel
91 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_dma_disable=$(MODULE)_d37A_dma_dis
92 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_dma_enable=$(MODULE)_d37A_dma_enab
93 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_get_best_mode=$(MODULE)_d37A_get_b
94 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_intr=$(MODULE)_d37A_intr
95 $(OBJECTS) $(LINTS) := CPPFLAGS += -DdEISA_setchain=$(MODULE)_dEISA_setchain
96 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_prog_chan=$(MODULE)_d37A_prog_chan
97 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_dma_swsetup=$(MODULE)_d37A_dma_sws
98 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_dma_swstart=$(MODULE)_d37A_dma_sws
99 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_dma_stop=$(MODULE)_d37A_dma_stop
100 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_get_chan_stat=$(MODULE)_d37A_get_c
101 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_set_mode=$(MODULE)_d37A_set_mode
102 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_write_addr=$(MODULE)_d37A_write_ad
103 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_read_addr=$(MODULE)_d37A_read_addr
104 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_write_count=$(MODULE)_d37A_write_c
105 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_read_count=$(MODULE)_d37A_read_cou
106 $(OBJECTS) $(LINTS) := CPPFLAGS +=

108 #
109 # For now, disable these lint checks; maintainers should endeavor
110 # to investigate and remove these for maximum lint coverage.
111 # Please do not carry these forward to new Makefiles.
112 #
113 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN

115 CERRWARN += -_gcc=-Wno-unused-variable

117 #
118 # Default build targets.
119 #
120 .KEEP_STATE:

122 def: $(DEF_DEPS)

124 all: $(ALL_DEPS)

126 clean: $(CLEAN_DEPS)

```

**new/usr/src/uts/i86pc/isa/Makefile**

**3**

```
128 clobber:      $(CLOBBER_DEPS)
130 lint:         $(LINT_DEPS)
132 modlintlib:   $(MODLINTLIB_DEPS)
134 clean.lint:   $(CLEAN_LINT_DEPS)
136 install:      $(INSTALL_DEPS)

138 #
139 #   Include common targets.
140 #
141 include $(UTSBASE)/i86pc/Makefile.targ
```



```

*****
2139 Mon Jan 7 13:07:06 2019
new/usr/src/uts/i86pc/pcie/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
12 #
13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/i86pc/pcie/Makefile
23 #
24 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
28 #
29 # This makefile drives the production of the kernel/misc/pcie module
30 # for PCI-E Error handling support in PCI-E nexus drivers.
31 #
32 # i86pc implementation architecture dependent
33 #
34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../../
38 #
39 # Define the module and object file sets.
40 #
41 MODULE = pcie
42 OBJECTS = $(PCIE_MISC_OBJS:%=$(OBJSDIR)/%) \
43           $(PCI_STRING_OBJS:%=$(OBJSDIR)/%)
44 LINTS = $(PCIE_MISC_OBJS:%.o=$(LINTSDIR)/%.ln) \
45         $(PCI_STRING_OBJS:%.o=$(LINTSDIR)/%.ln)
46 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
47 #
48 # Include common rules.
49 #
50 include $(UTSBASE)/i86pc/Makefile.i86pc
51 #
52 # Dependency
53 #
54 LDFLAGS += -dy -Nmisc/acpica -Nmisc/busra
55 #
56 # Define targets

```

```

56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
60 #
61 CERRWARN += -_gcc=-Wno-unused-value
62 CERRWARN += -_gcc=-Wno-uninitialized
63 CERRWARN += -_gcc=-Wno-parentheses
64 CERRWARN += -_gcc=-Wno-unused-variable
65 CERRWARN += -_gcc=-Wno-unused-function # safe
66 #
67 # needs work
68 SMOFF += all_func_returns,deref_check
69 #
70 #
71 # Default build targets.
72 #
73 .KEEP_STATE:
74 #
75 def: $(DEF_DEPS)
76 #
77 all: $(ALL_DEPS)
78 #
79 clean: $(CLEAN_DEPS)
80 #
81 clobber: $(CLOBBER_DEPS)
82 #
83 lint: $(LINT_DEPS)
84 #
85 modlintlib: $(MODLINTLIB_DEPS)
86 #
87 clean.lint: $(CLEAN_LINT_DEPS)
88 #
89 install: $(INSTALL_DEPS)
90 #
91 #
92 # Include common targets.
93 #
94 include $(UTSBASE)/i86pc/Makefile.targ

```

new/usr/src/uts/i86pc/pcplusmp/Makefile

1

```
*****
2158 Mon Jan 7 13:07:08 2019
new/usr/src/uts/i86pc/pcplusmp/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
12 #
13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/i86pc/pcplusmp/Makefile
23 #
24 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # Copyright 2017, Joyent, Inc.
28 #
30 #
31 # This makefile drives the production of the pcplusmp "mach"
32 # kernel module.
33 #
34 # pcplusmp implementation architecture dependent
35 #
37 #
38 # Path to the base of the uts directory tree (usually /usr/src/uts).
39 #
40 UTSBASE = ../../
42 #
43 # Define the module and object file sets.
44 #
45 MODULE = pcplusmp
46 OBJECTS = $(PCPLUSMP_OBJS:%=$(OBJSDIR)/%)
47 LINTS = $(PCPLUSMP_OBJS:%.o=$(LINTSDIR)/%.ln)
48 ROOTMODULE = $(ROOT_PSM_MACH_DIR)/$(MODULE)
50 #
51 # Include common rules.
52 #
53 include $(UTSBASE)/i86pc/Makefile.i86pc
55 #
56 # Define targets
57 #
58 ALL_TARGET = $(BINARY)
59 LINT_TARGET = $(MODULE).lint
60 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
```

new/usr/src/uts/i86pc/pcplusmp/Makefile

2

```
62 DEBUG_FLGS =
63 $(NOT_RELEASE_BUILD)DEBUG_DEFS += $(DEBUG_FLGS)
65 #
66 # Depends on ACPI CA interpreter
67 #
68 LDFLAGS += -dy -N misc/acpica
70 # needs work
71 $(OBJSDIR)/mp_platform_common.o := SMOFF += indenting
72 # needs work
73 $(OBJSDIR)/psm_common.o := SMOFF += deref_check
75 #
76 # Default build targets.
77 #
78 .KEEP_STATE:
80 def: $(DEF_DEPS)
82 all: $(ALL_DEPS)
84 clean: $(CLEAN_DEPS)
86 clobber: $(CLOBBER_DEPS)
88 lint: $(LINT_DEPS)
90 modlintlib: $(MODLINTLIB_DEPS)
92 clean.lint: $(CLEAN_LINT_DEPS)
94 install: $(INSTALL_DEPS)
96 #
97 # Include common targets.
98 #
99 include $(UTSBASE)/i86pc/Makefile.targ
```

new/usr/src/uts/i86pc/rootnex/Makefile

1

```
*****
2506 Mon Jan 7 13:07:11 2019
new/usr/src/uts/i86pc/rootnex/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
12 #
13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/i86pc/rootnex/Makefile
23 # Copyright 2010 Sun Microsystems, Inc. All rights reserved.
24 # Copyright (c) 2011 Bayard G. Bell. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the rootnex driver
28 #
29 # i86pc implementation architecture dependent
30 #
31 #
32 #
33 #
34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 # UTSBASE = ../../
38 #
39 # Define the module and object file sets.
40 #
41 # MODULE = rootnex
42 # OBJECTS = $(ROOTNEX_OBJS:%=$(OBJS_DIR)/%)
43 # LINTS = $(ROOTNEX_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 # ROOTMODULE = $(ROOT_PSM_DRV_DIR)/$(MODULE)
45 # CONF_SRCDIR = $(UTSBASE)/i86pc/io
46 #
47 #
48 # Include common rules.
49 #
50 #
51 # include $(UTSBASE)/i86pc/Makefile.i86pc
52 #
53 # Define targets
54 #
55 # ALL_TARGET = $(BINARY) $(SRC_CONFILE)
56 # LINT_TARGET = $(MODULE).lint
57 # INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
58 #
59 # Define dependencies on iomulib and acpica
60 #
```

new/usr/src/uts/i86pc/rootnex/Makefile

2

```
58 LDFLAGS += -dy -N misc/iomulib -N misc/acpica
60 #
61 # For now, disable these lint checks; maintainers should endeavor
62 # to investigate and remove these for maximum lint coverage.
63 # Please do not carry these forward to new Makefiles.
64 #
65 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
66 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
67 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
68 #
69 CERRWARN += -_gcc=-Wno-unused-label
70 CERRWARN += -_gcc=-Wno-parentheses
71 CERRWARN += -_gcc=-Wno-unused-function
72 CERRWARN += -_gcc=-Wno-uninitialized
73 #
74 # needs work
75 $(OBJS_DIR)/immu_qinv.o := SMOFF += index_overflow
76 #
77 #
78 # Default build targets.
79 #
80 .KEEP_STATE:
81 #
82 def: $(DEF_DEPS)
83 #
84 all: $(ALL_DEPS)
85 #
86 clean: $(CLEAN_DEPS)
87 #
88 clobber: $(CLOBBER_DEPS)
89 #
90 lint: $(LINT_DEPS)
91 #
92 modlintlib: $(MODLINTLIB_DEPS)
93 #
94 clean.lint: $(CLEAN_DEPS)
95 #
96 install: $(INSTALL_DEPS) $(CONF_INSTALL_DEPS)
97 #
98 #
99 # Include common targets.
100 #
101 include $(UTSBASE)/i86pc/Makefile.targ
```

```

*****
7032 Mon Jan 7 13:07:12 2019
new/usr/src/uts/i86pc/unix/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
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8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 #
24 # Copyright (c) 2018, Joyent, Inc.
25 # This makefile drives the production of unix (and unix.o).
26 #
27 #
28 # i86pc implementation architecture dependent
29 #
30 #
31 #
32 # Path to the base of the uts directory tree (usually /usr/src/uts).
33 #
34 # Define the module and object file sets.
35 #
36 #
37 #
38 #
39 #
40 #
41 #
42 #
43 #
44 #
45 #
46 #
47 #
48 #
49 #
50 #
51 #
52 #
53 #
54 #
55 #

```

```

56 GENUNIX_DIR = ../../intel/$(GENUNIX)
57 #
58 LIBOPTS = -L $(GENUNIX_DIR)/$(OBJS_DIR) -l $(GENUNIX)
59 #
60 COMMP_CTF_SRC = $(OBJS_DIR)/comm_page_ctf.c
61 #
62 CTFEXTRAOBJS = $(OBJS_DIR)/vers.o $(OBJS_DIR)/comm_page_ctf.o
63 #
64 DBOOT_OBJS_DIR = dboot/$(OBJS_DIR)
65 DBOOT_OBJECTS = $(DBOOT_OBJS:=$(DBOOT_OBJS_DIR)/%)
66 DBOOT_BIN = $(DBOOT_OBJS_DIR)/$(DBOOT)
67 DBOOT_O = $(OBJS_DIR)/$(DBOOT).o
68 DBOOT_S = $(DBOOT_O:%.o=%.s)
69 DBOOT_LINTS = $(DBOOT_OBJS:%.o=$(DBOOT_OBJS_DIR)/%.ln)
70 DBOOT_LINT = $(i386_LINT)
71 DBOOT_LINTTAGS = -erroff=E_STATIC_UNUSED
72 #
73 #
74 # Include common rules.
75 #
76 include $(UTSBASE)/i86pc/Makefile.i86pc
77 #
78 #
79 # Define targets
80 #
81 ALL_TARGET = $(UNIX_BIN)
82 LINT_TARGET = $(LINT_LIB) $(DBOOT_LINT_LIB)
83 INSTALL_TARGET = $(UNIX_BIN) $(ROOTMODULE)
84 #
85 #
86 # This is UNIX_DIR. Use a short path.
87 #
88 UNIX_DIR = .
89 #
90 #
91 # Overrides
92 #
93 CLEANFILES += \
94 $(UNIX_O) $(MODSTUBS_O) \
95 $(OBJS_DIR)/vers.c \
96 $(OBJS_DIR)/dtracestubs.s \
97 $(DTRACESTUBS_O) $(DTRACESTUBS) \
98 $(CTFEXTRAOBJS) \
99 $(COMMP_CTF_SRC)
100 #
101 CLEANFILES += \
102 $(DBOOT_O) $(DBOOT_S) \
103 $(DBOOT_OBJECTS) \
104 $(OBJS_DIR)/bios_call_src.o \
105 $(OBJS_DIR)/bios_call_src \
106 $(OBJS_DIR)/bios_call.s \
107 $(DBOOT_BIN)
108 #
109 CLEANFILES += \
110 $(DBOOT_OBJS_DIR)/$(VGATEXT_FONT).c \
111 $(OBJS_DIR)/$(VGATEXT_FONT).c
112 #
113 CLEANFILES += \
114 $(OBJS_DIR)/fb_swch_src.o \
115 $(OBJS_DIR)/fb_swch_src \
116 $(OBJS_DIR)/fb_swch.s
117 #
118 CLEANFILES += \
119 $(ZLIB_OBJS:%.o=$(OBJS_DIR)/%.o) \
120 $(ZLIB_OBJS:%.o=$(OBJS_DIR)/%.ln)

```

```

122 CLOBBERFILES = $(CLEANFILES) $(UNIX_BIN)
123 CLEANLINTFILES += $(LINT_LIB) $(DBOOT_LINT_LIB) $(DBOOT_LINTS)

125 # instr_size needs a special header
126 $(OBJS_DIR)/instr_size.o := EXTRA_OPTIONS = -I$(SRC)/common/dis/i386
127 $(OBJS_DIR)/instr_size.ln := EXTRA_OPTIONS = -I$(SRC)/common/dis/i386

129 #
130 # When performing shadow studio builds, the pre-processed comm page
131 # output from gcc can end up confusing studio.
132 #
133 $(OBJS_DIR)/comm_page_ctf.o := CERRWARN += -_gcc=-erroff=E_TKNS_IGNORED_AT_END_O

135 CFLAGS += -DDIS_MEM

137 #
138 # For now, disable these lint checks; maintainers should endeavor
139 # to investigate and remove these for maximum lint coverage.
140 # Please do not carry these forward to new Makefiles.
141 #
142 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
143 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
144 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
145 LINTTAGS += -erroff=E_STATIC_UNUSED
146 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
147 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

149 CERRWARN += _gcc=-Wno-parentheses
150 CERRWARN += _gcc=-Wno-uninitialized
151 CERRWARN += _gcc=-Wno-char-subscripts
152 CERRWARN += _gcc=-Wno-unused-variable
153 CERRWARN += _gcc=-Wno-unused-function
154 CERRWARN += _gcc=-Wno-unused-label
155 CERRWARN += _gcc=-Wno-type-limits
156 CERRWARN += _gcc=-Wno-clobbered
157 CERRWARN += _gcc=-Wno-empty-body
158 CERRWARN += _gcc=-Wno-unused-value

160 # false positives
161 SMOFF += index_overflow

163 # needs work
164 SMOFF += all_func_returns,deref_check,signed

166 $(OBJS_DIR)/fmsmb.o := SMOFF += indenting
167 $(OBJS_DIR)/zutil.o := SMOFF += indenting
168 $(OBJS_DIR)/bootrd_cpio.o := SMOFF += allocating_enough_data

170 # Ensure that lint sees 'struct cpu' containing a fully declared
171 # embedded 'struct machcpu'
172 #
173 LINTFLAGS += -D_MACHDEP -I../i86pc

175 #
176 # Default build targets.
177 #
178 .KEEP_STATE:

180 def: $(DEF_DEPS)

182 all: $(ALL_DEPS)

184 clean: $(CLEAN_DEPS)

186 clobber: $(CLOBBER_DEPS)

```

```

188 lint: $(LINT_DEPS)

190 clean.lint: $(CLEAN_LINT_DEPS)

192 install: $(INSTALL_DEPS)

194 MAPFILE_32 = $(MAPFILE)
195 MAPFILE_64 = $(MAPFILE).amd64

197 MAPFILE_NAME = $(MAPFILE_$(CLASS))

199 $(UNIX_BIN): $(UNIX_O) $(MODSTUBS_O) $(MAPFILE_NAME) \
200 $(GENLIB) $(DTRACESTUBS) $(DBOOT_O)
201 $(LD) -dy -b -o $@ -e dboot_image -znointerp -M $(MAPFILE_NAME) \
202 $(UNIX_O) $(DBOOT_O) $(MODSTUBS_O) $(LIBOPTS) \
203 $(DTRACESTUBS)
204 $(MBH_PATCH) $(UNIX_BIN)
205 $(CTFMERGE_UNIQIFY_AGAINST_GENUNIX)
206 $(POST_PROCESS)

208 $(UNIX_O): $(OBJECTS) $(CTFEXTRAOBJECTS)
209 $(LD) -r -o $@ $(OBJECTS) $(OBJS_DIR)/vers.o

211 $(DBOOT_BIN): $(DBOOT_OBJS_DIR) $(DBOOT_OBJECTS) dboot/Mapfile.dboot
212 $(LD) -dn -e _start -M dboot/Mapfile.dboot \
213 -o $(DBOOT_BIN) $(DBOOT_OBJECTS)

215 $(DBOOT_O): $(DBOOT_BIN)
216 @echo ".data" > $(DBOOT_S)
217 @echo ".globl dboot_image" >> $(DBOOT_S)
218 @echo "dboot_image:" >> $(DBOOT_S)
219 $(ELFEXTRACT) $(DBOOT_BIN) >> $(DBOOT_S)
220 $(COMPILE.s) -o $(DBOOT_O) $(DBOOT_S)

222 $(DBOOT_OBJS_DIR):
223 -@mkdir -p $@ 2> /dev/null

225 $(COMP_CTF_SRC): $(UTSBASE)/i86pc/ml/comm_page.s
226 $(COMPILE.cpp) -D_GENCFTF -o $@ $(UTSBASE)/i86pc/ml/comm_page.s

228 $(OBJS_DIR)/comm_page_ctf.o: $(COMP_CTF_SRC)
229 $(COMPILE.c) -o $@ $<
230 $(CTFCONVERT_O)

232 #
233 # Special rules for generating assym.h for inclusion in assembly files.
234 #
235 $(DSF_DIR)/$(OBJS_DIR)/assym.h $(DSF_DIR)/$(OBJS_DIR)/kdi_assym.h: FRC
236 @cd $(DSF_DIR); $(MAKE) all.targ

238 #
239 # The global lint target builds the kernel lint library (llib-lunix.ln)
240 # which is equivalent to a lint of /unix.o. Then all kernel modules for
241 # this architecture are linted against the kernel lint library.
242 #
243 # Note: lint errors in the kernel lint library will be repeated for
244 # each module. It is important that the kernel lint library
245 # be clean to keep the textual output to a reasonable level.
246 #

248 $(LINT_LIB): $(LINT_LIB_DIR) $(LINTS)
249 @pwd
250 @-$(ECHO) "\n$(UNIX): (library construction):"
251 @$ (LINT) -o$(UNIX) $(LINTFLAGS) $(LINTS)
252 @$ (MV) $@F $@

```

**new/usr/src/uts/i86pc/unix/Makefile**

5

```
254 $(DBOOT_LINT_LIB):      $(LINT_LIB_DIR) $(DBOOT_LINTS)
255     @pwd
256     @-$(ECHO) "\n$(DBOOT): (library construction):"
257     @$ (LINT) -o$(DBOOT) $(DBOOT_LINTFLAGS) $(DBOOT_LINTS)
258     @$ (MV) $(@F) $@

260 lintlib:               $(LINT_DEPS)

262 #
263 #     Include common targets.
264 #
265 include $(UTSBASE)/i86pc/Makefile.targ
```

```

*****
2354 Mon Jan 7 13:07:13 2019
new/usr/src/uts/i86pc/uppc/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
12 #
13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # i86pc/uppc/Makefile
23 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
26 # Copyright 2016 Joyent, Inc.
27 #
28 # This makefile drives the production of the uppc mach
29 # kernel module.
30 #
31 # i86pc implementation architecture dependent
32 #
33 #
34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../..
38 #
39 #
40 # Define the module and object file sets.
41 #
42 MODULE = uppc
43 OBJECTS = $(UPPC_OBJS:%=$(OBJS_DIR)/%)
44 LINTS = $(UPPC_OBJS:%.o=$(LINTS_DIR)/%.ln)
45 ROOTMODULE = $(ROOT_PSM_MACH_DIR)/$(MODULE)
46 #
47 #
48 # Include common rules.
49 #
50 include $(UTSBASE)/i86pc/Makefile.i86pc
51 #
52 #
53 # Define targets
54 #
55 ALL_TARGET = $(BINARY)
56 LINT_TARGET = $(MODULE).lint
57 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
58 #
59 #
60 # Overrides.

```

```

61 #
62 CERRWARN += -_gcc=-Wno-unused-function
63 DEBUG_FLGS =
64 DEBUG_DEFS += $(DEBUG_FLGS)
65 #
66 # needs work
67 $(OBJS_DIR)/psm_common.o := SMOFF += deref_check
68 #
69 #
70 # Depends on ACPI CA interpreter
71 #
72 LDFLAGS += -dy -N misc/acpica
73 #
74 #
75 # For now, disable these lint checks; maintainers should endeavor
76 # to investigate and remove these for maximum lint coverage.
77 # Please do not carry these forward to new Makefiles.
78 #
79 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
80 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
81 #
82 #
83 # Default build targets.
84 #
85 .KEEP_STATE:
86 #
87 def: $(DEF_DEPS)
88 #
89 all: $(ALL_DEPS)
90 #
91 clean: $(CLEAN_DEPS)
92 #
93 clobber: $(CLOBBER_DEPS)
94 #
95 lint: $(LINT_DEPS)
96 #
97 modlintlib: $(MODLINTLIB_DEPS)
98 #
99 clean.lint: $(CLEAN_LINT_DEPS)
100 #
101 install: $(INSTALL_DEPS)
102 #
103 #
104 # Include common targets.
105 #
106 include $(UTSBASE)/i86pc/Makefile.targ

```

```

*****
4696 Mon Jan 7 13:07:18 2019
new/usr/src/uts/i86xpv/isa/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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26 # Copyright (c) 2018, Joyent, Inc.
27 # Copyright 2016 Joyent, Inc.
28 #
29 # This makefile drives the production of the ISA nexus driver
30 #
31 # i86xpv implementation architecture dependent
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../
37 #
38 #
39 # Define the module and object file sets.
40 #
41 MODULE = isa
42 OBJECTS = $(ISANEXUS_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(ISANEXUS_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_PSM_DRV_DIR)/$(MODULE)
45 INC_PATH += -I$(UTSBASE)/i86pc/sys/acpi
46 #
47 #
48 # Include common rules.
49 #
50 include $(UTSBASE)/i86xpv/Makefile.i86xpv
51 #
52 #
53 # Define targets
54 #
55 ALL_TARGET = $(BINARY)
56 LINT_TARGET = $(MODULE).lint
57 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
58 #
59 $(OBJS_DIR)/isa.o := CERRWARN += -_gcc=-Wno-unused-function

```

```

61 # needs work
62 $(OBJS_DIR)/i8237A.o := SMOFF += indenting
63 #
64 #
65 # lint pass one enforcement
66 #
67 CFLAGS += $(CCVERBOSE)
68 #
69 #
70 # Dependency
71 LDFLAGS += -dy -Nmisc/acpica -Nmisc/busra -Nmisc/pci_autoconfig
72 #
73 #
74 # Define our version of dma_engine and i8237A interfaces
75 #
76 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_init=$(MODULE)_i_dmae_init
77 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_acquire=$(MODULE)_i_dmae_acquire
78 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_free=$(MODULE)_i_dmae_free
79 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_get_best_mode=$(MODULE)_i_dmae_g
80 $(OBJECTS) $(LINTS) := CPPFLAGS += -D_dmae_nxcookie=$(MODULE)_dmae_nxcookie
81 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_prog=$(MODULE)_i_dmae_prog
82 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_swsetup=$(MODULE)_i_dmae_swsetup
83 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_swstart=$(MODULE)_i_dmae_swstart
84 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_stop=$(MODULE)_i_dmae_stop
85 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_enable=$(MODULE)_i_dmae_enable
86 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_disable=$(MODULE)_i_dmae_disable
87 $(OBJECTS) $(LINTS) := CPPFLAGS += -Di_dmae_get_chan_stat=$(MODULE)_i_dmae_g
88 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_init=$(MODULE)_d37A_init
89 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_dma_valid=$(MODULE)_d37A_dma_valid
90 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_dma_release=$(MODULE)_d37A_dma_rel
91 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_dma_disable=$(MODULE)_d37A_dma_dis
92 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_dma_enable=$(MODULE)_d37A_dma_enab
93 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_get_best_mode=$(MODULE)_d37A_get_b
94 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_intr=$(MODULE)_d37A_intr
95 $(OBJECTS) $(LINTS) := CPPFLAGS += -DdEISA_setchain=$(MODULE)_dEISA_setchain
96 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_prog_chan=$(MODULE)_d37A_prog_chan
97 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_dma_swsetup=$(MODULE)_d37A_dma_sws
98 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_dma_swstart=$(MODULE)_d37A_dma_sws
99 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_dma_stop=$(MODULE)_d37A_dma_stop
100 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_get_chan_stat=$(MODULE)_d37A_get_c
101 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_set_mode=$(MODULE)_d37A_set_mode
102 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_write_addr=$(MODULE)_d37A_write_ad
103 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_read_addr=$(MODULE)_d37A_read_addr
104 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_write_count=$(MODULE)_d37A_write_c
105 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_read_count=$(MODULE)_d37A_read_cou
106 $(OBJECTS) $(LINTS) := CPPFLAGS += -Dd37A_read_count=$(MODULE)_d37A_read_cou
107 #
108 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
109 CERRWARN += -_gcc=-Wno-unused-variable
110 #
111 #
112 # Default build targets.
113 #
114 .KEEP_STATE:
115 #
116 def: $(DEF_DEPS)
117 #
118 all: $(ALL_DEPS)
119 #
120 clean: $(CLEAN_DEPS)
121 #
122 clobber: $(CLOBBER_DEPS)
123 #
124 lint: $(LINT_DEPS)
125 #
126 modlintlib: $(MODLINTLIB_DEPS)

```



**new/usr/src/uts/i86xpv/isa/Makefile**

**3**

```
128 clean.lint:    $(CLEAN_LINT_DEPS)
130 install:      $(INSTALL_DEPS)

132 #
133 #      Include common targets.
134 #
135 include $(UTSBASE)/i86xpv/Makefile.targ
```

```

*****
6289 Mon Jan 7 13:07:22 2019
new/usr/src/uts/i86xpv/unix/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
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21 #
22 #
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24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 #
28 # This makefile drives the production of unix (and unix.o).
29 #
30 # i86xpv implementation architecture dependent
31 #
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../

38 #
39 # Define the module and object file sets.
40 #
41 UNIX = unix
42 DBOOT = dboot

44 OBJECTS = $(SPECIAL_OBJS:%=$(OBJS_DIR)/%) \
$(CORE_OBJS:%=$(OBJS_DIR)/%) \
$(KRTLD_OBJS:%=$(OBJS_DIR)/%) \
$(MACH_NOT_YET_KMODS:%=$(OBJS_DIR)/%)

49 LINTS = $(SPECIAL_OBJS:%.o=$(LINTS_DIR)/%.ln) \
$(CORE_OBJS:%.o=$(LINTS_DIR)/%.ln) \
$(KRTLD_OBJS:%.o=$(LINTS_DIR)/%.ln) \
$(MACH_NOT_YET_KMODS:%.o=$(LINTS_DIR)/%.ln) \
$(LINTS_DIR)/vers.ln \
$(LINTS_DIR)/modstubs.ln

56 ROOTMODULE = $(ROOT_PSM_KERN_DIR)/$(UNIX)

58 UNIX_BIN = $(OBJS_DIR)/$(UNIX)

60 LIBS = $(GENLIB)

```

```

62 GENUNIX = genunix
63 GENUNIX_DIR = ../../intel/$(GENUNIX)

65 LIBOPTS = -L $(GENUNIX_DIR)/$(OBJS_DIR) -l $(GENUNIX)

67 CTFFEXTRAOBJS = $(OBJS_DIR)/vers.o

69 DBOOT_OBJS_DIR = dboot/$(OBJS_DIR)
70 DBOOT_OBJECTS = $(DBOOT_OBJS:%=$(DBOOT_OBJS_DIR)/%)
71 DBOOT_BIN = $(DBOOT_OBJS_DIR)/$(DBOOT)
72 DBOOT_O = $(OBJS_DIR)/$(DBOOT).o
73 DBOOT_S = $(DBOOT_O:%.o=%.s)
74 DBOOT_LINTS = $(DBOOT_OBJS:%.o=$(DBOOT_OBJS_DIR)/%.ln)
75 DBOOT_LINT = $(LINT_$(MACH)_$(CLASS))

77 #
78 # Include common rules.
79 #
80 include $(UTSBASE)/i86xpv/Makefile.i86xpv

82 #
83 # Define targets
84 #
85 ALL_TARGET = $(UNIX_BIN)
86 LINT_TARGET = $(LINT_LIB) $(DBOOT_LINT_LIB)
87 INSTALL_TARGET = $(UNIX_BIN) $(ROOTMODULE)

89 #
90 # This is UNIX_DIR. Use a short path.
91 #
92 UNIX_DIR = .

94 #
95 # Overrides
96 #
97 CLEANFILES += \
98 $(UNIX_O) $(MODSTUBS_O) \
99 $(OBJS_DIR)/vers.c $(OBJS_DIR)/vers.o \
100 $(OBJS_DIR)/dtracestubs.s \
101 $(DTRACESTUBS_O) $(DTRACESTUBS)

103 CLEANFILES += \
104 $(DBOOT_O) $(DBOOT_S) \
105 $(DBOOT_OBJECTS) \
106 $(DBOOT_BIN)

108 CLEANFILES += \
109 $(DBOOT_OBJS_DIR)/$(VGATEXT_FONT).c \
110 $(OBJS_DIR)/$(VGATEXT_FONT).c

112 CLEANFILES += \
113 $(OBJS_DIR)/fb_sw_tch_src.o \
114 $(OBJS_DIR)/fb_sw_tch_src \
115 $(OBJS_DIR)/fb_sw_tch.s

117 CLEANFILES += \
118 $(ZLIB_OBJS:%.o=$(OBJS_DIR)/%.o) \
119 $(ZLIB_OBJS:%.o=$(OBJS_DIR)/%.ln)

121 CLOBBERFILES = $(CLEANFILES) $(UNIX_BIN)
122 CLEANLINTFILES += $(LINT_LIB) $(DBOOT_LINT_LIB) $(DBOOT_LINTS)

124 # instr_size needs a special header
125 $(OBJS_DIR)/instr_size.o := EXTRA_OPTIONS = -I$(SRC)/common/dis/i386
126 $(OBJS_DIR)/instr_size.ln := EXTRA_OPTIONS = -I$(SRC)/common/dis/i386

```

```

128 CFLAGS += -DDIS_MEM

130 #
131 # For now, disable these lint checks; maintainers should endeavor
132 # to investigate and remove these for maximum lint coverage.
133 # Please do not carry these forward to new Makefiles.
134 #
135 LINTTAGS      += -erroff=E_BAD_PTR_CAST_ALIGN
136 LINTTAGS      += -erroff=E_SUSPICIOUS_COMPARISON
137 LINTTAGS      += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
138 LINTTAGS      += -erroff=E_STATIC_UNUSED
139 LINTTAGS      += -erroff=E_PTRDIFF_OVERFLOW
140 LINTTAGS      += -erroff=E_ASSIGN_NARROW_CONV

142 CERRWARN      += -_gcc=-Wno-parentheses
143 CERRWARN      += -_gcc=-Wno-uninitialized
144 CERRWARN      += -_gcc=-Wno-char-subscripts
145 CERRWARN      += -_gcc=-Wno-unused-variable
146 CERRWARN      += -_gcc=-Wno-unused-function
147 CERRWARN      += -_gcc=-Wno-unused-label
148 CERRWARN      += -_gcc=-Wno-type-limits
149 CERRWARN      += -_gcc=-Wno-clobbered
150 CERRWARN      += -_gcc=-Wno-unused-value
151 CERRWARN      += -_gcc=-Wno-empty-body

153 # needs work
154 SMATCH=off

156 # Ensure that lint sees 'struct cpu' containing a fully declared
157 # embedded 'struct machcpu'
158 #
159 LINTFLAGS      += -D_MACHDEP -I../i86pc

161 #
162 #      Default build targets.
163 #
164 .KEEP_STATE:

166 def: $(DEF_DEPS)

168 all: $(ALL_DEPS)

170 clean: $(CLEAN_DEPS)

172 clobber: $(CLOBBER_DEPS)

174 lint: $(LINT_DEPS)

176 clean.lint: $(CLEAN_LINT_DEPS)

178 install: $(INSTALL_DEPS)

180 MAPFILE_32 = $(MAPFILE)
181 MAPFILE_64 = $(MAPFILE).amd64

183 MAPFILE_NAME = $(MAPFILE_$(CLASS))

185 $(UNIX_BIN): $(UNIX_O) $(MODSTUBS_O) $(MAPFILE_NAME) \
186               $(GENLIB) $(DTRACESTUBS) $(DBOOT_O)
187     $(LD) -dy -b -o $@ -e dboot_image -znointerp -M $(MAPFILE_NAME) \
188           $(UNIX_O) $(DBOOT_O) $(MODSTUBS_O) $(LIBOPTS) \
189           $(DTRACESTUBS)
190     $(CTFMERGE_UNIQIFY_AGAINST_GENUNIX)
191     $(POST_PROCESS)

193 $(UNIX_O): $(OBJECTS) $(OBJS_DIR)/vers.o

```

```

194     $(LD) -r -o $@ $(OBJECTS) $(OBJS_DIR)/vers.o

196 $(DBOOT_BIN): $(DBOOT_OBJS_DIR) $(DBOOT_OBJECTS) dboot/Mapfile.dboot
197     $(LD) -dn -e _start -M dboot/Mapfile.dboot \
198     -o $(DBOOT_BIN) $(DBOOT_OBJECTS)

200 $(DBOOT_O): $(DBOOT_BIN)
201     @echo ".data" > $(DBOOT_S)
202     @echo ".globl dboot_image" >> $(DBOOT_S)
203     @echo "dboot_image:" >> $(DBOOT_S)
204     $(ELFEXTRACT) $(DBOOT_BIN) >> $(DBOOT_S)
205     $(COMPILE.s) -o $(DBOOT_O) $(DBOOT_S)

207 $(DBOOT_OBJS_DIR):
208     -@mkdir -p $@ 2> /dev/null

210 #
211 #      Special rules for generating assym.h for inclusion in assembly files.
212 #
213 $(DSF_DIR)/$(OBJS_DIR)/assym.h $(DSF_DIR)/$(OBJS_DIR)/kdi_assym.h: FRC
214     @cd $(DSF_DIR); $(MAKE) all.targ

216 #
217 #      The global lint target builds the kernel lint library (llib-lunix.ln)
218 #      which is equivalent to a lint of /unix.o. Then all kernel modules for
219 #      this architecture are linted against the kernel lint library.
220 #
221 #      Note: lint errors in the kernel lint library will be repeated for
222 #      each module. It is important that the kernel lint library
223 #      be clean to keep the textual output to a reasonable level.
224 #

226 $(LINT_LIB): $(LINT_LIB_DIR) $(LINTS)
227     @pwd
228     @-$(ECHO) "\n$(UNIX): (library construction):"
229     @$$(LINT) -o$(UNIX) $(LINTFLAGS) $(LINTS)
230     @$$(MV) $@F $@

232 $(DBOOT_LINT_LIB): $(LINT_LIB_DIR) $(DBOOT_LINTS)
233     @pwd
234     @-$(ECHO) "\n$(DBOOT): (library construction):"
235     @$$(LINT) -o$(DBOOT) $(DBOOT_LINTFLAGS) $(DBOOT_LINTS)
236     @$$(MV) $@F $@

238 lintlib: $(LINT_DEPS)

240 #
241 #      Include common targets.
242 #
243 include $(UTSBASE)/i86xpv/Makefile.targ

```

```

*****
2140 Mon Jan 7 13:07:25 2019
new/usr/src/uts/i86xpv/xnb/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2010 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 #
28 # This makefile drives the production of the xnb
29 # network driver support module.
30 #
31 # i86xpv architecture dependent
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../..
37 #
38 # Define the module and object file sets.
39 #
40 #
41 #
42 #
43 #
44 include $(UTSBASE)/i86xpv/Makefile.i86xpv
45 #
46 #
47 # Define targets
48 #
49 ALL_TARGET = $(BINARY)
50 LINT_TARGET = $(MODULE).lint
51 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
52 #
53 #
54 # Overrides
55 #

```

```

56 DEF_BUILDS = $(DEF_BUILDS64)
57 ALL_BUILDS = $(ALL_BUILDS64)
58 #
59 #
60 # Module depends on mac.
61 #
62 LDFLAGS += -dy -N misc/mac
63 #
64 #
65 # use Solaris specific code in xen public header files
66 #
67 CFLAGS += -D_SOLARIS
68 LINTFLAGS += -D_SOLARIS
69 #
70 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
71 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
72 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
73 #
74 CERRWARN += -_gcc=-Wno-uninitialized
75 #
76 # needs work
77 SMOFF += signed
78 #
79 #
80 # Default build targets.
81 #
82 .KEEP_STATE:
83 #
84 def: $(DEF_DEPS)
85 #
86 all: $(ALL_DEPS)
87 #
88 clean: $(CLEAN_DEPS)
89 #
90 clobber: $(CLOBBER_DEPS)
91 #
92 lint: $(LINT_DEPS)
93 #
94 modlintlib: $(MODLINTLIB_DEPS)
95 #
96 clean.lint: $(CLEAN_LINT_DEPS)
97 #
98 install: $(INSTALL_DEPS)
99 #
100 #
101 # Include common targets.
102 #
103 include $(UTSBASE)/i86xpv/Makefile.targ

```

```

*****
2063 Mon Jan 7 13:07:27 2019
new/usr/src/uts/i86xpv/xnf/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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20 #
21 #
22 #
23 # uts/i86xpv/xnf/Makefile
24 #
25 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
26 # Use is subject to license terms.
27 #
28 # Copyright (c) 2018, Joyent, Inc.
29 # ident "%Z%M% %I% %E% SMI"
30 #
31 # This makefile drives the production of the xve
32 # network driver kernel module.
33 #
34 # i86xpv architecture dependent
35 #
36 #
37 #
38 #
39 #
40 #
41 #
42 #
43 #
44 #
45 #
46 #
47 #
48 #
49 #
50 #
51 #
52 #
53 #

```

```

54 #
55 #
56 # Driver depends on MAC & IP
57 #
58 # LDFLAGS += -dy -N misc/mac -N drv/ip
59 #
60 #
61 # use Solaris specific code in xen public header files
62 #
63 # CFLAGS += -D_SOLARIS
64 # LINTFLAGS += -D_SOLARIS
65 #
66 # LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
67 # LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
68 # LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
69 #
70 # needs work
71 # SMATCH=off
72 #
73 #
74 # Default build targets.
75 #
76 # .KEEP_STATE:
77 #
78 # def: $(DEF_DEPS)
79 #
80 # all: $(ALL_DEPS)
81 #
82 # clean: $(CLEAN_DEPS)
83 #
84 # clobber: $(CLOBBER_DEPS)
85 #
86 # lint: $(LINT_DEPS)
87 #
88 # modlintlib: $(MODLINTLIB_DEPS)
89 #
90 # clean.lint: $(CLEAN_LINT_DEPS)
91 #
92 # install: $(INSTALL_DEPS)
93 #
94 #
95 # Include common targets.
96 #
97 # include $(UTSBASE)/i86xpv/Makefile.targ

```

new/usr/src/uts/i86xpv/xpv\_psm/Makefile

1

```
*****
2366 Mon Jan 7 13:07:29 2019
new/usr/src/uts/i86xpv/xpv_psm/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # Copyright 2016 Joyent, Inc.
28 #
29 #
30 # This makefile drives the production of the xpv_psm mach
31 # kernel module.
32 #
33 # i86xpv implementation architecture dependent
34 #
35 #
36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTSBASE = ../../
40 #
41 #
42 # Define the module and object file sets.
43 #
44 MODULE = xpv_psm
45 OBJECTS = $(XPV_PSM_OBJS:%=$(OBJS_DIR)/%)
46 LINTS = $(XPV_PSM_OBJS:%.o=$(LINTS_DIR)/%.ln)
47 ROOTMODULE = $(ROOT_PSM_MACH_DIR)/$(MODULE)
48 #
49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/i86xpv/Makefile.i86xpv
53 #
54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
```

new/usr/src/uts/i86xpv/xpv\_psm/Makefile

2

```
61 #
62 # Overrides.
63 #
64 DEBUG_FLGS =
65 DEBUG_DEFS += $(DEBUG_FLGS)
66 #
67 #
68 #
69 # Depends on ACPI CA interpreter
70 #
71 LDFLAGS += -dy -N misc/acpica
72 #
73 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
74 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
75 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
76 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
77 #
78 CERRWARN += -_gcc=-Wno-type-limits
79 CERRWARN += -_gcc=-Wno-parentheses
80 CERRWARN += -_gcc=-Wno-uninitialized
81 CERRWARN += -_gcc=-Wno-empty-body
82 CERRWARN += -_gcc=-Wno-unused-function
83 #
84 # needs work
85 SMATCH=off
86 #
87 # Default build targets.
88 #
89 .KEEP_STATE:
90 #
91 def: $(DEF_DEPS)
92 #
93 all: $(ALL_DEPS)
94 #
95 clean: $(CLEAN_DEPS)
96 #
97 clobber: $(CLOBBER_DEPS)
98 #
99 lint: $(LINT_DEPS)
100 #
101 modlintlib: $(MODLINTLIB_DEPS)
102 #
103 clean.lint: $(CLEAN_LINT_DEPS)
104 #
105 install: $(INSTALL_DEPS)
106 #
107 #
108 # Include common targets.
109 #
110 include $(UTSBASE)/i86xpv/Makefile.targ
```

new/usr/src/uts/i86xpv/xpv\_uppc/Makefile

1

```
*****
2264 Mon Jan 7 13:07:32 2019
new/usr/src/uts/i86xpv/xpv_uppc/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
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9 # or http://www.opensolaris.org/os/licensing.
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18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
26 # Copyright 2016 Joyent, Inc.
27 #
28 # This makefile drives the production of the xpv_uppc mach
29 # kernel module.
30 #
31 # i86xpv implementation architecture dependent
32 #
33 #
34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTBASE = ../../
38 #
39 #
40 # Define the module and object file sets.
41 #
42 MODULE = xpv_uppc
43 OBJECTS = $(XPV_UPPC_OBJS:%=$(OBJS_DIR)/%)
44 LINTS = $(XPV_UPPC_OBJS:%.o=$(LINTS_DIR)/%.ln)
45 ROOTMODULE = $(ROOT_PSM_MACH_DIR)/$(MODULE)
46 #
47 #
48 # Include common rules.
49 #
50 include $(UTSBASE)/i86xpv/Makefile.i86xpv
51 #
52 #
53 # Define targets
54 #
55 ALL_TARGET = $(BINARY)
56 LINT_TARGET = $(MODULE).lint
57 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
58 #
59 #
60 # Overrides.
```

new/usr/src/uts/i86xpv/xpv\_uppc/Makefile

2

```
61 #
62 DEBUG_FLGS =
63 DEBUG_DEFS += $(DEBUG_FLGS)
64 #
65 CERRWARN += -_gcc=-Wno-unused-function
66 #
67 # needs work
68 $(OBJS_DIR)/psm_common.o := SMOFF += deref_check
69 #
70 #
71 #
72 # Depends on ACPI CA interpreter
73 #
74 LDFLAGS += -dy -N misc/acpica
75 #
76 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
77 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
78 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
79 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
80 #
81 # Default build targets.
82 #
83 .KEEP_STATE:
84 #
85 def: $(DEF_DEPS)
86 #
87 all: $(ALL_DEPS)
88 #
89 clean: $(CLEAN_DEPS)
90 #
91 clobber: $(CLOBBER_DEPS)
92 #
93 lint: $(LINT_DEPS)
94 #
95 modlintlib: $(MODLINTLIB_DEPS)
96 #
97 clean.lint: $(CLEAN_LINT_DEPS)
98 #
99 install: $(INSTALL_DEPS)
100 #
101 #
102 # Include common targets.
103 #
104 include $(UTSBASE)/i86xpv/Makefile.targ
```

```

*****
2145 Mon Jan 7 13:07:34 2019
new/usr/src/uts/intel/FSS/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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4 # The contents of this file are subject to the terms of the
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
25 # uts/intel/FSS/Makefile
26 #

27 #
28 # This makefile drives the production of the FSS scheduling class
29 # kernel module.
30 #
31 # intel architecture dependent
32 #

34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../../

39 #
40 # Define the module and object file sets.
41 #
42 MODULE = FSS
43 OBJECTS = $(FSS_OBJS:%=$(OBJS_DIR)/%)
44 LINTS = $(FSS_OBJS:%.o=$(LINTS_DIR)/%.ln)
45 ROOTMODULE = $(USR_SCHED_DIR)/$(MODULE)

47 #
48 # Include common rules.
49 #
50 include $(UTSBASE)/intel/Makefile.intel

52 #
53 # Define targets
54 #
55 ALL_TARGET = $(BINARY)
56 LINT_TARGET = $(MODULE).lint
57 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

59 #

```

```

60 # For now, disable these lint checks; maintainers should endeavor
61 # to investigate and remove these for maximum lint coverage.
62 # Please do not carry these forward to new Makefiles.
63 #
64 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

66 CERRWARN += -_gcc=-Wno-uninitialized

68 # needs work
69 $(OBJS_DIR)/fss.o := SMOFF += deref_check

71 #
72 # Default build targets.
73 #
74 .KEEP_STATE:

76 def: $(DEF_DEPS)

78 all: $(ALL_DEPS)

80 clean: $(CLEAN_DEPS)

82 clobber: $(CLOBBER_DEPS)

84 lint: $(LINT_DEPS)

86 modlintlib: $(MODLINTLIB_DEPS)

88 clean.lint: $(CLEAN_LINT_DEPS)

90 install: $(INSTALL_DEPS)

92 #
93 # Include common targets.
94 #
95 include $(UTSBASE)/intel/Makefile.targ

```



new/usr/src/uts/intel/aac/Makefile

1

```
*****
2212 Mon Jan 7 13:07:36 2019
new/usr/src/uts/intel/aac/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 #
28 #
29 # This makefile drives the production of the aac driver kernel module.
30 #
31 # intel implementation architecture dependent
32 #
33 #
34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../../
38 #
39 # Define the module and object file sets.
40 #
41 #
42 MODULE = aac
43 OBJECTS = $(AAC_OBJS:%=$(OBJS_DIR)/%)
44 LINTS = $(AAC_OBJS:%.o=$(LINTS_DIR)/%.ln)
45 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
46 CONF_SRCDIR = $(UTSBASE)/common/io/aac
47 #
48 #
49 # Include common rules.
50 #
51 include $(UTSBASE)/intel/Makefile.intel
52 #
53 #
54 # Define targets
55 #
56 ALL_TARGET = $(BINARY) $(CONFMOD)
57 LINT_TARGET = $(MODULE).lint
58 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
59 #
60 #
61 # Kernel Module Dependencies
```

new/usr/src/uts/intel/aac/Makefile

2

```
62 #
63 LDFLAGS += -dy -Nmisc/scsi
64 #
65 #
66 # Overrides
67 #
68 #
69 CERRWARN += -_gcc=-Wno-uninitialized
70 CERRWARN += -_gcc=-Wno-parentheses
71 CERRWARN += -_gcc=-Wno-unused-value
72 CERRWARN += -_gcc=-Wno-unused-label
73 CERRWARN += -_gcc=-Wno-switch
74 #
75 # needs work
76 $(OBJS_DIR)/aac.o := SMOFF += shift_to_zero
77 #
78 #
79 # Default build targets.
80 #
81 .KEEP_STATE:
82 #
83 def: $(DEF_DEPS)
84 #
85 all: $(ALL_DEPS)
86 #
87 clean: $(CLEAN_DEPS)
88 #
89 clobber: $(CLOBBER_DEPS)
90 #
91 lint: $(LINT_DEPS)
92 #
93 modlintlib: $(MODLINTLIB_DEPS)
94 #
95 clean.lint: $(CLEAN_LINT_DEPS)
96 #
97 install: $(INSTALL_DEPS)
98 #
99 #
100 # Include common targets.
101 #
102 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/ac97/Makefile

1

```
*****
1825 Mon Jan 7 13:07:39 2019
new/usr/src/uts/intel/ac97/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/ac97/Makefile
23 #
24 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the ac97 support module.
28 #
30 #
31 # Path to the base of the uts directory tree (usually /usr/src/uts).
32 #
33 UTSBASE = ../../

35 #
36 # Define the module and object file sets.
37 #
38 MODULE = ac97
39 OBJECTS = $(AC97_OBJS:%=$(OBJDIR)/%)
40 LINTS = $(AC97_OBJS:%.o=$(LINTSDIR)/%.ln)
41 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)

43 #
44 # Include common rules.
45 #
46 include $(UTSBASE)/intel/Makefile.intel

48 #
49 # Define targets
50 #
51 ALL_TARGET = $(BINARY)
52 LINT_TARGET = $(MODULE).lint
53 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

55 LDFLAGS += -dy -Ndrv/audio

57 # needs work
58 $(OBJDIR)/ac97.o := SMOFF += index_overflow

60 #
```

new/usr/src/uts/intel/ac97/Makefile

2

```
61 # Default build targets.
62 #
63 .KEEP_STATE:

65 def: $(DEF_DEPS)

67 all: $(ALL_DEPS)

69 clean: $(CLEAN_DEPS)

71 clobber: $(CLOBBER_DEPS)

73 lint: $(LINT_DEPS)

75 modlintlib: $(MODLINTLIB_DEPS)

77 clean.lint: $(CLEAN_LINT_DEPS)

79 install: $(INSTALL_DEPS)

81 #
82 # Include common targets.
83 #
84 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
1678 Mon Jan 7 13:07:41 2019
new/usr/src/uts/intel/acpica/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
3 # Use is subject to license terms.
4 #
5 # Copyright (c) 2018, Joyent, Inc.
5 # Copyright 2016 Joyent, Inc.
6 #
7 #
8 # This makefile drives the production of the ACPI CA services
9 # kernel module.
10 #
11 # intel architecture dependent
12 #
14 #
15 # Path to the base of the uts directory tree (usually /usr/src/uts).
16 #
17 UTBASE = ../../

19 #
20 # Define the module and object file sets.
21 #
22 MODULE = acpica
23 OBJECTS = $(ACPICA_OBJS:%=$(OBJDIR)/%)
24 LINTS = $(ACPICA_OBJS:%.o=$(LINTSDIR)/%.ln)
25 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
26 INC_PATH += -I$(UTSBASE)/intel/sys/acpi
27 INC_PATH += -I$(UTSBASE)/i86pc
28 INC_PATH += -I$(SRC)/common

30 #
31 # Include common rules.
32 #
33 include $(UTSBASE)/intel/Makefile.intel

35 #
36 # Define targets
37 #
38 ALL_TARGET = $(BINARY) $(CONFMOD)
39 LINT_TARGET = $(MODULE).lint
40 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

42 #
43 # Overrides.
44 #
45 DEBUG_DEFS += $(DEBUG_FLGS)

47 #
48 # lint pass one non-enforcement
49 #
50 CFLAGS += $(CCVERBOSE) -DPWRDMN -DACPI_USE_LOCAL_CACHE -DACPI_DEBUG_OUTPUT

52 #
53 # 3rd party code is not lint clean
54 #
55 CERRWARN += -erroff=E_STATEMENT_NOT_REACHED

57 SMOFF += all_func_returns

59 LINTFLAGS += -errchk=%none
60 LINTFLAGS += -errhdr=%none

```

```

61 LINTFLAGS += -erroff=%all
62 LINTFLAGS += -errwarn=%none

64 CERRWARN += -_gcc=-Wno-unused-variable
65 CERRWARN += -_gcc=-Wno-parentheses
66 CERRWARN += -_gcc=-Wno-uninitialized
67 CERRWARN += -_gcc=-Wno-unused-function

69 #
70 # Default build targets.
71 #
72 .KEEP_STATE:

74 def: $(DEF_DEPS)

76 all: $(ALL_DEPS)

78 clean: $(CLEAN_DEPS)

80 clobber: $(CLOBBER_DEPS)

82 lint: $(LINT_DEPS)

84 modlintlib: $(MODLINTLIB_DEPS)

86 clean.lint: $(CLEAN_LINT_DEPS)

88 install: $(INSTALL_DEPS)

90 #
91 # Include common targets.
92 #
93 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/agpgart/Makefile

1

\*\*\*\*\*

1463 Mon Jan 7 13:07:44 2019

new/usr/src/uts/intel/agpgart/Makefile

10077 usr/src/uts/ Makefile changes for smatch

\*\*\*\*\*

```
1 #
2 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
3 # Use is subject to license terms.
4 #
5 # Copyright (c) 2018, Joyent, Inc.
5 # uts/intel/agpgart/Makefile
6 #

7 #
8 # This makefile drives the framework of agp protocol
9 # (agpgart) kernel module.
10 #
11 #
12 # Path to the base of the uts directory tree (usually /usr/src/uts).
13 #
14 UTSBASE = ../../

16 #
17 # Define the module and object file sets.
18 #
19 MODULE = agpgart
20 OBJECTS = $(AGPGART_OBJS:%=$(OBJDIR)/%)
21 LINTS = $(AGPGART_OBJS:%.o=$(LINTSDIR)/%.ln)
22 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
23 CONF_SRCDIR = $(UTSBASE)/intel/io/agpgart

25 #
26 # Include common rules.
27 #
28 include $(UTSBASE)/intel/Makefile.intel

30 #
31 # Define targets
32 #
33 ALL_TARGET = $(BINARY) $(SRC_CONFILE)
34 LINT_TARGET = $(MODULE).lint
35 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

37 CERRWARN += _gcc=-Wno-switch
38 CERRWARN += _gcc=-Wno-parentheses
39 CERRWARN += _gcc=-Wno-uninitialized

41 # needs work
42 $(OBJDIR)/agpgart.o := SMOFF += deref_check

44 #
45 # For now, disable these lint checks; maintainers should endeavor
46 # to investigate and remove these for maximum lint coverage.
47 # Please do not carry these forward to new Makefiles.
48 #
49 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN

51 #
52 # Default build targets.
53 #
54 .KEEP_STATE:

56 def: $(DEF_DEPS)

58 all: $(ALL_DEPS)
```

new/usr/src/uts/intel/agpgart/Makefile

2

```
60 clean: $(CLEAN_DEPS)

62 clobber: $(CLOBBER_DEPS)

64 lint: $(LINT_DEPS)

66 modlintlib: $(MODLINTLIB_DEPS)

68 clean.lint: $(CLEAN_LINT_DEPS)

70 install: $(INSTALL_DEPS)

72 #
73 # Include common targets.
74 #
75 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/amd8111s/Makefile

1

\*\*\*\*\*

1858 Mon Jan 7 13:07:47 2019

new/usr/src/uts/intel/amd8111s/Makefile

10077 usr/src/uts/ Makefile changes for smatch

\*\*\*\*\*

```
1 #
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
26 #
27 # This makefile drives the production of the AMD8111
28 # Fast Ethernet (amd8111s) driver module in intel systems
29 #
30 #
31 # Path to the base of the uts directory tree (usually /usr/src/uts).
32 #
33 # Define the module and object file sets.
34 #
35 MODULE = amd8111s
36 OBJECTS = $(AMD8111S_OBJS:%=$(OBJS_DIR)/%)
37 LINTS = $(AMD8111S_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
39 #
40 #
41 # Include common rules.
42 #
43 include $(UTSBASE)/intel/Makefile.intel
44 #
45 # Define targets
46 #
47 #
48 ALL_TARGET = $(BINARY)
49 LINT_TARGET = $(MODULE).lint
50 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
51 #
52 #
53 # Overrides
54 #
55 #
56 #
```

new/usr/src/uts/intel/amd8111s/Makefile

2

```
57 # Driver depends on GLDv3
58 #
59 LDFLAGS += -dy -N misc/mac
```

```
61 # seems wrong
62 $(OBJS_DIR)/amd8111s_hw.o := SMOFF += precedence
```

```
64 #
65 # Default build targets.
66 #
67 .KEEP_STATE:
68 #
69 def: $(DEF_DEPS)
70 #
71 all: $(ALL_DEPS)
72 #
73 clean: $(CLEAN_DEPS)
74 #
75 clobber: $(CLOBBER_DEPS)
76 #
77 lint: $(LINT_DEPS)
78 #
79 modlintlib: $(MODLINTLIB_DEPS)
80 #
81 clean.lint: $(CLEAN_LINT_DEPS)
82 #
83 install: $(INSTALL_DEPS)
84 #
85 #
86 # Include common targets.
87 #
88 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2037 Mon Jan 7 13:07:49 2019
new/usr/src/uts/intel/arcmsr/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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4 # The contents of this file are subject to the terms of the
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
22 # Use is subject to license terms.
23 #
24 # Copyright (c) 2018, Joyent, Inc.

26 #
27 # Paths to the base of the uts directory trees
28 #
29 UTSBASE = ../../

31 #
32 # Define the module and object file sets.
33 #
34 MODULE = arcmsr
35 OBJECTS = $(ARCMSR_OBJS:%=$(OBJDIR)/%)
36 LINTS = $(ARCMSR_OBJS:%.o=$(LINTSDIR)/%.ln)
37 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
38 CONF_SRCDIR = $(UTSBASE)/intel/io/scsi/adapters/arcmsr

40 #
41 # Include common rules.
42 #
43 include $(UTSBASE)/intel/Makefile.intel

45 CERRWARN += -_gcc=-Wno-switch
46 CERRWARN += -_gcc=-Wno-unused-label
47 CERRWARN += -_gcc=-Wno-parentheses
48 CERRWARN += -_gcc=-Wno-uninitialized

50 # needs work
51 $(OBJDIR)/arcmsr.o := SMOFF += deref_check

53 #
54 # Define targets.
55 #
56 ALL_TARGET = $(BINARY) $(CONFMOD)
57 LINT_TARGET = $(MODULE).lint
58 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

60 #
61 # Note dependency on misc/scsi.

```

```

62 #
63 LDFLAGS += -dy -N"misc/scsi"

65 #
66 # Default build targets.
67 #
68 .KEEP_STATE:

70 def: $(DEF_DEPS)

72 all: $(ALL_DEPS)

74 clean: $(CLEAN_DEPS)

76 clobber: $(CLOBBER_DEPS)

78 lint: $(LINT_DEPS)

80 modlintlib: $(MODLINTLIB_DEPS)

82 clean.lint: $(CLEAN_LINT_DEPS)

84 install: $(INSTALL_DEPS)

86 #
87 # Include common targets.
88 #
89 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2218 Mon Jan 7 13:07:49 2019
new/usr/src/uts/intel/arn/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
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25 # Copyright (c) 2018, Joyent, Inc.

28 #
29 # This file makes the atheros IEEE 802.11n driver for an intel system
30 #
31 # intel architecture dependent
32 #

34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../..
38 #
39 # Define the module and object file sets.
40 #
41 MODULE = arn
42 OBJECTS = ${ARN_OBJS:%=${OBJDIR}/%}
43 LINTS = ${ARN_OBJS:%.o=${LINTSDIR}/%.ln}
44 ROOTMODULE = ${ROOT_DRV_DIR}/${MODULE}

46 #
47 # Include common rules.
48 #
49 include ${UTSBASE}/intel/Makefile.intel

51 #
52 # Define targets
53 #
54 ALL_TARGET = ${BINARY}
55 LINT_TARGET = ${MODULE}.lint
56 INSTALL_TARGET = ${BINARY} ${ROOTMODULE}

58 #
59 # Driver depends on GLDv3 & wifi kernel support module.
60 #
61 LDFLAGS += -dy -Nmisc/mac -Nmisc/net80211

```

```

63 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
65 CERRWARN += -_gcc=-Wno-unused-variable
66 CERRWARN += -_gcc=-Wno-uninitialized
67 CERRWARN += -_gcc=-Wno-char-subscripts

69 # various issues
70 SMATCH=off

72 #
73 # Default build targets.
74 #
75 .KEEP_STATE:

77 def: $(DEF_DEPS)

79 all: $(ALL_DEPS)

81 clean: $(CLEAN_DEPS)

83 clobber: $(CLOBBER_DEPS)

85 lint: $(LINT_DEPS)

87 modlintlib: $(MODLINTLIB_DEPS)

89 clean.lint: $(CLEAN_LINT_DEPS)

91 install: $(INSTALL_DEPS)

93 #
94 # Include common targets.
95 #
96 include ${UTSBASE}/intel/Makefile.targ

98 #
99 # If you have any special case that general
100 # Makefile rules don't serve for you, just do
101 # it yourself.
102 #

```

new/usr/src/uts/intel/arp/Makefile

1

```
*****
2568 Mon Jan 7 13:07:50 2019
new/usr/src/uts/intel/arp/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
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5 # Common Development and Distribution License (the "License").
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/arp/Makefile
23 #
24 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
28 # This makefile drives the production of the arp driver kernel module.
29 # intel implementation architecture dependent
30 #
31 #
32 # Path to the base of the uts directory tree (usually /usr/src/uts).
33 #
34 # UTSBASE = ../../
35 #
36 # Define the module and object file sets.
37 #
38 # MODULE = arp
39 # OBJECTS = $(ARP_OBJS:%=$(OBJS_DIR)/%)
40 # LINTS = $(ARP_OBJS:%.o=$(LINTS_DIR)/%.ln)
41 # ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
42 # ROOTLINK = $(ROOT_STRMOD_DIR)/$(MODULE)
43 # CONF_SRCDIR = $(UTSBASE)/common/inet/arp
44 #
45 # Extra for $(MODULE).check target
46 #
47 # Need to remove ipddi.o since it has non-static defines for _init etc.
48 # IP_CHECK_OBJS = $(IP_OBJS:ipddi.o=ip.o)
49 # EXTRA_CHECK_OBJS = $(IP_CHECK_OBJS:%=../ip/$(OBJS_DIR)/%)
50 #
51 #
52 # Include common rules.
53 #
54 # include $(UTSBASE)/intel/Makefile.intel
55 #
56 #
57 # Define targets
```

new/usr/src/uts/intel/arp/Makefile

2

```
58 #
59 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
60 LINT_TARGET = $(MODULE).lint
61 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTLINK) $(ROOT_CONFFILE)
62 #
63 #
64 # depends on ip
65 #
66 LDFLAGS += -dy -Ndrv/ip
67 #
68 #
69 # For now, disable these lint checks; maintainers should endeavor
70 # to investigate and remove these for maximum lint coverage.
71 # Please do not carry these forward to new Makefiles.
72 #
73 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
74 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
75 #
76 # false positive
77 SMOFF += index_overflow
78 #
79 #
80 # Default build targets.
81 #
82 .KEEP_STATE:
83 #
84 def: $(DEF_DEPS)
85 #
86 all: $(ALL_DEPS) $(SISCHECK_DEPS)
87 #
88 clean: $(CLEAN_DEPS) $(SISCLEAN_DEPS)
89 #
90 clobber: $(CLOBBER_DEPS) $(SISCLEAN_DEPS)
91 #
92 lint: $(LINT_DEPS)
93 #
94 modlintlib: $(MODLINTLIB_DEPS)
95 #
96 clean.lint: $(CLEAN_LINT_DEPS)
97 #
98 install: $(INSTALL_DEPS) $(SISCHECK_DEPS)
99 #
100 $(ROOTLINK): $(ROOT_STRMOD_DIR) $(ROOTMODULE)
101 -$(RM) $@; ln $(ROOTMODULE) $@
102 #
103 #
104 # Include common targets.
105 #
106 include $(UTSBASE)/intel/Makefile.targ
```



new/usr/src/uts/intel/ath/Makefile

1

```
*****
2763 Mon Jan 7 13:07:50 2019
new/usr/src/uts/intel/ath/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
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20 #
21 #
22 # Copyright 2010 Sun Microsystems, Inc. All rights reserved.
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24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # This file makes the atheros driver for an intel system
29 #
30 # intel architecture dependent
31 #

33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../
37 #
38 # Define the module and object file sets.
39 #
40 MODULE = ath
41 OBJECTS = $(ATH_OBJS:%=$(OBJS_DIR)/%) $(OBJS_DIR)/hal.o
42 LINTS = $(LINTS_DIR)/ath_aux.ln \
43 $(LINTS_DIR)/ath_main.ln \
44 $(LINTS_DIR)/ath_osdep.ln \
45 $(LINTS_DIR)/ath_rate.ln

47 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

61 #
```

new/usr/src/uts/intel/ath/Makefile

2

```
62 # Driver depends on GLDv3 & wifi kernel support module.
63 #
64 LDFFLAGS += -dy -Nmisc/mac -Nmisc/net80211

66 CERRWARN += -_gcc=-Wno-type-limits
67 CERRWARN += -_gcc=-Wno-unused-variable
68 CERRWARN += -_gcc=-Wno-empty-body

70 # needs work
71 $(OBJS_DIR)/ath_rate.o := SMOFF += index_overflow

73 #
74 # Default build targets.
75 #
76 .KEEP_STATE:

78 def: $(DEF_DEPS)

80 all: $(ALL_DEPS)

82 clean: $(CLEAN_DEPS)

84 clobber: $(CLOBBER_DEPS)

86 lint: $(LINT_DEPS)

88 modlintlib: $(MODLINTLIB_DEPS)

90 clean.lint: $(CLEAN_LINT_DEPS)

92 install: $(INSTALL_DEPS)

94 #
95 # Include common targets.
96 #
97 include $(UTSBASE)/intel/Makefile.targ

99 #
100 # If you have any special case that general
101 # Makefile rules don't serve for you, just do
102 # it yourself.
103 #
104 # The amd64 version of this object has the .eh_frame section tagged
105 # as SHT_PROGBITS, while the ABI requires SHT_AMD64_UNWIND. The Solaris
106 # ld enforces this, so use elfedit to bring the object in line with
107 # this requirement.
108 #
109 ATHEROS_HAL=$(UTSBASE)/common/io/ath/hal_x86_$(CLASS).o.uu
110 $(OBJS_DIR)/hal.o: $(ATHEROS_HAL)
111 uudecode -o @ $(ATHEROS_HAL)
112 if [ `elfedit -r -e 'ehdr:e_machine' @$ = EM_AMD64 ]; \
113 then elfedit -e 'shdr:sh_type .eh_frame SHT_AMD64_UNWIND' @$; fi
```

new/usr/src/uts/intel/atu/Makefile

1

```
*****
2138 Mon Jan 7 13:07:51 2019
new/usr/src/uts/intel/atu/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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20 #
21 #
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24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # This makefile drives the production of the atu driver kernel module.
29 #
30 # i86pc architecture dependent
31 #

33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../

38 #
39 # Define the module and object file sets.
40 #
41 MODULE = atu
42 OBJECTS = $(ATU_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(ATU_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

46 #
47 # Include common rules.
48 #
49 include $(UTSBASE)/intel/Makefile.intel

51 #
52 # Define targets
53 #
54 ALL_TARGET = $(BINARY)
55 LINT_TARGET = $(MODULE).lint
56 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

58 #
59 # Driver depends on GLDv3 & wifi kernel support module.
60 #
61 LDFLAGS += -dy -Nmisc/mac -Nmisc/net80211 -Nmisc/usba
```

new/usr/src/uts/intel/atu/Makefile

2

```
63 CERRWARN += -_gcc=-Wno-switch

65 # needs work
66 $(OBJS_DIR)/atu.o := SMOFF += all_func_returns

68 #
69 # Default build targets.
70 #
71 .KEEP_STATE:

73 def: $(DEF_DEPS)

75 all: $(ALL_DEPS)

77 clean: $(CLEAN_DEPS)

79 clobber: $(CLOBBER_DEPS)

81 lint: $(LINT_DEPS)

83 modlintlib: $(MODLINTLIB_DEPS)

85 clean.lint: $(CLEAN_LINT_DEPS)

87 install: $(INSTALL_DEPS)

89 #
90 # Include common targets.
91 #
92 include $(UTSBASE)/intel/Makefile.targ

94 #
95 # If you have any special case that general
96 # Makefile rules don't serve for you, just do
97 # it yourself.
98 #
```

new/usr/src/uts/intel/audio/Makefile

1

```
*****
1920 Mon Jan 7 13:07:51 2019
new/usr/src/uts/intel/audio/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/audio/Makefile
23 #
24 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the audio support module.
28 #

29 #
30 # Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 UTSBASE = ../../

34 #
35 # Define the module and object file sets.
36 #
37 MODULE = audio
38 OBJECTS = $(AUDIO_OBJS:%=$(OBJS_DIR)/%)
39 LINTS = $(AUDIO_OBJS:%.o=$(LINTS_DIR)/%.ln)
40 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
41 CONF_SRCDIR = $(UTSBASE)/common/io/audio/impl

43 #
44 # Include common rules.
45 #
46 include $(UTSBASE)/intel/Makefile.intel

48 CERRWARN += _gcc=-Wno-uninitialized

50 # needs work
51 $(OBJS_DIR)/audio_grc3.o := SMOFF += index_overflow

53 #
54 # Define targets
55 #
56 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
57 LINT_TARGET = $(MODULE).lint
58 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
```

new/usr/src/uts/intel/audio/Makefile

2

```
60 #
61 # Default build targets.
62 #
63 .KEEP_STATE:

65 def: $(DEF_DEPS)
67 all: $(ALL_DEPS)
69 clean: $(CLEAN_DEPS)
71 clobber: $(CLOBBER_DEPS)
73 lint: $(LINT_DEPS)
75 modlintlib: $(MODLINTLIB_DEPS)
77 clean.lint: $(CLEAN_LINT_DEPS)
79 install: $(INSTALL_DEPS)

81 #
82 # Include common targets.
83 #
84 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/audioemu10k/Makefile

1

```
*****
2839 Mon Jan 7 13:07:51 2019
new/usr/src/uts/intel/audioemu10k/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
```

```
1 #
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/audioemu10k/Makefile
23 #
24 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
28 #   This makefile drives the production of the audioemu10k driver.
29 #
30 #   Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 UTSBASE = ../../
33 #
34 #
35 #   Define the module and object file sets.
36 #
37 MODULE      = audioemu10k
38 OBJECTS     = $(AUDIOEMU10K_OBJS:%=$(OBJS_DIR)/%)
39 LINTS       = $(AUDIOEMU10K_OBJS:%.o=$(LINTS_DIR)/%.ln)
40 ROOTMODULE  = $(ROOT_DRV_DIR)/$(MODULE)
41 DSP_SRCDIR  = $(UTSBASE)/common/io/audio/drv/audioemu10k/dsp
42 DSP_HNAMES  = emul0k_gpr.h emul0k1_dsp.h emul0k2_dsp.h
43 DSP_HDRS    = $(DSP_HNAMES:%=$(OBJS_DIR)/%)
44 DSP_SNAMES  = emul0k.dsp emul0k1.mac emul0k2.mac
45 DSP_SRCS   = $(DSP_SNAMES:%=$(DSP_SRCDIR)/%)
46 ASM10K     = $(OBJS_DIR)/asm10k
47 #
48 #
49 #   Include common rules.
50 #
51 include $(UTSBASE)/intel/Makefile.intel
52 #
53 #
54 #   Define targets
55 #
56 ALL_TARGET = $(BINARY)
57 LINT_TARGET = $(MODULE).lint
58 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
```

new/usr/src/uts/intel/audioemu10k/Makefile

2

```
60 LDFLAGS      += -dy -Ndrv/audio -Nmisc/ac97
62 CERRWARN     += _gcc=-Wno-uninitialized
63 #
64 # needs work
65 SMOFF += all_func_returns
66 #
67 $(OBJS_DIR)/%.o      := CPPFLAGS += -I$(OBJS_DIR)
68 $(OBJS_DIR)/%.ln    := CPPFLAGS += -I$(OBJS_DIR)
69 $(OBJS_DIR)/emul0k_gpr.h := ASM10KFLAGS = -v -0 -P gpr
70 $(OBJS_DIR)/emul0k1_dsp.h := ASM10KFLAGS = -v -1 -P emul0k1
71 $(OBJS_DIR)/emul0k2_dsp.h := MODEL10K = SBLIVE
72 $(OBJS_DIR)/emul0k2_dsp.h := ASM10KFLAGS = -v -2 -P emul0k2
73 $(OBJS_DIR)/emul0k2_dsp.h := MODEL10K = AUDIGY
74 #
75 #
76 #   Default build targets.
77 #
78 .KEEP_STATE:
79 #
80 def:          $(DEF_DEPS)
81 #
82 all:          $(ALL_DEPS)
83 #
84 clean:        $(CLEAN_DEPS)
85 #
86 clobber:      $(CLOBBER_DEPS)
87 #
88 lint:         $(LINT_DEPS)
89 #
90 modlintlib:   $(MODLINTLIB_DEPS)
91 #
92 clean.lint:   $(CLEAN_LINT_DEPS)
93 #
94 install:      $(INSTALL_DEPS)
95 #
96 $(BINARY):   $(OBJS_DIR)/asm10k $(DSP_HDRS)
97 #
98 $(ASM10K):    $(DSP_SRCDIR)/asm10k.c
99              $(NATIVEECC) $(NATIVE_CFLAGS) -o $@ $(DSP_SRCDIR)/asm10k.c
100 #
101 $(DSP_HDRS): $(ASM10K) $(DSP_SRCS)
102              $(CPP) -D$(MODEL10K) -I$(DSP_SRCDIR) $(DSP_SRCDIR)/emul0k.dsp | \
103              $(OBJS_DIR)/asm10k $(ASM10KFLAGS) -h $@
104 #
105 CLOBBERFILES += $(ASM10K) $(DSP_HDRS)
106 #
107 #
108 #   Include common targets.
109 #
110 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
1861 Mon Jan 7 13:07:52 2019
new/usr/src/uts/intel/audiosolo/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/audiosolo/Makefile
23 #
24 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the audiosolo driver.
28 #

29 #
30 # Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 UTSBASE = ../../

34 #
35 # Define the module and object file sets.
36 #
37 MODULE = audiosolo
38 OBJECTS = $(AUDIOSOLO_OBJS:%=$(OBJDIR)/%)
39 LINTS = $(AUDIOSOLO_OBJS:%.o=$(LINTSDIR)/%.ln)
40 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel

47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

54 LDFLAGS += -dy -Ndrv/audio

56 CERRWARN += -_gcc=-Wno-uninitialized

58 # needs work
59 SMOFF += assign_vs_compare

```

```

61 #
62 # Default build targets.
63 #
64 .KEEP_STATE:

66 def: $(DEF_DEPS)

68 all: $(ALL_DEPS)

70 clean: $(CLEAN_DEPS)

72 clobber: $(CLOBBER_DEPS)

74 lint: $(LINT_DEPS)

76 modlintlib: $(MODLINTLIB_DEPS)

78 clean.lint: $(CLEAN_LINT_DEPS)

80 install: $(INSTALL_DEPS)

82 #
83 # Include common targets.
84 #
85 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2459 Mon Jan 7 13:07:52 2019
new/usr/src/uts/intel/avl1394/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
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24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # Path to the base of the uts directory tree.
29 #
30 UTSBASE = ../../

32 #
33 # Define the module and object file sets.
34 #
35 MODULE = avl1394
36 OBJECTS = $(AV1394_OBJS:%=$(OBJS_DIR)/%)
37 LINTS = $(AV1394_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

40 #
41 # Include common rules.
42 #
43 include $(UTSBASE)/intel/Makefile.intel

45 #
46 # Define targets
47 #
48 ALL_TARGET = $(BINARY)
49 LINT_TARGET = $(MODULE).lint
50 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

52 #
53 # Overrides
54 #
55 DEBUG_DEFS_DBG32 += -DTNF_DEBUG

57 #
58 # lint pass one enforcement
59 #
60 CFLAGS += $(CCVERBOSE)

```

```

62 #
63 # For now, disable these lint checks; maintainers should endeavor
64 # to investigate and remove these for maximum lint coverage.
65 # Please do not carry these forward to new Makefiles.
66 #
67 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
68 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
69 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
70 LINTTAGS += -erroff=E_STATIC_UNUSED
71 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
72 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

74 CERRWARN += -_gcc=-Wno-uninitialized
75 CERRWARN += -_gcc=-Wno-parentheses
76 CERRWARN += -_gcc=-Wno-type-limits

78 # needs work
79 $(OBJS_DIR)/avl1394_isoch.o := SMOFF += signed

81 #
82 # depends on misc/s1394
83 LDFLAGS += -dy -Nmisc/s1394

85 # Default build targets.
86 #
87 .KEEP_STATE:

89 def: $(DEF_DEPS)

91 all: $(ALL_DEPS)

93 clean: $(CLEAN_DEPS)

95 clobber: $(CLOBBER_DEPS)

97 lint: $(LINT_DEPS)

99 modlintlib: $(MODLINTLIB_DEPS)

101 clean.lint: $(CLEAN_LINT_DEPS)

103 install: $(INSTALL_DEPS)

105 #
106 # Include common targets.
107 #
108 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2412 Mon Jan 7 13:07:53 2019
new/usr/src/uts/intel/bge/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
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20 #
21 #
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25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # This makefile drives the production of the Broadcom BCM57xx
29 # Gigabit Ethernet (BGE) driver module in intel systems
30 #

32 #
33 # Path to the base of the uts directory tree (usually /usr/src/uts).
34 #
35 UTSBASE = ../../

37 #
38 # Define the module and object file sets.
39 #
40 MODULE = bge
41 OBJECTS = $(BGE_OBJS:%=$(OBJSDIR)/%)
42 LINTS = $(LINTSDIR)/bge_lint.ln
43 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
44 CONF_SRCDIR = $(UTSBASE)/common/io/bge

46 #
47 # Include common rules.
48 #
49 include $(UTSBASE)/intel/Makefile.intel

51 #
52 # Define targets
53 #
54 ALL_TARGET = $(BINARY)
55 LINT_TARGET = $(MODULE).lint
56 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

58 #
59 # Overrides
60 #
61 #

```

```

62 # For now, disable these lint checks; maintainers should endeavor
63 # to investigate and remove these for maximum lint coverage.
64 # Please do not carry these forward to new Makefiles.
65 #
66 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN

68 CERRWARN += -_gcc=-Wno-uninitialized
69 CERRWARN += -_gcc=-Wno-switch
70 CERRWARN += -_gcc=-Wno-parentheses
71 CERRWARN += -_gcc=-Wno-unused-variable
72 CERRWARN += -_gcc=-Wno-unused-function

74 # 3rd party source
75 SMOFF += all_func_returns

77 #
78 # Driver depends on MAC
79 #
80 LDFLAGS += -dy -N misc/mac

82 #
83 # Default build targets.
84 #
85 .KEEP_STATE:

87 def: $(DEF_DEPS)

89 all: $(ALL_DEPS)

91 clean: $(CLEAN_DEPS)

93 clobber: $(CLOBBER_DEPS)

95 lint: $(LINT_DEPS)

97 modlintlib: $(MODLINTLIB_DEPS)

99 clean.lint: $(CLEAN_LINT_DEPS)

101 install: $(INSTALL_DEPS)

103 #
104 # Include common targets.
105 #
106 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/blowfish/Makefile

1

```
*****
2522 Mon Jan 7 13:07:54 2019
new/usr/src/uts/intel/blowfish/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
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22 #
23 # Copyright 2014 Garrett D'Amore <garrett@damore.org>
24 #
25 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
26 # Use is subject to license terms.
27 #
28 #
29 #
30 # This makefile drives the production of the Blowfish KEF provider.
31 #
32 # intel implementation architecture dependent
33 #
34 #
35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../
39 COM_DIR = $(COMMONBASE)/crypto
40 #
41 #
42 # Define the module and object file sets.
43 #
44 MODULE = blowfish
45 OBJECTS = $(BLOWFISHPROV_OBJS:%=$(OBJS_DIR)/%)
46 LINTS = $(BLOWFISHPROV_OBJS:%.o=$(LINTS_DIR)/%.ln)
47 ROOTMODULE = $(ROOT_CRYPTODIR)/$(MODULE)
48 #
49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel
53 #
54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
60 #
61 #
```

new/usr/src/uts/intel/blowfish/Makefile

2

```
62 # Linkage dependencies
63 #
64 LDFLAGS += -dy -Nmisc/kcf
65 #
66 CPPFLAGS += -I$(COM_DIR)
67 CPPFLAGS += -DCRYPTO_PROVIDER_NAME=\"$(MODULE)\"
68 #
69 #
70 # For now, disable these lint checks; maintainers should endeavor
71 # to investigate and remove these for maximum lint coverage.
72 # Please do not carry these forward to new Makefiles.
73 #
74 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
75 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
76 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
77 #
78 CERRWARN += -_gcc=-Wno-uninitialized
79 CERRWARN += -_gcc=-Wno-parentheses
80 #
81 # needs work
82 $(OBJS_DIR)/blowfish_impl.o := SMOFF += precedence
83 #
84 #
85 # Default build targets.
86 #
87 .KEEP_STATE:
88 #
89 def: $(DEF_DEPS)
90 #
91 all: $(ALL_DEPS)
92 #
93 clean: $(CLEAN_DEPS)
94 #
95 clobber: $(CLOBBER_DEPS)
96 #
97 lint: $(LINT_DEPS)
98 #
99 modlintlib: $(MODLINTLIB_DEPS)
100 #
101 clean.lint: $(CLEAN_LINT_DEPS)
102 #
103 install: $(INSTALL_DEPS)
104 #
105 #
106 # Include common targets.
107 #
108 include $(UTSBASE)/intel/Makefile.targ
```



```

*****
2559 Mon Jan 7 13:07:54 2019
new/usr/src/uts/intel/bofi/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
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27 #
28 # This makefile drives the production of the bofi driver
29 # kernel module.
30 #

32 #
33 # Path to the base of the uts directory tree (usually /usr/src/uts).
34 #
35 UTBASE = ../..

37 #
38 # Define the module and object file sets.
39 #
40 MODULE = bofi
41 OBJECTS = $(BOFI_OBJS:%=$(OBJS_DIR)/%)
42 LINTS = $(BOFI_OBJS:%.o=$(LINTS_DIR)/%.ln)
43 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
44 CONF_SRCDIR = $(UTBASE)/common/io

46 #
47 # Include common rules.
48 #
49 include $(UTBASE)/intel/Makefile.intel

51 #
52 # Override defaults.
53 #
54 FILEMODE = 644

56 #
57 # Define targets
58 #
59 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
60 LINT_TARGET = $(MODULE).lint
61 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

```

```

63 #
64 # Override defaults to build a unique, local modstubs.o.
65 #
66 MODSTUBS_DIR = $(OBJS_DIR)
67 $(MODSTUBS_O) := AS_CPPFLAGS += -DBOFI_MODULE
68 CLEANFILES += $(MODSTUBS_O)

70 CPPFLAGS += -I../sun4

72 CERRWARN += -_gcc=-Wno-uninitialized

74 # needs work
75 $(OBJS_DIR)/bofi.o := SMOFF += signed_integer_overflow_check,deref_check

77 #
78 # For now, disable these lint checks; maintainers should endeavor
79 # to investigate and remove these for maximum lint coverage.
80 # Please do not carry these forward to new Makefiles.
81 #
82 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
83 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
84 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
85 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

87 #
88 # Default build targets.
89 #
90 .KEEP_STATE:

92 def: $(DEF_DEPS)

94 all: $(ALL_DEPS)

96 clean: $(CLEAN_DEPS)

98 clobber: $(CLOBBER_DEPS)

100 lint: $(LINT_DEPS)

102 modlintlib: $(MODLINTLIB_DEPS)

104 clean.lint: $(CLEAN_LINT_DEPS)

106 install: $(INSTALL_DEPS)

108 #
109 # Include common targets.
110 #
111 include $(UTBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/bpf/Makefile

1

```
*****
2431 Mon Jan 7 13:07:55 2019
new/usr/src/uts/intel/bpf/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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26 #
27 # uts/intel/bpf/Makefile
28 #
29 #
30 # This makefile drives the production of the bpf driver
31 # kernel module.
32 #
33 # intel architecture dependent
34 #
35 #
36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTSBASE = ../../

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = bpf
45 OBJECTS = $(BPF_OBJS:%=$(OBJS_DIR)/%)
46 LINTS = $(BPF_OBJS:%.o=$(LINTS_DIR)/%.ln)
47 ROOTMODULE = $(USR_DRV_DIR)/$(MODULE)
48 CONF_SRCDIR = $(UTSBASE)/common/io/bpf

50 #
51 # Include common rules.
52 #
53 include $(UTSBASE)/intel/Makefile.intel

55 #
56 # Define targets
57 #
58 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
59 LINT_TARGET = $(MODULE).lint
60 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
```

new/usr/src/uts/intel/bpf/Makefile

2

```
62 #
63 #
64 CFLAGS += $(CCVERBOSE)
65 LDFLAGS += -dy -Nmisc/mac -Nmisc/dls -Ndrv/ipnet -Nmisc/neti
66 INC_PATH += -I$(UTSBASE)/common/io/bpf

68 #
69 # For now, disable these lint checks; maintainers should endeavor
70 # to investigate and remove these for maximum lint coverage.
71 # Please do not carry these forward to new Makefiles.
72 #
73 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW -erroff=E_BAD_PTR_CAST_ALIGN

75 CERRWARN += -_gcc=-Wno-unused-label
76 CERRWARN += -_gcc=-Wno-uninitialized

78 # needs work
79 $(OBJS_DIR)/bpf.o := SMOFF += all_func_returns

81 #
82 # Default build targets.
83 #
84 .KEEP_STATE:

86 def: $(DEF_DEPS)

88 all: $(ALL_DEPS)

90 clean: $(CLEAN_DEPS)

92 clobber: $(CLOBBER_DEPS)

94 lint: $(LINT_DEPS)

96 modlintlib: $(MODLINTLIB_DEPS)

98 clean.lint: $(CLEAN_LINT_DEPS)

100 install: $(INSTALL_DEPS)

102 #
103 # Include common targets.
104 #
105 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/cardbus/Makefile

1

```
*****
2669 Mon Jan 7 13:07:55 2019
new/usr/src/uts/intel/cardbus/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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25 # uts/intel/cardbus/Makefile
26 #
27 #
28 # This makefile drives the production of the cardbus kernel module.
29 #
30 # intel architecture dependent
31 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../
38 #
39 # Define the module and object file sets.
40 #
41 MODULE = cardbus
42 OBJECTS = $(CARDBUS_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(CARDBUS_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
46 #
47 # Include common rules.
48 #
49 include $(UTSBASE)/intel/Makefile.intel
51 #
52 # Define targets
53 #
54 ALL_TARGET = $(BINARY)
55 LINT_TARGET = $(MODULE).lint
56 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
58 #
59 # Include sun4u specific header files
```

new/usr/src/uts/intel/cardbus/Makefile

2

```
60 #
61 INC_PATH += -I$(UTSBASE)/sun4u -I$(UTSBASE)/sun4 -I$(UTSBASE)/i86pc
63 #
64 # lint pass one enforcement
65 #
66 CFLAGS += $(CCVERBOSE)
68 CPPFLAGS += -DHOTPLUG
70 # dependency
71 LDFLAGS += -dy -Nmisc/busra -Nmisc/pcmcia -Nmisc/hpcsvc
73 #
74 # For now, disable these lint checks; maintainers should endeavor
75 # to investigate and remove these for maximum lint coverage.
76 # Please do not carry these forward to new Makefiles.
77 #
78 LINTTAGS += -errorf=E_SUSPICIOUS_COMPARISON
79 LINTTAGS += -errorf=E_BAD_PTR_CAST_ALIGN
80 LINTTAGS += -errorf=E_STATIC_UNUSED
81 LINTTAGS += -errorf=E_PTRDIFF_OVERFLOW
82 LINTTAGS += -errorf=E_ASSIGN_NARROW_CONV
84 CERRWARN += -_gcc=-Wno-uninitialized
85 CERRWARN += -_gcc=-Wno-parentheses
86 CERRWARN += -_gcc=-Wno-unused-function
87 CERRWARN += -_gcc=-Wno-unused-variable
89 SMOFF += indenting,no_if_block
91 #
92 # Default build targets.
93 #
94 .KEEP_STATE:
96 def: $(DEF_DEPS)
98 all: $(ALL_DEPS)
100 clean: $(CLEAN_DEPS)
102 clobber: $(CLOBBER_DEPS)
104 lint: $(LINT_DEPS)
106 modlintlib: $(MODLINTLIB_DEPS)
108 clean.lint: $(CLEAN_LINT_DEPS)
110 install: $(INSTALL_DEPS)
112 # Include common targets.
113 #
114 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
3238 Mon Jan 7 13:07:56 2019
new/usr/src/uts/intel/chxge/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # This makefile drives the production of the Chelsio N110
29 # 10G Ethernet (CH) driver module in x86 systems
30 #

32 #
33 # Paths to the base of the uts directory trees
34 #
35 UTSBASE = ../../

37 #
38 # Define the module and object file sets.
39 #
40 MODULE = chxge
41 OBJECTS = $(CH_OBJS:%=$(OBJS_DIR)/%) $(CH_COM_OBJS:%=$(OBJS_DIR)/%)
42 LINTS = $(CH_OBJS:%.o=$(LINTS_DIR)/%.ln) $(CH_COM_OBJS:%.o=$(LINTS_DIR)
43 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

45 #
46 # Include common rules.
47 #
48 include $(UTSBASE)/intel/Makefile.intel

50 #
51 # Define targets
52 #
53 ALL_TARGET = $(BINARY)
54 LINT_TARGET = $(MODULE).lint
55 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

57 CFLAGS += -DC99_NOT_SUPPORTED -DCONFIG_CHELSIO_T1_1G \
58 -I$(UTSBASE)/common/io/chxge/ \
59 -I$(UTSBASE)/common/io/chxge/com

61 CFLAGS += -DCONFIG_SMP

```

```

63 CFLAGS += -DRX_MAX_COUNT=256

65 CFLAGS += -DCONFIG_CHELSIO_T1_OFFLOAD

67 CFLAGS += -DSUN_KSTATS -DHOST_PAUSE -DTX_CKSUM_FIX -DTX_THREAD_RECLAIM

69 # Debug flags
70 # CFLAGS += -DCH_DEBUG=1 -DPE_DBGOUT_ENABLED=1

72 #
73 # Driver depends on GLD & IP
74 #
75 LDFLAGS += -dy -N misc/gld -N drv/ip

77 # Lint flag
78 #
79 LINTFLAGS += -DC99_NOT_SUPPORTED -DCONFIG_CHELSIO_T1_1G \
80 -I$(UTSBASE)/common/io/chxge/ \
81 -I$(UTSBASE)/common/io/chxge/com

83 LINTFLAGS += -DCONFIG_SMP

85 LINTFLAGS += -DRX_MAX_COUNT=256

87 LINTFLAGS += -DCONFIG_CHELSIO_T1_OFFLOAD

89 LINTFLAGS += -DSUN_KSTATS -DHOST_PAUSE -DTX_CKSUM_FIX -DTX_THREAD_RECLAIM

91 LINTFLAGS += -Xc99=%all

93 #
94 # For now, disable these lint checks; maintainers should endeavor
95 # to investigate and remove these for maximum lint coverage.
96 # Please do not carry these forward to new Makefiles.
97 #
98 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
99 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
100 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
101 LINTTAGS += -erroff=E_STATIC_UNUSED

103 CERRWARN += -_gcc=-Wno-unused-label
104 CERRWARN += -_gcc=-Wno-unused-function
105 CERRWARN += -_gcc=-Wno-unused-variable
106 CERRWARN += -_gcc=-Wno-parentheses

108 SMOFF += indenting,all_func_returns,no_if_block

110 # needs work
111 $(OBJS_DIR)/sge.o := SMOFF += index_overflow

113 #
114 #
115 # Default build targets.
116 #
117 .KEEP_STATE:

119 def: $(DEF_DEPS)

121 all: $(ALL_DEPS)

123 clean: $(CLEAN_DEPS)

125 clobber: $(CLOBBER_DEPS)

127 lint: $(LINT_DEPS)

```

```
129 modlintlib:    $(MODLINTLIB_DEPS)
131 clean.lint:    $(CLEAN_LINT_DEPS)
133 install:       $(INSTALL_DEPS)

135 #
136 #       Include common targets.
137 #
138 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
1888 Mon Jan 7 13:07:56 2019
new/usr/src/uts/intel/clone/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
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19 #
20 # CDDL HEADER END
21 #
22 #
23 # uts/intel/clone/Makefile
24 #
25 # Copyright 2004 Sun Microsystems, Inc. All rights reserved.
26 # Use is subject to license terms.
27 #
28 # Copyright (c) 2018, Joyent, Inc.
28 #pragma ident "%Z%M% %I% %E% SMI"
29 #
30 # This makefile drives the production of the clone driver
31 #
32 # intel implementation architecture dependent
33 #

30 #
31 # Path to the base of the uts directory tree (usually /usr/src/uts).
32 #
33 UTSBASE = ../../

35 #
36 # Define the module and object file sets.
37 #
38 MODULE = clone
39 OBJECTS = $(CLONE_OBJS:%=$(OBJS_DIR)/%)
40 LINTS = $(CLONE_OBJS:%.o=$(LINTS_DIR)/%.ln)
41 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
42 CONF_SRCDIR = $(UTSBASE)/common/io

44 #
45 # Include common rules.
46 #
47 include $(UTSBASE)/intel/Makefile.intel

49 #
50 # Define targets
51 #
52 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
53 LINT_TARGET = $(MODULE).lint
54 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

```

```

56 # needs work
57 $(OBJS_DIR)/clone.o := SMOFF += all_func_returns

59 #
60 # Default build targets.
61 #
62 .KEEP_STATE:

64 def: $(DEF_DEPS)

66 all: $(ALL_DEPS)

68 clean: $(CLEAN_DEPS)

70 clobber: $(CLOBBER_DEPS)

72 lint: $(LINT_DEPS)

74 modlintlib: $(MODLINTLIB_DEPS)

76 clean.lint: $(CLEAN_LINT_DEPS)

78 install: $(INSTALL_DEPS)

80 #
81 # Include common targets.
82 #
83 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2413 Mon Jan 7 13:07:56 2019
new/usr/src/uts/intel/cmlb/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
12 #
13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the cmlb "misc"
28 # kernel module.
29 # intel architecture dependent
30 #
31 #
32 #
33 #
34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 #
38 # Define the module and object file sets.
39 #
40 #
41 #
42 #
43 # MODULE = cmlb
44 # OBJECTS = $(CMLB_OBJS:%=$(OBJS_DIR)/%)
45 # LINTS = $(CMLB_OBJS:%.o=$(LINTS_DIR)/%.ln)
46 # ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
47 #
48 # Include common rules.
49 #
50 # include $(UTSBASE)/intel/Makefile.intel
51 #
52 # Define targets
53 #
54 # ALL_TARGET = $(BINARY)
55 # LINT_TARGET = $(MODULE).lint
56 # INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
57 #
58 # Overrides.
59 #

```

```

57 DEBUG_FLGS =
58 DEBUG_DEFS += $(DEBUG_FLGS)
59 #
60 # enable larger than 1TB VTOC support
61 #
62 CPPFLAGS += -D_EXTVTOC
63 #
64 #
65 # For now, disable these lint checks; maintainers should endeavor
66 # to investigate and remove these for maximum lint coverage.
67 # Please do not carry these forward to new Makefiles.
68 #
69 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
70 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
71 LINTTAGS += -erroff=E_STATIC_UNUSED
72 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
73 #
74 CERRWARN += -_gcc=-Wno-unused-label
75 CERRWARN += -_gcc=-Wno-type-limits
76 CERRWARN += -_gcc=-Wno-parentheses
77 CERRWARN += -_gcc=-Wno-unused-function
78 CERRWARN += -_gcc=-Wno-uninitialized
79 #
80 # needs work
81 $(OBJS_DIR)/cmlb.o := SMOFF += signed
82 #
83 #
84 # Default build targets.
85 #
86 .KEEP_STATE:
87 #
88 def: $(DEF_DEPS)
89 #
90 all: $(ALL_DEPS)
91 #
92 clean: $(CLEAN_DEPS)
93 #
94 clobber: $(CLOBBER_DEPS)
95 #
96 lint: $(LINT_DEPS)
97 #
98 modlintlib: $(MODLINTLIB_DEPS)
99 #
100 clean.lint: $(CLEAN_LINT_DEPS)
101 #
102 install: $(INSTALL_DEPS)
103 #
104 #
105 # Include common targets.
106 #
107 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/cpqary3/Makefile

1

\*\*\*\*\*

1391 Mon Jan 7 13:07:57 2019

new/usr/src/uts/intel/cpqary3/Makefile

10077 usr/src/uts/ Makefile changes for smatch

\*\*\*\*\*

```
1 #
2 # This file and its contents are supplied under the terms of the
3 # Common Development and Distribution License ("CDDL"), version 1.0.
4 # You may only use this file in accordance with the terms of version
5 # 1.0 of the CDDL.
6 #
7 # A full copy of the text of the CDDL should have accompanied this
8 # source. A copy of the CDDL is also available via the Internet at
9 # http://www.illumos.org/license/CDDL.
10 #
```

```
12 #
13 # Copyright (c) 2018, Joyent, Inc.
14 # Copyright 2013 Joyent, Inc. All rights reserved.
15 #
```

```
16 #
17 # Path to the base of the uts directory tree
18 #
19 UTSBASE = ../..
```

```
21 #
22 # Define the module and object file sets.
23 #
24 MODULE = cpqary3
25 OBJECTS = $(CPQARY3_OBJS:%=$(OBJDIR)/%)
26 LINTS = $(CPQARY3_OBJS:%.o=$(LINTSDIR)/%.ln)
27 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
28 CONF_SRCDIR = $(UTSBASE)/common/io/cpqary3
```

```
30 #
31 # Include common rules.
32 #
33 include $(UTSBASE)/intel/Makefile.intel
```

```
35 #
36 # Define targets
37 #
38 ALL_TARGET = $(BINARY) $(CONFMOD)
39 LINT_TARGET = $(MODULE).lint
40 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
```

```
42 CPPFLAGS += -I$(UTSBASE)/common/io/cpqary3
```

```
44 #
45 # Kernel Module Dependencies
46 #
47 LDFLAGS += -dy -Nmisc/scsi
```

```
49 SMATCH=off
```

```
51 #
52 # Default build targets.
53 #
54 .KEEP_STATE:
```

```
56 def: $(DEF_DEPS)
```

```
58 all: $(ALL_DEPS)
```

```
60 clean: $(CLEAN_DEPS)
```

new/usr/src/uts/intel/cpqary3/Makefile

2

```
62 clobber: $(CLOBBER_DEPS)
```

```
64 lint: $(LINT_DEPS)
```

```
66 modlintlib: $(MODLINTLIB_DEPS)
```

```
68 clean.lint: $(CLEAN_LINT_DEPS)
```

```
70 install: $(INSTALL_DEPS)
```

```
72 #
```

```
73 # Include common targets.
```

```
74 #
```

```
75 include $(UTSBASE)/intel/Makefile.targ
```



```

*****
2183 Mon Jan 7 13:07:57 2019
new/usr/src/uts/intel/ctfs/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
12 #
13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # This makefile drives the production of the contract file system
29 # kernel module.
30 #
31 # intel implementation architecture dependent
32 #

34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../../

39 #
40 # Define the module and object file sets.
41 #
42 MODULE = ctfs
43 OBJECTS = $(CTFS_OBJS:%=$(OBJS_DIR)/%)
44 LINTS = $(CTFS_OBJS:%.o=$(LINTS_DIR)/%.ln)
45 ROOTMODULE = $(ROOT_FS_DIR)/$(MODULE)

47 #
48 # Include common rules.
49 #
50 include $(UTSBASE)/intel/Makefile.intel

52 #
53 # Define targets
54 #
55 ALL_TARGET = $(BINARY)
56 LINT_TARGET = $(MODULE).lint
57 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

59 #
60 # For now, disable these lint checks; maintainers should endeavor
61 # to investigate and remove these for maximum lint coverage.

```

```

62 # Please do not carry these forward to new Makefiles.
63 #
64 LINTTAGS += -eroff=E_ASSIGN_NARROW_CONV

66 CERRWARN += -_gcc=-Wno-parentheses

68 # needs work
69 $(OBJS_DIR)/ctfs_root.o := SMOFF += signed_integer_overflow_check

71 #
72 # Default build targets.
73 #
74 .KEEP_STATE:

76 def: $(DEF_DEPS)

78 all: $(ALL_DEPS)

80 clean: $(CLEAN_DEPS)

82 clobber: $(CLOBBER_DEPS)

84 lint: $(LINT_DEPS)

86 modlintlib: $(MODLINTLIB_DEPS)

88 clean.lint: $(CLEAN_LINT_DEPS)

90 install: $(INSTALL_DEPS)

92 #
93 # Include common targets.
94 #
95 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/cxgbe/t4nex/Makefile

1

```
*****
3046 Mon Jan 7 13:07:57 2019
new/usr/src/uts/intel/cxgbe/t4nex/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # This file and its contents are supplied under the terms of the
3 # Common Development and Distribution License ("CDDL"), version 1.0.
4 # You may only use this file in accordance with the terms of version
5 # 1.0 of the CDDL.
6 #
7 # A full copy of the text of the CDDL should have accompanied this
8 # source. A copy of the CDDL is also available via the Internet at
9 # http://www.illumos.org/license/CDDL.
10 #
12 #
13 # Copyright (c) 2013 by Chelsio Communications, Inc. All rights reserved.
14 #
15 # Copyright (c) 2018, Joyent, Inc.
17 #
18 # This makefile drives the production of the Chelsio Terminator 4 10G Ethernet
19 # (CXGBE) driver nexus modules on x86 systems.
20 #
22 #
23 # Paths to the base of the uts directory trees
24 #
25 UTSBASE = ../../..
27 #
28 # Define the module and object file sets.
29 #
30 MODULE      = t4nex
31 OBJECTS     = $(CXGBE_COM_OBJS:%=$(OBJSDIR)/%) \
32              $(CXGBE_FW_OBJS:%=$(OBJSDIR)/%) \
33              $(CXGBE_NEX_OBJS:%=$(OBJSDIR)/%)
34 LINTS       = $(CXGBE_COM_OBJS:%.o=$(LINTSDIR)/%.ln) \
35              $(CXGBE_FW_OBJS:%.o=$(LINTSDIR)/%.ln) \
36              $(CXGBE_NEX_OBJS:%.o=$(LINTSDIR)/%.ln)
37 ROOTMODULE  = $(ROOTDRVDIR)/$(MODULE)
39 #
40 # Include common rules
41 #
42 include $(UTSBASE)/intel/Makefile.intel
44 #
45 # Define targets
46 #
47 ALL_TARGET = $(BINARY)
48 LINT_TARGET = $(MODULE).lint
49 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
51 CFLAGS += -g -I$(UTSBASE)/common/io/cxgbe -I$(UTSBASE)/common/io/cxgbe/common \
52           -I$(UTSBASE)/common/io/cxgbe/t4nex -I$(UTSBASE)/common/io/cxgbe/shared \
53           -I$(UTSBASE)/common/io/cxgbe/firmware
55 #
56 # Driver depends
57 #
58 LDFLAGS += -dy -N misc/mac -N drv/ip
60 #
61 # Lint
```

new/usr/src/uts/intel/cxgbe/t4nex/Makefile

2

```
62 #
63 LINTFLAGS += -I$(UTSBASE)/common/io/cxgbe -I$(UTSBASE)/common/io/cxgbe/common \
64             -I$(UTSBASE)/common/io/cxgbe/t4nex -I$(UTSBASE)/common/io/cxgbe/shared \
65             -I$(UTSBASE)/common/io/cxgbe/firmware
66 LINTFLAGS += -Xc99=%all
68 #
69 # A lot of these come from the fact that there are static inlines and
70 # that this driver leverages various C99 and some GNU C extensions.
71 #
72 LINTTAGS += -erroff=E_STATIC_UNUSED
73 LINTTAGS += -erroff=E_FUNC_ARG_UNUSED
74 LINTTAGS += -erroff=E_ZERO_OR_NEGATIVE_SUBSCRIPT
75 LINTTAGS += -erroff=E_ZERO_SIZED_STRUCT_UNION
76 LINTTAGS += -erroff=E_ANONYMOUS_UNION_DECL
77 LINTTAGS += -erroff=E_CONSTANT_CONDITION
79 #
80 # These have been manually verified at the moment. When updating this
81 # driver, please manually check the following lint warnings. Note some
82 # of these are due to limitations in Sun Studio.
83 #
84 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
85 LINTTAGS += -erroff=E_ENUM_VAL_OVERFLOWS_INT_MAX
86 LINTTAGS += -erroff=E_FUNC_NO_RET_VAL
87 LINTTAGS += -erroff=E_NOP_IF_STMT
88 LINTTAGS += -erroff=E_FUNC_SET_NOT_USED
89 LINTTAGS += -erroff=E_FUNC_RET_ALWAYS_IGNORED2
90 LINTTAGS += -erroff=E_FUNC_RET_MAYBE_IGNORED2
91 LINTTAGS += -erroff=E_CASE_FALLTHRU
92 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
94 # needs work
95 SMATCH=off
97 #
98 # Default build targets.
99 #
100 .KEEP_STATE:
102 def:          $(DEF_DEPS)
104 all:         $(ALL_DEPS)
106 clean:       $(CLEAN_DEPS)
108 clobber:     $(CLOBBER_DEPS)
110 lint:        $(LINT_DEPS)
112 modlintlib:  $(MODLINTLIB_DEPS)
114 clean.lint:  $(CLEAN_LINT_DEPS)
116 install:     $(INSTALL_DEPS)
118 #
119 # Include common targets.
120 #
121 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/dadk/Makefile

1

```
*****
2186 Mon Jan 7 13:07:58 2019
new/usr/src/uts/intel/dadk/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
12 #
13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/dadk/Makefile
23 #
24 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the dadk "misc"
28 # kernel module.
29 #
30 # intel architecture dependent
31 #
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 #
37 # UTSBASE = ../../
38 #
39 # Define the module and object file sets.
40 #
41 #
42 #
43 # MODULE = dadk
44 # OBJECTS = $(DADK_OBJS:%=$(OBJS_DIR)/%)
45 # LINTS = $(DADK_OBJS:%.o=$(LINTS_DIR)/%.ln)
46 # ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
47 #
48 # Include common rules.
49 #
50 # include $(UTSBASE)/intel/Makefile.intel
51 #
52 # Define targets
53 #
54 # ALL_TARGET = $(BINARY)
55 # LINT_TARGET = $(MODULE).lint
56 # INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
57 #
58 # Overrides.
59 #
```

new/usr/src/uts/intel/dadk/Makefile

2

```
57 DEBUG_FLGS =
58 DEBUG_DEFS += $(DEBUG_FLGS)
59 LDFLAGS += -dy -Nmisc/gda
60 #
61 # Larger than 1TB VTOC support
62 #
63 CPPFLAGS += -D_EXTVTOC
64 #
65 #
66 # For now, disable these lint checks; maintainers should endeavor
67 # to investigate and remove these for maximum lint coverage.
68 # Please do not carry these forward to new Makefiles.
69 #
70 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
71 #
72 CERRWARN += -_gcc=-Wno-parentheses
73 #
74 # needs work
75 SMOFF += all_func_returns,signed
76 #
77 #
78 # Default build targets.
79 #
80 .KEEP_STATE:
81 #
82 def: $(DEF_DEPS)
83 #
84 all: $(ALL_DEPS)
85 #
86 clean: $(CLEAN_DEPS)
87 #
88 clobber: $(CLOBBER_DEPS)
89 #
90 lint: $(LINT_DEPS)
91 #
92 modlintlib: $(MODLINTLIB_DEPS)
93 #
94 clean.lint: $(CLEAN_LINT_DEPS)
95 #
96 install: $(INSTALL_DEPS)
97 #
98 #
99 # Include common targets.
100 #
101 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2451 Mon Jan 7 13:07:58 2019
new/usr/src/uts/intel/daplt/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
12 #
13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #

22 #
23 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.

28 #
29 # Paths to the base of the uts directory trees
30 #
31 UTBASE = ../../

33 #
34 # Define the module and object file sets.
35 #
36 MODULE = daplt
37 OBJECTS = $(DAPLT_OBJS:%=$(OBJS_DIR)/%)
38 LINTS = $(DAPLT_OBJS:%.o=$(LINTS_DIR)/%.ln)
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
40 CONF_SRCDIR = $(UTSBASE)/common/io/ib/clients/daplt
41 LDFLAGS += -dy -Nmisc/ibt1 -Nmisc/ibcm

43 #
44 # Include common rules.
45 #
46 include $(UTSBASE)/intel/Makefile.intel

48 #
49 # Define targets
50 #
51 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
52 LINT_TARGET = $(MODULE).lint
53 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

55 #
56 # path for header files
57 #
58 INCLUDE_PATH += -I$(UTSBASE)/common/sys/ib/clients/daplt

60 #
61 # Overrides.

```

```

62 #
63 #
64 #
65 # For now, disable these lint checks; maintainers should endeavor
66 # to investigate and remove these for maximum lint coverage.
67 # Please do not carry these forward to new Makefiles.
68 #
69 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
70 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
71 LINTTAGS += -erroff=E_STATIC_UNUSED

73 CERRWARN += -_gcc=-Wno-parentheses
74 CERRWARN += -_gcc=-Wno-unused-variable
75 CERRWARN += -_gcc=-Wno-unused-function
76 CERRWARN += -_gcc=-Wno-uninitialized

78 # needs work
79 $(OBJS_DIR)/daplt.o := SMOFF += all_func_returns,deref_check

81 #
82 # Default build targets.
83 #
84 .KEEP_STATE:

86 def: $(DEF_DEPS)

88 all: $(ALL_DEPS)

90 clean: $(CLEAN_DEPS)

92 clobber: $(CLOBBER_DEPS)

94 lint: $(LINT_DEPS)

96 modlintlib: $(MODLINTLIB_DEPS)

98 clean.lint: $(CLEAN_LINT_DEPS)

100 install: $(INSTALL_DEPS)

102 #
103 # Include common targets.
104 #
105 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2444 Mon Jan 7 13:07:59 2019
new/usr/src/uts/intel/dca/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
12 #
13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2014 Garrett D'Amore <garrett@damore.org>
24 #
25 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
26 # Use is subject to license terms.
27 #
28 # Copyright (c) 2018, Joyent, Inc.
29 #
30 #
31 # This makefile drives the production of the DCA kCF provider.
32 #
33 # intel implementation architecture dependent
34 #
35 #
36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTSBASE = ../..
40 #
41 #
42 # Define the module and object file sets.
43 #
44 MODULE = dca
45 OBJECTS = $(DCA_OBJS:%=$(OBJS_DIR)/%)
46 LINTS = $(DCA_OBJS:%.o=$(LINTS_DIR)/%.ln)
47 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
48 CONF_SRCDIR = $(UTSBASE)/common/crypto/io
49 #
50 #
51 # Include common rules.
52 #
53 include $(UTSBASE)/intel/Makefile.intel
54 #
55 #
56 # Define targets
57 #
58 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
59 LINT_TARGET = $(MODULE).lint
60 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTLINK) $(ROOT_CONFFILE)

```

```

62 #
63 # For now, disable these lint checks; maintainers should endeavor
64 # to investigate and remove these for maximum lint coverage.
65 # Please do not carry these forward to new Makefiles.
66 #
67 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
68 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
69 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
70 #
71 CERRWARN += -_gcc=-Wno-parentheses
72 #
73 # needs work
74 $(OBJS_DIR)/dca_rsa.o := SMOFF += deref_check
75 #
76 #
77 # Default build targets.
78 #
79 .KEEP_STATE:
80 #
81 def: $(DEF_DEPS)
82 #
83 all: $(ALL_DEPS)
84 #
85 clean: $(CLEAN_DEPS)
86 #
87 clobber: $(CLOBBER_DEPS)
88 #
89 lint: $(LINT_DEPS)
90 #
91 modlintlib: $(MODLINTLIB_DEPS)
92 #
93 clean.lint: $(CLEAN_LINT_DEPS)
94 #
95 install: $(INSTALL_DEPS)
96 #
97 $(ROOTLINK): $(ROOT_CRYPTODIR) $(ROOTMODULE)
98 -$(RM) $@; ln $(ROOTMODULE) $@
99 #
100 #
101 # Include common targets.
102 #
103 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/devfs/Makefile

1

```
*****
2083 Mon Jan 7 13:07:59 2019
new/usr/src/uts/intel/devfs/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
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27 #
28 # uts/intel/devfs/Makefile
29 # This makefile drives the production of the devfs file system
30 # kernel module.
31 #
32 # intel implementation architecture dependent
33 #

35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTBASE = ../../

40 #
41 # Define the module and object file sets.
42 #
43 MODULE = devfs
44 OBJECTS = $(DEVFS_OBJS:%=$(OBJS_DIR)/%)
45 LINTS = $(DEVFS_OBJS:%.o=$(LINTS_DIR)/%.ln)
46 ROOTMODULE = $(ROOT_FS_DIR)/$(MODULE)

48 #
49 # Include common rules.
50 #
51 include $(UTSBASE)/intel/Makefile.intel

53 CERRWARN += -_gcc=-Wno-parentheses

55 # needs work
56 $(OBJS_DIR)/devfs_vnops.o := SMOFF += signed

58 #
59 # Define targets
60 #
61 ALL_TARGET = $(BINARY)
```

new/usr/src/uts/intel/devfs/Makefile

2

```
62 LINT_TARGET = $(MODULE).lint
63 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

65 #
66 # Overrides.
67 #
68 MODSTUBS_DIR = $(OBJS_DIR)
69 $(MODSTUBS_O) := AS_CPPFLAGS += -DDEVFS_MODULE
70 CLEANFILES += $(MODSTUBS_O)

72 #
73 # Default build targets.
74 #
75 .KEEP_STATE:

77 def: $(DEF_DEPS)

79 all: $(ALL_DEPS)

81 clean: $(CLEAN_DEPS)

83 clobber: $(CLOBBER_DEPS)

85 lint: $(LINT_DEPS)

87 modlintlib: $(MODLINTLIB_DEPS)

89 clean.lint: $(CLEAN_LINT_DEPS)

91 install: $(INSTALL_DEPS)

93 #
94 # Include common targets.
95 #
96 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/devinfo/Makefile

1

```
*****
2204 Mon Jan 7 13:08:00 2019
new/usr/src/uts/intel/devinfo/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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19 # CDDL HEADER END
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21 #
22 # uts/intel/devinfo/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
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27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the devinfo kernel
31 # driver to support libdevinfo.
32 #
33 # intel implementation architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTSBASE = ../../

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = devinfo
45 OBJECTS = $(DEVINFO_OBJS:%=$(OBJDIR)/%)
46 LINTS = $(DEVINFO_OBJS:%.o=$(LINTDIR)/%.ln)
47 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
48 CONF_SRCDIR = $(UTSBASE)/common/io

50 #
51 # Include common rules.
52 #
53 include $(UTSBASE)/intel/Makefile.intel

55 #
56 # Define targets
57 #
58 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
59 LINT_TARGET = $(MODULE).lint
60 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
```

new/usr/src/uts/intel/devinfo/Makefile

2

```
62 CPPFLAGS += -I$(SRC)/common
64 CERRWARN += -_gcc=-Wno-unused-label
65 CERRWARN += -_gcc=-Wno-uninitialized
66 CERRWARN += -_gcc=-Wno-parentheses
67 CERRWARN += -_gcc=-Wno-clobbered

69 # needs work
70 $(OBJDIR)/devinfo.o := SMOFF += deref_check,strcpy_overflow

72 #
73 # Default build targets.
74 #
75 .KEEP_STATE:

77 def: $(DEF_DEPS)

79 all: $(ALL_DEPS)

81 clean: $(CLEAN_DEPS)

83 clobber: $(CLOBBER_DEPS)

85 lint: $(LINT_DEPS)

87 modlintlib: $(MODLINTLIB_DEPS)

89 clean.lint: $(CLEAN_LINT_DEPS)

91 install: $(INSTALL_DEPS)

93 #
94 # Include common targets.
95 #
96 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/dld/Makefile

1

```
*****
2579 Mon Jan 7 13:08:02 2019
new/usr/src/uts/intel/dld/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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2 # CDDL HEADER START
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20 #
21 #
22 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTSBASE = ../../

32 #
33 # Define the module and object file sets.
34 #
35 MODULE          = dld
36 OBJECTS         = $(DLD_OBJS:%=$(OBJS_DIR)/%)
37 LINTS           = $(DLD_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE     = $(ROOT_DRV_DIR)/$(MODULE)
39 CONF_SRCDIR    = $(UTSBASE)/common/io/dld
40 ROOTDEVLINK    = $(ROOT)/dev/$(MODULE)

42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel

47 #
48 # Define targets
49 #
50 ALL_TARGET      = $(BINARY) $(SRC_CONFILE)
51 LINT_TARGET     = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

54 #
55 # Overrides
56 #
57 CFLAGS          += $(CCVERBOSE)
58 LDFLAGS         += -dy -N misc/dls -N misc/mac
59 INC_PATH        += -I$(UTSBASE)/common/io/bpf

61 #
```

new/usr/src/uts/intel/dld/Makefile

2

```
62 # For now, disable these lint checks; maintainers should endeavor
63 # to investigate and remove these for maximum lint coverage.
64 # Please do not carry these forward to new Makefiles.
65 #
66 LINTTAGS        += -erroff=E_BAD_PTR_CAST_ALIGN
67 LINTTAGS        += -erroff=E_PTRDIFF_OVERFLOW
68 LINTTAGS        += -erroff=E_ASSIGN_NARROW_CONV
69 LINTTAGS        += -erroff=E_SUSPICIOUS_COMPARISON

71 CERRWARN        += -_gcc=-Wno-uninitialized
72 CERRWARN        += -_gcc=-Wno-type-limits
73 CERRWARN        += -_gcc=-Wno-parentheses

75 # needs work
76 $(OBJS_DIR)/dld_proto.o := SMOFF += signed
77 $(OBJS_DIR)/dld_drv.o := SMOFF += cast_assign

79 #
80 #           Default build targets.
81 #
82 .KEEP_STATE:

84 def:           $(DEF_DEPS)

86 all:           $(ALL_DEPS)

88 clean:         $(CLEAN_DEPS)

90 clobber:       $(CLOBBER_DEPS)

92 lint:          $(LINT_DEPS)

94 modlintlib:    $(MODLINTLIB_DEPS)

96 clean.lint:    $(CLEAN_LINT_DEPS)

98 install:       $(INSTALL_DEPS) $(ROOTDEVLINK)

100 $(ROOTDEVLINK):
101     -$(RM) $@; $(SYMLINK) ../devices/pseudo/$(@F)@0:ctl $@

103 #
104 #           Include common targets.
105 #
106 include $(UTSBASE)/intel/Makefile.targ
```



new/usr/src/uts/intel/dls/Makefile

1

```
*****
2260 Mon Jan 7 13:08:02 2019
new/usr/src/uts/intel/dls/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTBASE = ../../

32 #
33 # Define the module and object file sets.
34 #
35 MODULE      = dls
36 OBJECTS     = $(DLS_OBJS:%=$(OBJS_DIR)/%)
37 LINTS       = $(DLS_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)

40 #
41 # Include common rules.
42 #
43 include $(UTBASE)/intel/Makefile.intel

45 #
46 # Define targets
47 #
48 ALL_TARGET = $(BINARY)
49 LINT_TARGET = $(MODULE).lint
50 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

52 #
53 # Overrides.
54 #
55 CFLAGS      += $(CCVERBOSE)
56 LDFLAGS     += -dy -N misc/mac
57 INC_PATH    += -I$(UTBASE)/common/io/bpf

59 #
60 # For now, disable these lint checks; maintainers should endeavor
61 # to investigate and remove these for maximum lint coverage.
```

new/usr/src/uts/intel/dls/Makefile

2

```
62 # Please do not carry these forward to new Makefiles.
63 #
64 LINTTAGS      += -erroff=E_BAD_PTR_CAST_ALIGN
65 LINTTAGS      += -erroff=E_STATIC_UNUSED
66 LINTTAGS      += -erroff=E_PTRDIFF_OVERFLOW
67 LINTTAGS      += -erroff=E_ASSIGN_NARROW_CONV

69 CERWARN      += -_gcc=-Wno-uninitialized

71 # needs work
72 $(OBJS_DIR)/dls_link.o := SMOFF += all_func_returns

75 #
76 # Default build targets.
77 #
78 .KEEP_STATE:

80 def:          $(DEF_DEPS)

82 all:          $(ALL_DEPS)

84 clean:        $(CLEAN_DEPS)

86 clobber:      $(CLOBBER_DEPS)

88 lint:         $(LINT_DEPS)

90 modlintlib:   $(MODLINTLIB_DEPS)

92 clean.lint:   $(CLEAN_LINT_DEPS)

94 install:      $(INSTALL_DEPS)

96 #
97 # Include common targets.
98 #
99 include $(UTBASE)/intel/Makefile.targ
```

```

*****
2254 Mon Jan 7 13:08:04 2019
new/usr/src/uts/intel/dnet/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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20 #
21 #
22 # uts/intel/dnet/Makefile
23 #
24 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the dnet
28 # network driver kernel module.
29 #
30 # intel architecture dependent
31 #

29 #
30 # Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 UTSBASE = ../../

34 #
35 # Define the module and object file sets.
36 #
37 MODULE = dnet
38 OBJECTS = ${DNET_OBJS:%=${OBJS_DIR}/%}
39 LINTS = ${DNET_OBJS:%.o=${LINTS_DIR}/%.ln}
40 ROOTMODULE = ${ROOT_DRV_DIR}/${MODULE}

42 #
43 # Include common rules.
44 #
45 include ${UTSBASE}/intel/Makefile.intel

47 #
48 # Define targets
49 #
50 ALL_TARGET = ${BINARY}
51 LINT_TARGET = ${MODULE}.lint
52 INSTALL_TARGET = ${BINARY} ${ROOTMODULE}

54 #
55 # For now, disable these lint checks; maintainers should endeavor
56 # to investigate and remove these for maximum lint coverage.

```

```

57 # Please do not carry these forward to new Makefiles.
58 #
59 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
60 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
61 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

63 CERRWARN += -_gcc=-Wno-parentheses
64 CERRWARN += -_gcc=-Wno-uninitialized

66 # needs work
67 ${OBJS_DIR}/dnet.o := SMOFF += index_overflow
68 ${OBJS_DIR}/dnet_mii.o := SMOFF += all_func_returns

70 LDFLAGS += -dy -N misc/mac

72 #
73 # Default build targets.
74 #
75 .KEEP_STATE:

77 def: ${DEF_DEPS}

79 all: ${ALL_DEPS}

81 clean: ${CLEAN_DEPS}

83 clobber: ${CLOBBER_DEPS}

85 lint: ${LINT_DEPS}

87 modlintlib: ${MODLINTLIB_DEPS}

89 clean.lint: ${CLEAN_LINT_DEPS}

91 install: ${INSTALL_DEPS}

93 #
94 # Include common targets.
95 #
96 include ${UTSBASE}/intel/Makefile.targ

```

```

*****
2415 Mon Jan 7 13:08:07 2019
new/usr/src/uts/intel/doorfs/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/doorfs/Makefile
23 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.

28 #
29 # This makefile drives the production of the doorfs driver kernel module.
30 #
31 # intel architecture dependent
32 #

34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTBASE = ../../

39 DOORFS_OBJS += door_vnops.o door_sys.o door_support.o

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = doorfs
45 OBJECTS = $(DOORFS_OBJS:%=$(OBJS_DIR)/%)
46 LINTS = $(DOORFS_OBJS:%.o=$(LINTS_DIR)/%.ln)
47 ROOTMODULE = $(ROOT_SYS_DIR)/$(MODULE)

49 #
50 # Include common rules.
51 #

53 include $(UTSBASE)/intel/Makefile.intel

55 #
56 # Define targets
57 #
58 ALL_TARGET = $(BINARY)
59 LINT_TARGET = $(MODULE).lint
60 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

```

```

62 #
63 # Overrides.
64 #
65 MODSTUBS_DIR = $(OBJS_DIR)
66 $(MODSTUBS_O) := AS_CPPFLAGS +=
67 CLEANFILES += $(MODSTUBS_O)

69 #
70 # For now, disable these lint checks; maintainers should endeavor
71 # to investigate and remove these for maximum lint coverage.
72 # Please do not carry these forward to new Makefiles.
73 #
74 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
75 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

77 CERRWARN += -_gcc=-Wno-parentheses
78 CERRWARN += -_gcc=-Wno-uninitialized

80 # needs work
81 $(OBJS_DIR)/door_sys.o := SMOFF += deref_check

83 #
84 # Default build targets.
85 #
86 .KEEP_STATE:

88 def: $(DEF_DEPS)

90 all: $(ALL_DEPS)

92 clean: $(CLEAN_DEPS)

94 clobber: $(CLOBBER_DEPS)

96 lint: $(LINT_DEPS)

98 modlintlib: $(MODLINTLIB_DEPS)

100 clean.lint: $(CLEAN_LINT_DEPS)

102 install: $(INSTALL_DEPS)

104 #
105 # Include common targets.
106 #
107 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2196 Mon Jan 7 13:08:09 2019
new/usr/src/uts/intel/drm/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 # uts/intel/drm/Makefile
22 #
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.

28 #
29 # This makefile drives the production of the DRM (Direct Rendering
30 # Manager) common misc module.
31 #
32 # Path to the base of the uts directory tree (usually /usr/src/uts).
33 #
34 UTSBASE = ../../

36 #
37 # Define the module and object file sets.
38 #
39 MODULE = drm
40 OBJECTS = $(DRM_OBJS:%=$(OBJS_DIR)/%)
41 LINTS = $(DRM_OBJS:%.o=$(LINTS_DIR)/%.ln)
42 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
43 DRM_SRC = $(UTSBASE)/i86pc/io/drm
44 GFX_DIR = $(UTSBASE)/i86pc/io/gfx_private

46 INC_PATH += -I$(DRM_SRC) -I$(GFX_DIR)

48 # Dependency
49 LDFLAGS += -dy -Nmisc/agpmaster -Nmisc/gfx_private

51 #
52 # Include common rules.
53 #
54 include $(UTSBASE)/intel/Makefile.intel

56 CERRWARN += -_gcc=-Wno-parentheses
57 CERRWARN += -_gcc=-Wno-uninitialized
58 CERRWARN += -_gcc=-Wno-unused-variable

60 # needs work
61 SMOFF += precedence,indenting,deref_check,all_func_returns

```

```

63 #
64 # Define targets
65 #
66 ALL_TARGET = $(BINARY)
67 LINT_TARGET = $(MODULE).lint
68 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

70 #
71 # Default build targets.
72 #
73 .KEEP_STATE:

75 def: $(DEF_DEPS)

77 all: $(ALL_DEPS)

79 clean: $(CLEAN_DEPS)

81 clobber: $(CLOBBER_DEPS)

83 lint: $(LINT_DEPS)

85 modlintlib: $(MODLINTLIB_DEPS)

87 clean.lint: $(CLEAN_LINT_DEPS)

89 install: $(INSTALL_DEPS)

91 #
92 # Include common targets.
93 #
94 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/dtrace/Makefile

1

\*\*\*\*\*

2330 Mon Jan 7 13:08:11 2019

new/usr/src/uts/intel/dtrace/Makefile

10077 usr/src/uts/ Makefile changes for smatch

\*\*\*\*\*

```
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
```

```
27 UTSBASE = ../../
```

```
29 MODULE          = dtrace
30 OBJECTS          = $(DTRACE_OBJS:%=$(OBJS_DIR)/%)
31 LINTS            = $(DTRACE_OBJS:%.o=$(LINTS_DIR)/%.ln)
32 ROOTMODULE      = $(ROOT_DRV_DIR)/$(MODULE)
33 CONF_SRCDIR     = $(UTSBASE)/common/dtrace
```

```
35 include $(UTSBASE)/intel/Makefile.intel
```

```
37 #
38 # For now, disable these lint checks; maintainers should endeavor
39 # to investigate and remove these for maximum lint coverage.
40 # Please do not carry these forward to new Makefiles.
41 #
```

```
42 LINTTAGS        += -erroff=E_SUSPICIOUS_COMPARISON
43 LINTTAGS        += -erroff=E_BAD_PTR_CAST_ALIGN
44 LINTTAGS        += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
45 LINTTAGS        += -erroff=E_STATIC_UNUSED
46 LINTTAGS        += -erroff=E_PTRDIFF_OVERFLOW
47 LINTTAGS        += -erroff=E_ASSIGN_NARROW_CONV
49 CERRWARN        += -_gcc=-Wno-parentheses
50 CERRWARN        += -_gcc=-Wno-type-limits
51 CERRWARN        += -_gcc=-Wno-uninitialized
```

```
53 # needs work
```

```
54 $(OBJS_DIR)/dtrace.o := SMOFF += signed_integer_overflow_check,deref_check
```

```
56 CPPFLAGS        += -I$(SRC)/common/util
```

```
58 ALL_TARGET      = $(BINARY) $(SRC_CONFILE)
59 LINT_TARGET      = $(MODULE).lint
60 INSTALL_TARGET  = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
61 AS_INC_PATH     += -I$(DSF_DIR)/$(OBJS_DIR)
```

new/usr/src/uts/intel/dtrace/Makefile

2

```
63 ASSYM_H         = $(DSF_DIR)/$(OBJS_DIR)/assym.h
```

```
65 .KEEP_STATE:
```

```
67 def:           $(DEF_DEPS)
```

```
69 all:           $(ALL_DEPS)
```

```
71 clean:         $(CLEAN_DEPS)
```

```
73 clobber:       $(CLOBBER_DEPS)
```

```
75 lint:          $(LINT_DEPS)
```

```
77 modlintlib:   $(MODLINTLIB_DEPS)
```

```
79 clean.lint:   $(CLEAN_LINT_DEPS)
```

```
81 install:      $(INSTALL_DEPS)
```

```
83 $(BINARY):    $(ASSYM_H)
```

```
85 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/e1000g/Makefile

1

```
*****
2523 Mon Jan 7 13:08:12 2019
new/usr/src/uts/intel/e1000g/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
25 # uts/intel/e1000g/Makefile
26 #
27 # This makefile drives the production of the iprb
28 # network driver kernel module.
29 #
30 # intel architecture dependent
31 #
32 #
33 # Paths to the base of the uts directory trees
34 #
35 UTSBASE = ../../
36 #
37 # Define the module and object file sets.
38 #
39 MODULE = e1000g
40 OBJECTS = $(E1000G_OBJS:%=$(OBJS_DIR)/%)
41 OBJECTS += $(E1000API_OBJS:%=$(OBJS_DIR)/%)
42 LINTS = $(E1000G_OBJS:%.o=$(LINTS_DIR)/%.ln)
43 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
44 CONF_SRCDIR = $(UTSBASE)/common/io/e1000g
45 #
46 # Include common rules.
47 #
48 include $(UTSBASE)/intel/Makefile.intel
49 #
50 CFLAGS += -D_KERNEL -Di386 -DNEWSTAT -DNOMUT -DRCVWORKAROUND \
51 -DINTEL_IP \
52 -DPAXSON -DBAY_CITY \
53 -DTANAX_WORKAROUND -I$(UTSBASE)/common/io/e1000g \
54 -I$(UTSBASE)/common/io/e1000api
55 #
56 LINTFLAGS += \
57 -I$(UTSBASE)/common/io/e1000g \
```

new/usr/src/uts/intel/e1000g/Makefile

2

```
55 -I$(UTSBASE)/common/io/e1000api
56 CFLAGS_CXX = -xCC
57 CFLAGS_CXX += $(CFLAGS_CXX_COMMENTS)
58 #
59 CERRWARN += -_gcc=-Wno-uninitialized
60 CERRWARN += -_gcc=-Wno-parentheses
61 CERRWARN += -_gcc=-Wno-switch
62 CERRWARN += -_gcc=-Wno-unused-label
63 CERRWARN += -_gcc=-Wno-unused-variable
64 CERRWARN += -_gcc=-Wno-unused-variable
65 CERRWARN += -_gcc=-Wno-unused-variable
66 CERRWARN += -_cc=-erroff=E_STATEMENT_NOT_REACHED
67 #
68 SMOFF += all_func_returns,indenting,shift_to_zero
69 #
70 LINTTAGS += -erroff=E_CONSTANT_CONDITION
71 #
72 # Define targets
73 #
74 #
75 ALL_TARGET = $(BINARY) $(CONFMOD)
76 LINT_TARGET = $(MODULE).lint
77 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
78 #
79 #
80 # Driver depends on MAC
81 #
82 LDFLAGS += -dy -N misc/mac
83 #
84 # Default build targets.
85 #
86 #
87 .KEEP_STATE:
88 #
89 def: $(DEF_DEPS)
90 #
91 all: $(ALL_DEPS)
92 #
93 clean: $(CLEAN_DEPS)
94 #
95 clobber: $(CLOBBER_DEPS)
96 #
97 #
98 lint: $(LINT_DEPS)
99 #
100 modlintlib: $(MODLINTLIB_DEPS)
101 #
102 clean.lint: $(CLEAN_LINT_DEPS)
103 #
104 install: $(INSTALL_DEPS)
105 #
106 #
107 # Include common targets.
108 #
109 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2438 Mon Jan 7 13:08:15 2019
new/usr/src/uts/intel/ecc/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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21 #
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23 #
24 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
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26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 #
28 # This makefile drives the production of the ECc kef provider.
29 #
30 # intel implementation architecture dependent
31 #
32 #
33 # Path to the base of the uts directory tree (usually /usr/src/uts).
34 #
35 #
36 #
37 # Define the module and object file sets.
38 #
39 MODULE = ecc
40 OBJECTS = $(ECCPROV_OBJS:%=$(OBJDIR)/%)
41 LINTS = $(LINTS_DIR)/ecc.ln
42 ROOTMODULE = $(ROOT_CRYPTO_DIR)/$(MODULE)
43 #
44 #
45 # Include common rules.
46 #
47 include $(UTSBASE)/intel/Makefile.intel
48 #
49 #
50 # Define targets
51 #
52 ALL_TARGET = $(BINARY)
53 LINT_TARGET = $(MODULE).lint
54 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
55 #
56 #

```

```

57 # Linkage dependencies
58 #
59 LDFLAGS += -dy -Nmisc/kcf
60 #
61 CPPFLAGS += -I$(COM1_DIR) -I$(COM2_DIR)
62 #
63 CFLAGS += -DMP_API_COMPATIBLE -DNSS_ECC_MORE_THAN_SUITE_B
64 #
65 LINTFLAGS += -I$(COM1_DIR) -I$(COM2_DIR)
66 #
67 CERRWARN += -_gcc=-Wno-switch
68 CERRWARN += -_gcc=-Wno-unused-label
69 CERRWARN += -_gcc=-Wno-uninitialized
70 CERRWARN += -_gcc=-Wno-type-limits
71 CERRWARN += -_gcc=-Wno-empty-body
72 CERRWARN += -_gcc=-Wno-unused-variable
73 CERRWARN += -_gcc=-Wno-parentheses
74 #
75 # false positive
76 $(OBJDIR)/ecdecode.o := SMOFF += assign_vs_compare
77 $(OBJDIR)/ec.o := SMOFF += assign_vs_compare
78 #
79 SMOFF += indenting,all_func_returns
80 #
81 #
82 # Default build targets.
83 #
84 .KEEP_STATE:
85 #
86 def: $(DEF_DEPS)
87 #
88 all: $(ALL_DEPS)
89 #
90 clean: $(CLEAN_DEPS)
91 #
92 clobber: $(CLOBBER_DEPS)
93 #
94 lint: $(LINT_DEPS)
95 #
96 modlintlib: $(MODLINTLIB_DEPS)
97 #
98 clean.lint: $(CLEAN_LINT_DEPS)
99 #
100 install: $(INSTALL_DEPS)
101 #
102 #
103 # Include common targets.
104 #
105 include $(UTSBASE)/intel/Makefile.targ
106 #
107 include Makefile.$(CLASS)

```

```

*****
1982 Mon Jan 7 13:08:18 2019
new/usr/src/uts/intel/ehci/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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25 # Copyright (c) 2018, Joyent, Inc.
26 #
27 #
28 #
29 #
30 UTSBASE = ../../
31 #
32 #
33 # Define the module and object file sets.
34 #
35 MODULE = ehci
36 OBJECTS = $(EHCI_OBJS:%=$(OBJS_DIR)/%)
37 LINTS = $(EHCI_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
39 CONF_SRCDIR = $(UTSBASE)/common/io/usb/hcd/ehci
40 #
41 #
42 # Include common rules.
43 #
44 include $(UTSBASE)/intel/Makefile.intel
45 #
46 CERRWARN += -_gcc=-Wno-uninitialized
47 CERRWARN += -_gcc=-Wno-switch
48 #
49 # needs work
50 SMOFF += deref_check,shift_to_zero
51 #
52 #
53 # depends on misc/usba
54 #
55 LDFLAGS += -dy -Nmisc/usba
56 #
57 #
58 # Define targets

```

```

59 #
60 ALL_TARGET = $(BINARY)
61 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
62 LINT_TARGET = $(MODULE).lint
63 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
64 #
65 #
66 # Default build targets.
67 #
68 .KEEP_STATE:
69 #
70 def: $(DEF_DEPS)
71 #
72 all: $(ALL_DEPS)
73 #
74 clean: $(CLEAN_DEPS)
75 #
76 clobber: $(CLOBBER_DEPS)
77 #
78 lint: $(LINT_DEPS)
79 #
80 modlintlib: $(MODLINTLIB_DEPS)
81 #
82 clean.lint: $(CLEAN_LINT_DEPS)
83 #
84 install: $(INSTALL_DEPS)
85 #
86 #
87 # Include common targets.
88 #
89 include $(UTSBASE)/intel/Makefile.targ

```



new/usr/src/uts/intel/eibnx/Makefile

1

```
*****
2258 Mon Jan 7 13:08:20 2019
new/usr/src/uts/intel/eibnx/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
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19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright (c) 2010, Oracle and/or its affiliates. All rights reserved.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
26 #
27 #
28 # This makefile drives the production of the EoIB Nexus driver
29 #
30 # intel architecture dependent
31 #
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts)
35 #
36 UTSBASE = ../../
37 #
38 #
39 # Define the module and object file sets
40 #
41 MODULE          = eibnx
42 OBJECTS         = $(EIBNX_OBJS:%=$(OBJS_DIR)/%)
43 LINTS           = $(EIBNX_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE     = $(ROOT_DRV_DIR)/$(MODULE)
45 CONF_SRCDIR    = $(UTSBASE)/common/io/ib/clients/eoib
46 #
47 #
48 # Include common rules
49 #
50 include $(UTSBASE)/intel/Makefile.intel
51 #
52 #
53 # Define targets
54 #
55 ALL_TARGET      = $(BINARY) $(SRC_CONFILE)
56 LINT_TARGET     = $(MODULE).lint
57 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
58 #
59 # Module specific debug flag
60 #
61 CPPFLAGS += -DENX_DEBUG
```

new/usr/src/uts/intel/eibnx/Makefile

2

```
63 #
64 # Lint pass one enforcement
65 #
66 CFLAGS += $(CCVERBOSE)
67 #
68 #
69 # Depends on misc/ibt1
70 #
71 LDFLAGS += -dy -Nmisc/ibcm -Nmisc/ibt1
72 #
73 #
74 # The only lint flag we should need
75 #
76 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
77 #
78 CERRWARN += -_gcc=-Wno-parentheses
79 CERRWARN += -_gcc=-Wno-uninitialized
80 #
81 # needs work
82 $(OBJS_DIR)/enx_ibt.o := SMOFF += deref_check
83 #
84 #
85 # Default build targets
86 #
87 .KEEP_STATE:
88 #
89 def:          $(DEF_DEPS)
90 #
91 all:         $(ALL_DEPS)
92 #
93 clean:       $(CLEAN_DEPS)
94 #
95 clobber:     $(CLOBBER_DEPS)
96 #
97 lint:        $(LINT_DEPS)
98 #
99 modlintlib:  $(MODLINTLIB_DEPS)
100 #
101 clean.lint:  $(CLEAN_LINT_DEPS)
102 #
103 install:     $(INSTALL_DEPS)
104 #
105 #
106 # Include common targets
107 #
108 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/emlxs/Makefile

1

```
*****
3526 Mon Jan 7 13:08:22 2019
new/usr/src/uts/intel/emlxs/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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25 # Copyright (c) 2018, Joyent, Inc.
26 #
27 # This makefile drives the production of the emlxs driver kernel module.
28 #
29 # Path to the base of the uts directory tree (usually /usr/src/uts).
30 #
31 UTSBASE = ../../
32 COMMON_BASE = ../.././common

34 #
35 # Define the module and object file sets.
36 #
37 MODULE = emlxs
38 OBJECTS = $(EMLXS_OBJS:%=$(OBJDIR)/%)
39 LINTS = $(EMLXS_OBJS:%.o=$(LINTDIR)/%.ln)
40 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
41 CONF_SRCDIR = $(UTSBASE)/common/io/fibre-channel/fca/emlxs

43 #
44 # Include common rules.
45 #
46 ARCHDIR = intel
47 include ../Makefile.$(ARCHDIR)

49 #
50 # Define targets
51 #
52 ALL_TARGET = $(BINARY) $(SRC_CONFILE)
53 LINT_TARGET = $(MODULE).lint
54 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

56 EMLXS_FLAGS = -DEMLXS_I386
57 EMLXS_FLAGS += -DS11
58 EMLXS_FLAGS += -DVERSION="\11\"
59 EMLXS_FLAGS += -DMACH="\$(MACH)\\"
60 EMLXS_CFLAGS = $(EMLXS_FLAGS)
61 EMLXS_LFLAGS = $(EMLXS_FLAGS)
```

new/usr/src/uts/intel/emlxs/Makefile

2

```
62 CFLAGS += $(EMLXS_CFLAGS) -DEMLXS_ARCH="\$(CLASS)\\"
63 LINTTAGS += $(EMLXS_LFLAGS) -DEMLXS_ARCH="\$(CLASS)\\"

66 #
67 # Overrides and depends_on
68 #
69 INC_PATH += -I$(ROOT)/usr/include
70 INC_PATH += -I$(UTSBASE)/common/sys
71 INC_PATH += -I$(COMMON_BASE)/bignum
72 INC_PATH += -I$(UTSBASE)/common/sys/fibre-channel
73 INC_PATH += -I$(UTSBASE)/common/sys/fibre-channel/fca
74 INC_PATH += -I$(UTSBASE)/common/sys/fibre-channel/fca/emlxs
75 INC_PATH += -I$(UTSBASE)/common/sys/fibre-channel/impl
76 INC_PATH += -I$(UTSBASE)/common/sys/fibre-channel/ulp

78 #
79 # misc/fctl required because #ifdef MODSYM_LOAD code
80 # triggered by -DS11; uses DDI calls to load FCA symbols
81 #
82 LDFLAGS += -dy -Nmisc/md5 -Nmisc/shal
83 LDFLAGS += -Nmisc/bignum -Nmisc/fctl

85 #
86 # For now, disable these lint checks; maintainers should endeavor
87 # to investigate and remove these for maximum lint coverage.
88 #
89 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
90 LINTTAGS += -erroff=E_STATIC_UNUSED
91 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
92 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
93 LINTTAGS += -erroff=E_INCONS_VAL_TYPE_DECL2

95 CERRWARN += _gcc=-Wno-parentheses
96 CERRWARN += _gcc=-Wno-unused-label
97 CERRWARN += _gcc=-Wno-uninitialized

99 SMOFF += indenting,deref_check,all_func_returns

101 # seems definitely wrong
102 $(OBJDIR)/emlxs_fcf.o := SMOFF += logical_instead_of_bitwise

104 #
105 # Default build targets.
106 #
107 .KEEP_STATE:

109 def: $(DEF_DEPS)

111 all: $(ALL_DEPS)

113 clean: $(CLEAN_DEPS)

115 clobber: $(CLOBBER_DEPS)

117 lint: $(LINT_DEPS)

119 modlintlib: $(MODLINTLIB_DEPS)

121 clean.lint: $(CLEAN_LINT_DEPS)

123 install: $(INSTALL_DEPS)

125 #
126 # Include common targets.
127 #
```

**new/usr/src/uts/intel/emlxs/Makefile**

**3**

```
128 include ../Makefile.targ
```

```

*****
2444 Mon Jan 7 13:08:23 2019
new/usr/src/uts/intel/emul64/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
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22 # Use is subject to license terms.
23 # Copyright (c) 2011 Bayard G. Bell. All rights reserved.
24 # Copyright (c) 2018, Joyent, Inc.
25 #
26 # This makefile drives the production of the emul64 kernel
27 # driver to testing hotplugging operations
28 #
29 #
30 #
31 # Path to the base of the uts directory tree (usually /usr/src/uts).
32 #
33 UTSBASE = ../..
34 #
35 #
36 # Define the module and object file sets.
37 #
38 MODULE = emul64
39 OBJECTS = $(EMUL64_OBJS:%=$(OBJSDIR)/%)
40 LINTS = $(EMUL64_OBJS:%.o=$(LINTSDIR)/%.ln)
41 ROOTMODULE = $(USR_DRV_DIR)/$(MODULE)
42 CONF_SRCDIR = $(UTSBASE)/common/io
43 #
44 #
45 # Include common rules.
46 #
47 include $(UTSBASE)/intel/Makefile.intel
48 #
49 CLOBBERFILES += $(MODULE)
50 CFLAGS += $(CCVERBOSE)
51 #
52 #
53 # Define targets
54 #
55 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
56 LINT_TARGET = $(MODULE).lint
57 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
58 #
59 #
60 # lint pass one enforcement
61 #

```

```

62 CFLAGS += $(CCVERBOSE)
63 #
64 #
65 # For now, disable these lint checks; maintainers should endeavor
66 # to investigate and remove these for maximum lint coverage.
67 # Please do not carry these forward to new Makefiles.
68 #
69 LINTTAGS += -eroff=E_BAD_PTR_CAST_ALIGN
70 #
71 CERRWARN += -_gcc=-Wno-unused-label
72 CERRWARN += -_gcc=-Wno-uninitialized
73 #
74 # needs work
75 SMOFF += indenting,shift_to_zero
76 #
77 #
78 # Depends on scsi
79 #
80 LDFLAGS += -dy -N misc/scsi
81 #
82 #
83 # Default build targets.
84 #
85 .KEEP_STATE:
86 #
87 def: $(DEF_DEPS)
88 #
89 all: $(ALL_DEPS)
90 #
91 clean: $(CLEAN_DEPS)
92 #
93 clobber: $(CLOBBER_DEPS)
94 #
95 lint: $(LINT_DEPS)
96 #
97 modlintlib: $(MODLINTLIB_DEPS)
98 #
99 clean.lint: $(CLEAN_LINT_DEPS)
100 #
101 install: $(INSTALL_DEPS)
102 #
103 #
104 # Include common targets.
105 #
106 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2108 Mon Jan 7 13:08:26 2019
new/usr/src/uts/intel/eoib/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
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21 #
22 #
23 # Copyright (c) 2010, Oracle and/or its affiliates. All rights reserved.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
26 #
27 #
28 # This makefile drives the production of the EoIB Nexus driver
29 #
30 # intel architecture dependent
31 #
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts)
35 #
36 UTSBASE = ../../
37 #
38 #
39 # Define the module and object file sets
40 #
41 MODULE = eoib
42 OBJECTS = $(EOIB_OBJS:%=$(OBJDIR)/%)
43 LINTS = $(EOIB_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
45 #
46 #
47 # Include common rules
48 #
49 include $(UTSBASE)/intel/Makefile.intel
50 #
51 #
52 # Define targets
53 #
54 ALL_TARGET = $(BINARY)
55 LINT_TARGET = $(MODULE).lint
56 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
57 #
58 # Module specific debug flag
59 #
60 CPPFLAGS += -DEIB_DEBUG

```

```

62 #
63 # Lint pass one enforcement
64 #
65 CFLAGS += $(CCVERBOSE)
66 #
67 CERRWARN += -_gcc=-Wno-switch
68 CERRWARN += -_gcc=-Wno-uninitialized
69 #
70 # needs work
71 $(OBJDIR)/eib_ibt.o := SMOFF += deref_check
72 #
73 #
74 # Depends on misc/ibt1
75 #
76 LDFLAGS += -dy -Nmisc/mac -Nmisc/ibt1 -Nmisc/ibcm -Nmisc/ibmf
77 #
78 #
79 # Default build targets
80 #
81 .KEEP_STATE:
82 #
83 def: $(DEF_DEPS)
84 #
85 all: $(ALL_DEPS)
86 #
87 clean: $(CLEAN_DEPS)
88 #
89 clobber: $(CLOBBER_DEPS)
90 #
91 lint: $(LINT_DEPS)
92 #
93 modlintlib: $(MODLINTLIB_DEPS)
94 #
95 clean.lint: $(CLEAN_LINT_DEPS)
96 #
97 install: $(INSTALL_DEPS)
98 #
99 #
100 # Include common targets
101 #
102 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/fcip/Makefile

1

```
*****
2278 Mon Jan 7 13:08:28 2019
new/usr/src/uts/intel/fcip/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 # uts/intel/fcip/Makefile
22 #
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of fcip driver kernel module.
28 # i86pc architecture dependent
29 #
30 #
31 # Path to the base of the uts directory tree (usually /usr/src/uts).
32 #
33 #
34 # Define the module and object file sets.
35 #
36 MODULE = fcip
37 OBJECTS = $(FCIP_OBJS:%=$(OBJS_DIR)/%)
38 LINTS = $(FCIP_OBJS:%.o=$(LINTS_DIR)/%.ln)
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
40 CONF_SRCDIR = $(UTSBASE)/common/io/fibre-channel/ulp/
41 #
42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/Makefile.uts
46 include $(UTSBASE)/intel/Makefile.intel
47 #
48 #
49 # Define targets
50 #
51 ALL_TARGET = $(BINARY) $(CONFMOD) $(ITUMOD)
52 LINT_TARGET = $(MODULE).lint
53 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
54 #
55 #
56 # header file directories
57 #
```

new/usr/src/uts/intel/fcip/Makefile

2

```
58 INC_PATH += -I$(SRC)/uts/common
59 #
60 LDFLAGS += -dy -Nmisc/fctl -Ndrv/ip
61 #
62 #
63 # Lint tags
64 #
65 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
66 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
67 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
68 LINTTAGS += -erroff=E_INCONS_ARG_USED2
69 LINTTAGS += -erroff=E_INCONS_VAL_TYPE_USED2
70 #
71 CERRWARN += -_gcc=-Wno-parentheses
72 CERRWARN += -_gcc=-Wno-type-limits
73 #
74 # needs work
75 SMATCH=off
76 #
77 #
78 # Default build targets.
79 #
80 .KEEP_STATE:
81 #
82 def: $(DEF_DEPS)
83 #
84 all: $(ALL_DEPS)
85 #
86 clean: $(CLEAN_DEPS)
87 #
88 clobber: $(CLOBBER_DEPS)
89 #
90 lint: $(LINT_DEPS)
91 #
92 modlintlib: $(MODLINTLIB_DEPS)
93 #
94 clean.lint: $(CLEAN_LINT_DEPS)
95 #
96 install: $(INSTALL_DEPS)
97 #
98 #
99 # Include common targets.
100 #
101 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/fcoei/Makefile

1

```
*****
1981 Mon Jan 7 13:08:30 2019
new/usr/src/uts/intel/fcoei/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
25 # This makefile drives the production of the fcoei driver for
26 # LEADVILLE.

27 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTSBASE = ../../
31 ARCHDIR:sh = cd ../; basename `pwd`

33 #
34 # Define the module and object file sets.
35 #
36 MODULE = fcoei
37 OBJECTS = $(FCOEI_OBJS:%=$(OBJS_DIR)/%)
38 LINTS = $(FCOEI_OBJS:%.o=$(LINTS_DIR)/%.ln)
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
40 CONF_SRCDIR = $(UTSBASE)/common/io/fibre-channel/fca/fcoei

42 # needs work
43 SMOFF += all_func_returns

45 #
46 # Include common rules.
47 #
48 include ../Makefile.$(ARCHDIR)

50 #
51 # Define targets
52 #
53 ALL_TARGET = $(BINARY) $(SRC_CONFILE)
54 LINT_TARGET = $(MODULE).lint
55 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

57 #
58 # Overrides and depends_on
59 #
```

new/usr/src/uts/intel/fcoei/Makefile

2

```
60 LDFLAGS += -dy -Nmisc/fctl -Ndrv/fcoe
61 INC_PATH += -I$(UTSBASE)/common/io/fibre-channel/fca/fcoei

63 #
64 # Default build targets.
65 #
66 .KEEP_STATE:

68 def: $(DEF_DEPS)

70 all: $(ALL_DEPS)

72 clean: $(CLEAN_DEPS)

74 clobber: $(CLOBBER_DEPS)

76 lint: $(LINT_DEPS)

78 modlintlib: $(MODLINTLIB_DEPS)

80 clean.lint: $(CLEAN_LINT_DEPS)

82 install: $(INSTALL_DEPS)

84 #
85 # Include common targets.
86 #
87 include ../Makefile.targ
```

new/usr/src/uts/intel/fcoet/Makefile

1

```
*****
1891 Mon Jan 7 13:08:33 2019
new/usr/src/uts/intel/fcoet/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 # uts/intel/fcoet/Makefile
22 #
23 # Copyright (c) 2009, 2010, Oracle and/or its affiliates. All rights reserved.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
26 #

27 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTBASE = ../..
31 ARCHDIR:sh = cd ../; basename `pwd`

33 #
34 # Define the module and object file sets.
35 #
36 MODULE = fcoet
37 OBJECTS = $(FCOET_OBJS:%=$(OBJS_DIR)/%)
38 LINTS = $(FCOET_OBJS:%.o=$(LINTS_DIR)/%.ln)
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

41 # needs work
42 SMOFF += all_func_returns,shift_to_zero

44 #
45 # Include common rules.
46 #
47 include ../Makefile.$(ARCHDIR)

49 #
50 # Define targets
51 #
52 ALL_TARGET = $(BINARY)
53 LINT_TARGET = $(MODULE).lint
54 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

56 #
57 # Overrides and depends_on
58 #
59 MODSTUBS_DIR = $(OBJS_DIR)
```

new/usr/src/uts/intel/fcoet/Makefile

2

```
60 LDFLAGS += -dy -Ndrv/stmf -Ndrv/fct -Ndrv/fcoe

62 #
63 # Default build targets.
64 #
65 .KEEP_STATE:

67 def: $(DEF_DEPS)

69 all: $(ALL_DEPS)

71 clean: $(CLEAN_DEPS)

73 clobber: $(CLOBBER_DEPS)

75 lint: $(LINT_DEPS)

77 modlintlib: $(MODLINTLIB_DEPS)

79 clean.lint: $(CLEAN_LINT_DEPS)

81 install: $(INSTALL_DEPS)

83 #
84 # Include common targets.
85 #
86 include ../Makefile.targ
```



new/usr/src/uts/intel/fcp/Makefile

1

```
*****
2153 Mon Jan 7 13:08:36 2019
new/usr/src/uts/intel/fcp/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 # uts/intel/fcp/Makefile
22 #
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of fcp driver kernel module.
28 #
29 # Path to the base of the uts directory tree (usually /usr/src/uts).
30 #
31 UTSBASE = ../../
32 ARCHDIR:sh = cd ../; basename `pwd`
33 #
34 # Define the module and object file sets.
35 #
36 #
37 MODULE = fcp
38 OBJECTS = $(FCP_OBJS:%=$(OBJS_DIR)/%)
39 LINTS = $(FCP_OBJS:%.o=$(LINTS_DIR)/%.ln)
40 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
41 CONF_SRCDIR = $(UTSBASE)/common/io/fibre-channel/ulp
42 #
43 # Include common rules.
44 #
45 #
46 include ../Makefile.$(ARCHDIR)
47 #
48 # Define targets
49 #
50 #
51 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
52 LINT_TARGET = $(MODULE).lint
53 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
54 #
55 # Overrides
56 #
57 #
58 LDFLAGS += -dy -Nmisc/fctl -Nmisc/scsi
```

new/usr/src/uts/intel/fcp/Makefile

2

```
60 #
61 # Lint tags
62 #
63 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
64 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
65 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
66 #
67 CERRWARN += -_gcc=-Wno-parentheses
68 CERRWARN += -_gcc=-Wno-switch
69 CERRWARN += -_gcc=-Wno-uninitialized
70 #
71 SMATCH=off
72 #
73 # Default build targets.
74 #
75 #
76 .KEEP_STATE:
77 #
78 def: $(DEF_DEPS)
79 #
80 all: $(ALL_DEPS)
81 #
82 clean: $(CLEAN_DEPS)
83 #
84 clobber: $(CLOBBER_DEPS)
85 #
86 lint: $(LINT_DEPS)
87 #
88 modlintlib: $(MODLINTLIB_DEPS)
89 #
90 clean.lint: $(CLEAN_LINT_DEPS)
91 #
92 install: $(INSTALL_DEPS)
93 #
94 # Include common targets.
95 #
96 #
97 include ../Makefile.targ
```

new/usr/src/uts/intel/fct/Makefile

1

```
*****
2290 Mon Jan 7 13:08:38 2019
new/usr/src/uts/intel/fct/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright (c) 2008, 2010, Oracle and/or its affiliates. All rights reserved.
23 #
24 # Copyright (c) 2018, Joyent, Inc.
25 # This makefile drives the production of the fct driver for
26 # COMSTAR.
27 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 #
31 UTSBASE = ../..
32 #
33 ARCHDIR:sh = cd ../; basename `pwd`
34 #
35 #
36 # Define the module and object file sets.
37 #
38 MODULE = fct
39 OBJECTS = $(FCT_OBJS:%=$(OBJSDIR)/%)
40 LINTS = $(FCT_OBJS:%.o=$(LINTSDIR)/%.ln)
41 ROOTMODULE = $(ROOTDRVDIR)/$(MODULE)
42 CONF_SRCDIR = $(UTSBASE)/common/io/comstar/port/fct
43 #
44 #
45 # Include common rules.
46 #
47 include ../Makefile.$(ARCHDIR)
48 #
49 #
50 # Define targets
51 #
52 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
53 LINT_TARGET = $(MODULE).lint
54 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
55 #
56 #
57 # Overrides and depends_on
58 #
59 MODSTUBS_DIR = $(OBJSDIR)
```

new/usr/src/uts/intel/fct/Makefile

2

```
60 LDFLAGS += -dy -Ndrv/stmf
62 C99LMODE= -Xc99=%all
63 #
64 #
65 # For now, disable these lint checks; maintainers should endeavor
66 # to investigate and remove these for maximum lint coverage.
67 #
68 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
69 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
70 LINTTAGS += -erroff=E_IF_ELSE_ANNOTATION
71 #
72 CERRWARN += -_gcc=-Wno-unused-label
73 CERRWARN += -_gcc=-Wno-switch
74 CERRWARN += -_gcc=-Wno-uninitialized
75 #
76 # needs work
77 SMOFF += strcpy_overflow
78 #
79 #
80 # Default build targets.
81 #
82 .KEEP_STATE:
83 #
84 def: $(DEF_DEPS)
85 #
86 all: $(ALL_DEPS)
87 #
88 clean: $(CLEAN_DEPS)
89 #
90 clobber: $(CLOBBER_DEPS)
91 #
92 lint: $(LINT_DEPS)
93 #
94 modlintlib: $(MODLINTLIB_DEPS)
95 #
96 clean.lint: $(CLEAN_LINT_DEPS)
97 #
98 install: $(INSTALL_DEPS)
99 #
100 #
101 # Include common targets.
102 #
103 include ../Makefile.targ
```

new/usr/src/uts/intel/fctl/Makefile

1

```
*****
2101 Mon Jan 7 13:08:41 2019
new/usr/src/uts/intel/fctl/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 # uts/intel/fctl/Makefile
22 #
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
26 # ident "@(#)Makefile 1.10 08/07/25 SMI"
27 #
28 # This makefile drives the production of fctl driver kernel module.
29 #
30 # i86pc architecture dependent
31 #

28 #
29 # Path to the base of the uts directory tree (usually /usr/src/uts).
30 #
31 UTBASE = ../../

33 #
34 # Define the module and object file sets.
35 #
36 MODULE = fctl
37 OBJECTS = ${FCTL_OBJS:%=${OBJS_DIR}/%}
38 LINTS = ${FCTL_OBJS:%.o=${LINTS_DIR}/%.ln}
39 ROOTMODULE = ${ROOT_MISC_DIR}/${MODULE}

41 #
42 # Include common rules.
43 #
44 include ${UTSBASE}/Makefile.uts
45 include ${UTSBASE}/intel/Makefile.intel

47 #
48 # Define targets
49 #
50 ALL_TARGET = ${BINARY} ${CONFMOD} ${ITUMOD}
51 LINT_TARGET = ${MODULE}.lint
52 INSTALL_TARGET = ${BINARY} ${ROOTMODULE}

54 #
55 # header file directories
```

new/usr/src/uts/intel/fctl/Makefile

2

```
56 #
57 INCS += -I${SRC}/uts/common/

59 #
60 # Lint tags
61 #
62 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
63 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
64 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON

66 CERRWARN += -_gcc=-Wno-uninitialized

68 # needs work
69 ${OBJS_DIR}/fctl.o := SMOFF += all_func_returns,deref_check

71 #
72 # Default build targets.
73 #
74 .KEEP_STATE:

76 def: $(DEF_DEPS)

78 all: $(ALL_DEPS)

80 clean: $(CLEAN_DEPS)

82 clobber: $(CLOBBER_DEPS)

84 lint: $(LINT_DEPS)

86 modlintlib: $(MODLINTLIB_DEPS)

88 clean.lint: $(CLEAN_LINT_DEPS)

90 install: $(INSTALL_DEPS)

92 #
93 # Include common targets.
94 #
95 include ${UTSBASE}/intel/Makefile.targ
```

new/usr/src/uts/intel/fd/Makefile

1

```
*****
2215 Mon Jan 7 13:08:43 2019
new/usr/src/uts/intel/fd/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/fd/Makefile
23 #
24 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
28 #
29 #
30 # This makefile drives the production of the floppy driver
31 # intel architecture dependent
32 #
33 #
34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../../
38 #
39 #
40 # Define the module and object file sets.
41 #
42 #
43 MODULE = fd
44 OBJECTS = $(FD_OBJS:%=$(OBJS_DIR)/%)
45 LINTS = $(FD_OBJS:%.o=$(LINTS_DIR)/%.lint)
46 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
47 CONF_SRCDIR = $(UTSBASE)/intel/io
48 #
49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel
53 #
54 #
55 # Define targets
56 #
57 #
58 ALL_TARGET = $(BINARY) $(CONFMOD)
59 LINT_TARGET = $(MODULE).lint
60 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
61 #
62 #
```

new/usr/src/uts/intel/fd/Makefile

2

```
56 # Overrides.
57 #
58 #
59 #
60 # For now, disable these lint checks; maintainers should endeavor
61 # to investigate and remove these for maximum lint coverage.
62 # Please do not carry these forward to new Makefiles.
63 #
64 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
65 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
66 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
67 #
68 CERRWARN += -_gcc=-Wno-uninitialized
69 #
70 # needs work
71 $(OBJS_DIR)/fd.o := SMOFF += all_func_returns
72 #
73 #
74 # Default build targets.
75 #
76 .KEEP_STATE:
77 #
78 def: $(DEF_DEPS)
79 #
80 all: $(ALL_DEPS)
81 #
82 clean: $(CLEAN_DEPS)
83 #
84 clobber: $(CLOBBER_DEPS)
85 #
86 lint: $(LINT_DEPS)
87 #
88 modlintlib: $(MODLINTLIB_DEPS)
89 #
90 clean.lint: $(CLEAN_LINT_DEPS)
91 #
92 install: $(INSTALL_DEPS)
93 #
94 #
95 # Include common targets.
96 #
97 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/fp/Makefile

1

```
*****
2332 Mon Jan 7 13:08:45 2019
new/usr/src/uts/intel/fp/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 # uts/intel/fp/Makefile
22 #
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
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25 #
26 # Copyright (c) 2018, Joyent, Inc.
26 # ident "@(#)Makefile 1.10 08/07/25 SMI"
27 #
28 # This makefile drives the production of fp driver kernel module.
29 #
30 # i86pc architecture dependent
31 #
32 #
28 #
29 # Path to the base of the uts directory tree (usually /usr/src/uts).
30 #
31 UTSBASE = ../../
32 #
33 #
34 # Define the module and object file sets.
35 #
36 MODULE = fp
37 OBJECTS = ${FP_OBJS:%=${OBJDIR}/%}
38 LINTS = ${FP_OBJS:%.o=${LINTSDIR}/%.ln}
39 ROOTMODULE = ${ROOT_DRV_DIR}/${MODULE}
40 CONF_SRCDIR = ${UTSBASE}/intel/io/fibre-channel/impl/
41 #
42 #
43 # Include common rules.
44 #
45 include ${UTSBASE}/Makefile.uts
46 include ${UTSBASE}/intel/Makefile.intel
47 #
48 #
49 # Define targets
50 #
51 #ALL_TARGET = ${BINARY} ${CONFMOD} ${ITUMOD}
52 ALL_TARGET = ${BINARY}
53 LINT_TARGET = ${MODULE}.lint
54 INSTALL_TARGET = ${BINARY} ${ROOTMODULE} ${ROOT_CONF_FILE}
```

new/usr/src/uts/intel/fp/Makefile

2

```
56 #
57 # header file directories
58 #
59 INCS += -I${SRC}/uts/common/
60 #
61 #
62 # Overrides
63 #
64 LDFLAGS += -dy -Ndrv/fcp -Nmisc/fctl -Nmisc/scsi
65 #
66 #
67 # Lint tags
68 #
69 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
70 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
71 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
72 LINTTAGS += -erroff=E_STATIC_UNUSED
73 #
74 CERRWARN += -_gcc=-Wno-uninitialized
75 CERRWARN += -_gcc=-Wno-unused-function
76 #
77 # needs work
78 ${OBJDIR}/fp.o := SMOFF += all_func_returns,deref_check
79 #
80 #
81 # Default build targets.
82 #
83 .KEEP_STATE:
84 #
85 def: ${DEF_DEPS}
86 #
87 all: ${ALL_DEPS}
88 #
89 clean: ${CLEAN_DEPS}
90 #
91 clobber: ${CLOBBER_DEPS}
92 #
93 lint: ${LINT_DEPS}
94 #
95 modlintlib: ${MODLINTLIB_DEPS}
96 #
97 clean.lint: ${CLEAN_LINT_DEPS}
98 #
99 install: ${INSTALL_DEPS}
100 #
101 #
102 # Include common targets.
103 #
104 include ${UTSBASE}/intel/Makefile.targ
```

new/usr/src/uts/intel/gen\_drv/Makefile

1

```
*****
2072 Mon Jan 7 13:08:48 2019
new/usr/src/uts/intel/gen_drv/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
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5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
27 # This makefile drives the production of the gen_drv kernel
28 # driver to testing hotplugging operations
29 #

31 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTSBASE = ../../

32 #
33 # Define the module and object file sets.
34 #
35 MODULE = gen_drv
36 OBJECTS = $(GEN_DRV_OBJS:%=$(OBJS_DIR)/%)
37 LINTS = $(GEN_DRV_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(USR_DRV_DIR)/$(MODULE)

40 #
41 # Include common rules.
42 #
43 include $(UTSBASE)/intel/Makefile.intel

45 CLOBBERFILES += $(MODULE)

47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

54 #
55 # For now, disable these lint checks; maintainers should endeavor
56 # to investigate and remove these for maximum lint coverage.
```

new/usr/src/uts/intel/gen\_drv/Makefile

2

```
57 # Please do not carry these forward to new Makefiles.
58 #
59 LINTTAGS += -eroff=E_STATIC_UNUSED

61 CERRWARN += -_gcc=-Wno-unused-variable

63 # needs work
64 $(OBJS_DIR)/gen_drv.o := SMOFF += indenting,deref_check

66 #
67 # Default build targets.
68 #
69 .KEEP_STATE:

71 def: $(DEF_DEPS)

73 all: $(ALL_DEPS)

75 clean: $(CLEAN_DEPS)

77 clobber: $(CLOBBER_DEPS)

79 lint: $(LINT_DEPS)

81 modlintlib: $(MODLINTLIB_DEPS)

83 clean.lint: $(CLEAN_LINT_DEPS)

85 install: $(INSTALL_DEPS)

87 #
88 # Include common targets.
89 #
90 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
4677 Mon Jan 7 13:08:49 2019
new/usr/src/uts/intel/genunix/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 #
28 #
29 # This makefile drives the production of the generic
30 # unix kernel module.
31 #
32 # x86 implementation architecture dependent
33 #
34 #
35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../
39 #
40 #
41 # Define the module and object file sets.
42 #
43 MODULE = genunix
44 GENUNIX = $(OBJS_DIR)/$(MODULE)
45 #
46 OBJECTS = $(GENUNIX_OBJS:%=$(OBJS_DIR)/%) \
47 $(NOT_YET_KMODS:%=$(OBJS_DIR)/%)
48 #
49 LINTS = $(GENUNIX_OBJS:%.o=$(LINTS_DIR)/%.ln) \
50 $(NOT_YET_KMODS:%.o=$(LINTS_DIR)/%.ln)
51 #
52 ROOTMODULE = $(ROOT_KERN_DIR)/$(MODULE)
53 #
54 LIBGEN = $(OBJS_DIR)/libgenunix.so
55 LIBSTUBS = $(GENSTUBS_OBJS:%=$(OBJS_DIR)/%)
56 #
57 #
58 # Include common rules.
59 #
60 include $(UTSBASE)/intel/Makefile.intel

```

```

62 #
63 # Define targets
64 #
65 ALL_TARGET = $(LIBGEN) $(GENUNIX)
66 LINT_TARGET = $(MODULE).lint
67 INSTALL_TARGET = $(LIBGEN) $(GENUNIX) $(ROOTMODULE)
68 #
69 #
70 # Overrides
71 #
72 CLOBBERFILES += $(GENUNIX)
73 CLEANFILES += $(LIBSTUBS) $(LIBGEN)
74 BINARY =
75 #
76 #
77 # Non-patch genunix builds merge a version of the ip module called ipctf. This
78 # is to ensure that the common network-related types are included in genunix and
79 # can thus be unquified out of other modules. We don't want to do this for
80 # patch builds, since we can't guarantee that ip and genunix will be in the same
81 # patch.
82 #
83 IPCTF_TARGET = $(IPCTF)
84 $(PATCH_BUILD)IPCTF_TARGET =
85 #
86 CPPFLAGS += -I$(SRC)/common
87 CPPFLAGS += -I$(SRC)/uts/common/fs/zfs
88 #
89 CPPFLAGS += -I$(UTSBASE)/i86pc
90 #
91 #
92 # For now, disable these lint checks; maintainers should endeavor
93 # to investigate and remove these for maximum lint coverage.
94 # Please do not carry these forward to new Makefiles.
95 #
96 LINTTAGS += -errorf=E_SUSPICIOUS_COMPARISON
97 LINTTAGS += -errorf=E_BAD_PTR_CAST_ALIGN
98 LINTTAGS += -errorf=E_SUPPRESSION_DIRECTIVE_UNUSED
99 LINTTAGS += -errorf=E_STATIC_UNUSED
100 LINTTAGS += -errorf=E_PTRDIFF_OVERFLOW
101 LINTTAGS += -errorf=E_ASSIGN_NARROW_CONV
102 #
103 CERRWARN += -_gcc=-Wno-unused-label
104 CERRWARN += -_gcc=-Wno-unused-variable
105 CERRWARN += -_gcc=-Wno-unused-value
106 CERRWARN += -_gcc=-Wno-unused-function
107 CERRWARN += -_gcc=-Wno-parentheses
108 CERRWARN += -_gcc=-Wno-switch
109 CERRWARN += -_gcc=-Wno-type-limits
110 CERRWARN += -_gcc=-Wno-uninitialized
111 CERRWARN += -_gcc=-Wno-clobbered
112 CERRWARN += -_gcc=-Wno-empty-body
113 #
114 # false positives
115 SMOFF += index_overflow
116 $(OBJS_DIR)/seg_vn.o := SMOFF += deref_check
117 $(OBJS_DIR)/ddi_intr_irm.o := SMOFF += deref_check
118 #
119 # need work still
120 SMOFF += signed_indenting_all_func_returns
121 $(OBJS_DIR)/clock_highres.o := SMOFF += signed_integer_overflow_check
122 $(OBJS_DIR)/evchannels.o := SMOFF += allocating_enough_data
123 $(OBJS_DIR)/klpd.o := SMOFF += cast_assign
124 $(OBJS_DIR)/lookup.o := SMOFF += strcpy_overflow
125 $(OBJS_DIR)/process.o := SMOFF += or_vs_and
126 $(OBJS_DIR)/sunpci.o := SMOFF += deref_check
127 $(OBJS_DIR)/timers.o := SMOFF += signed_integer_overflow_check

```

```
129 # definitely wrong
130 $(OBJS_DIR)/acl_common.o := SMOFF += or_vs_and

132 #
133 # Ensure that lint sees 'struct cpu' containing a fully declared
134 # embedded 'struct machcpu'
135 #
136 LINTFLAGS      += -D_MACHDEP -I../.. /i86pc

138 #
139 #      Default build targets.
140 #
141 .KEEP_STATE:

143 def:           $(DEF_DEPS)
145 all:           $(ALL_DEPS)
147 clean:        $(CLEAN_DEPS)
149 clobber:      $(CLOBBER_DEPS)
151 lint:         $(LINT_DEPS)
153 modlintlib:   $(MODLINTLIB_DEPS)
155 clean.lint:   $(CLEAN_LINT_DEPS)
157 install:     $(INSTALL_DEPS)

159 $(LIBGEN):    $(GENUNIX) $(LIBSTUBS)
160              $(BUILD.SO) $(GENUNIX) $(LIBSTUBS)

162 $(IPCTF_TARGET) ipctf_target: FRC
163              @cd $(IPDRV_DIR); pwd; $(MAKE) ipctf.$(OBJS_DIR)
164              @pwd

166 $(GENUNIX):  $(IPCTF_TARGET) $(OBJECTS)
167              $(LD) -r $(LD_FLAGS) -o $@ $(OBJECTS)
168              $(CTFMERGE_GENUNIX_MERGE)
169              $(POST_PROCESS)

171 #
172 #      Include common targets.
173 #
174 include $(UTSBASE)/intel/Makefile.targ

176 #
177 #      Software workarounds for hardware "features".
178 #
179 include $(UTSBASE)/i86pc/Makefile.workarounds

181 ALL_DEFS += $(WORKAROUND_DEFS)

183 #
184 #      Override.
185 #
186 $(MODULE).lint := GEN_LINT_LIB =
```



new/usr/src/uts/intel/hermon/Makefile

1

```
*****
2249 Mon Jan 7 13:08:52 2019
new/usr/src/uts/intel/hermon/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the hermon InfiniBand module
28 # intel architecture dependent
29 #
30 # uts/intel/hermon/Makefile
31 #
32 #
33 # Path to the base of the uts directory tree (usually /usr/src/uts).
34 #
35 #
36 # UTSBASE = ../../
37 #
38 # Define the module and object file sets.
39 #
40 # MODULE = hermon
41 # OBJECTS = $(HERMON_OBJS:%=$(OBJDIR)/%)
42 # LINTS = $(HERMON_OBJS:%.o=$(LINTSDIR)/%.ln)
43 # ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
44 # CONF_SRCDIR = $(UTSBASE)/common/io/ib/adapters/hermon
45 # LDFFLAGS += -dy -Ndrv/ib -Nmisc/ibt1 -Nmisc/ibmf
46 #
47 # Include common rules.
48 #
49 # include $(UTSBASE)/intel/Makefile.intel
50 #
51 # Define targets
52 #
53 # ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
54 # LINT_TARGET = $(MODULE).lint
55 # INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
56 #
```

new/usr/src/uts/intel/hermon/Makefile

2

```
56 # Overrides
57 #
58 #
59 #
60 # Build with no debug TNF probes enabled (by default)
61 #
62 #DEBUG_DEFS_DBG32 += -DTNF_DEBUG
63 #DEBUG_DEFS_DBG64 += -DTNF_DEBUG
64 #
65 #
66 # lint pass one enforcement
67 #
68 CFLAGS += $(CCVERBOSE)
69 #
70 CERRWARN += _gcc=-Wno-switch
71 CERRWARN += _gcc=-Wno-unused-value
72 CERRWARN += _gcc=-Wno-unused-label
73 CERRWARN += _gcc=-Wno-parentheses
74 CERRWARN += _gcc=-Wno-uninitialized
75 #
76 # needs work
77 SMATCH=off
78 #
79 #
80 # Default build targets.
81 #
82 .KEEP_STATE:
83 #
84 def: $(DEF_DEPS)
85 #
86 all: $(ALL_DEPS)
87 #
88 clean: $(CLEAN_DEPS)
89 #
90 clobber: $(CLOBBER_DEPS)
91 #
92 lint: $(LINT_DEPS)
93 #
94 modlintlib: $(MODLINTLIB_DEPS)
95 #
96 clean.lint: $(CLEAN_LINT_DEPS)
97 #
98 install: $(INSTALL_DEPS)
99 #
100 #
101 # Include common targets.
102 #
103 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/hme/Makefile

1

```
*****
1980 Mon Jan 7 13:08:54 2019
new/usr/src/uts/intel/hme/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/hme/Makefile
23 #
24 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the hme driver
28 # kernel module.
29 #
30 #
31 # Path to the base of the uts directory tree (usually /usr/src/uts).
32 #
33 UTSBASE = ../../
34 #
35 #
36 # Define the module and object file sets.
37 #
38 MODULE = hme
39 OBJECTS = ${HME_OBJS:%=${OBJS_DIR}/%}
40 LINTS = ${HME_OBJS:%.o=${LINTS_DIR}/%.ln}
41 ROOTMODULE = ${ROOT_DRV_DIR}/${MODULE}
42 #
43 #
44 # Include common rules.
45 #
46 include ${UTSBASE}/intel/Makefile.intel
47 #
48 #
49 # Define targets
50 #
51 ALL_TARGET = ${BINARY}
52 LINT_TARGET = ${MODULE}.lint
53 INSTALL_TARGET = ${BINARY} ${ROOTMODULE}
54 #
55 #
56 # Overrides.
57 #
58 CFLAGS += $(CCVERBOSE)
```

new/usr/src/uts/intel/hme/Makefile

2

```
59 LDFLAGS += -dy -Nmisc/mii -Nmisc/mac
61 CERRWARN += -_gcc=-Wno-switch
62 CERRWARN += -_gcc=-Wno-unused-label
63 CERRWARN += -_gcc=-Wno-uninitialized
64 #
65 # needs work
66 $(OBJS_DIR)/hme.o := SMOFF += indenting,deref_check
67 #
68 #
69 # Default build targets.
70 #
71 .KEEP_STATE:
72 #
73 def: $(DEF_DEPS)
74 #
75 all: $(ALL_DEPS)
76 #
77 clean: $(CLEAN_DEPS)
78 #
79 clobber: $(CLOBBER_DEPS)
80 #
81 lint: $(LINT_DEPS)
82 #
83 modlintlib: $(MODLINTLIB_DEPS)
84 #
85 clean.lint: $(CLEAN_LINT_DEPS)
86 #
87 install: $(INSTALL_DEPS)
88 #
89 #
90 # Include common targets.
91 #
92 include ${UTSBASE}/intel/Makefile.targ
```

new/usr/src/uts/intel/hook/Makefile

1

```
*****
2041 Mon Jan 7 13:08:57 2019
new/usr/src/uts/intel/hook/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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4 # The contents of this file are subject to the terms of the
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/hook/Makefile
23 #
24 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the hook driver kernel module.
31 #
32 # INTEL implementation architecture dependent
33 #

35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../

40 #
41 # Define the module and object file sets.
42 #
43 MODULE = hook
44 OBJECTS = $(HOOK_OBJS:%=$(OBJS_DIR)/%)
45 LINTS = $(HOOK_OBJS:%.o=$(LINTS_DIR)/%.ln)
46 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)

49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 CERRWARN += -_gcc=-Wno-uninitialized

56 # needs work
57 $(OBJS_DIR)/hook.o := SMOFF += all_func_returns

59 #
60 # Define targets
61 #
```

new/usr/src/uts/intel/hook/Makefile

2

```
62 ALL_TARGET = $(BINARY)
63 LINT_TARGET = $(MODULE).lint
64 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTLINK)

67 #
68 # Default build targets.
69 #
70 .KEEP_STATE:

72 def: $(DEF_DEPS)

74 all: $(ALL_DEPS) $(SISCHECK_DEPS)

76 clean: $(CLEAN_DEPS) $(SISCLEAN_DEPS)

78 clobber: $(CLOBBER_DEPS) $(SISCLEAN_DEPS)

80 lint: $(LINT_DEPS)

82 modlintlib: $(MODLINTLIB_DEPS)

84 clean.lint: $(CLEAN_LINT_DEPS)

86 install: $(INSTALL_DEPS) $(SISCHECK_DEPS)

88 #
89 # Include common targets.
90 #
91 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
1922 Mon Jan 7 13:08:58 2019
new/usr/src/uts/intel/hpcsvc/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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19 #
20 # CDDL HEADER END
21 #
22 #
23 # uts/intel/hpcsvc/Makefile
24 # Copyright 2004 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the hpcsvc "misc"
31 # kernel module.
32 # intel architecture dependent
33 #

35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../

34 #
35 # Define the module and object file sets.
36 #
37 MODULE = hpcsvc
38 OBJECTS = $(HPCSVCSVC_OBJS:%=$(OBJS_DIR)/%)
39 LINTS = $(HPCSVCSVC_OBJS:%.o=$(LINTS_DIR)/%.ln)
40 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)

42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel

47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

54 #

```

```

55 # Overrides.
56 #
57 DEBUG_FLGS =
58 DEBUG_DEFS += $(DEBUG_FLGS)

60 CERRWARN += -_gcc=-Wno-uninitialized

62 # needs work
63 $(OBJS_DIR)/hpcsvc.o := SMOFF += all_func_returns

65 #
66 # Default build targets.
67 #
68 .KEEP_STATE:

70 def: $(DEF_DEPS)

72 all: $(ALL_DEPS)

74 clean: $(CLEAN_DEPS)

76 clobber: $(CLOBBER_DEPS)

78 lint: $(LINT_DEPS)

80 modlintlib: $(MODLINTLIB_DEPS)

82 clean.lint: $(CLEAN_LINT_DEPS)

84 install: $(INSTALL_DEPS)

86 #
87 # Include common targets.
88 #
89 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2538 Mon Jan 7 13:08:59 2019
new/usr/src/uts/intel/hsfs/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/hsfs/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 # Copyright (c) 2011 Bayard G. Bell. All rights reserved.
27 #
28 # Copyright (c) 2018, Joyent, Inc.
29 # This makefile drives the production of the hsfs file system
30 # kernel module.
31 # intel architecture dependent
32 #
33 #
34 #
35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 # UTSBASE = ../../
39 #
40 # Define the module and object file sets.
41 #
42 # MODULE = hsfs
43 # OBJECTS = $(HSFS_OBJS:%=$(OBJS_DIR)/%)
44 # LINTS = $(HSFS_OBJS:%.o=$(LINTS_DIR)/%.ln)
45 # ROOTMODULE = $(ROOT_FS_DIR)/$(MODULE)
46 #
47 # Include common rules.
48 #
49 # include $(UTSBASE)/intel/Makefile.intel
50 #
51 # Define targets
52 #
53 # ALL_TARGET = $(BINARY)
54 # LINT_TARGET = $(MODULE).lint
55 # INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
56 #
57 # Define dependency on specsfs

```

```

57 #
58 LDFLAGS += -dy -N fs/specfs
59 #
60 #
61 # For now, disable these lint checks; maintainers should endeavor
62 # to investigate and remove these for maximum lint coverage.
63 # Please do not carry these forward to new Makefiles.
64 #
65 LINTTAGS += -errorf=E_SUSPICIOUS_COMPARISON
66 LINTTAGS += -errorf=E_BAD_PTR_CAST_ALIGN
67 LINTTAGS += -errorf=E_SUPPRESSION_DIRECTIVE_UNUSED
68 LINTTAGS += -errorf=E_STATIC_UNUSED
69 LINTTAGS += -errorf=E_PTRDIFF_OVERFLOW
70 LINTTAGS += -errorf=E_ASSIGN_NARROW_CONV
71 #
72 CERRWARN += -_gcc=-Wno-parentheses
73 CERRWARN += -_gcc=-Wno-uninitialized
74 CERRWARN += -_gcc=-Wno-unused-function
75 CERRWARN += -_gcc=-Wno-type-limits
76 CERRWARN += -_gcc=-Wno-switch
77 #
78 # needs work
79 $(OBJS_DIR)/hsfs_vnops.o := SMOFF += 64bit_shift,signed
80 #
81 #
82 # Default build targets.
83 #
84 .KEEP_STATE:
85 #
86 def: $(DEF_DEPS)
87 #
88 all: $(ALL_DEPS)
89 #
90 clean: $(CLEAN_DEPS)
91 #
92 clobber: $(CLOBBER_DEPS)
93 #
94 lint: $(LINT_DEPS)
95 #
96 modlintlib: $(MODLINTLIB_DEPS)
97 #
98 clean.lint: $(CLEAN_LINT_DEPS)
99 #
100 install: $(INSTALL_DEPS)
101 #
102 #
103 # Include common targets.
104 #
105 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2670 Mon Jan 7 13:09:00 2019
new/usr/src/uts/intel/hxge/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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18 #
19 # CDDL HEADER END
20 #
21 # uts/intel/hxge/Makefile
22 #
23 # Copyright (c) 2008, 2010, Oracle and/or its affiliates. All rights reserved.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

25 # This makefile drives the production of the Sun
26 # 10G hxge Ethernet leaf driver kernel module.
27 #
28 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTSBASE = ../../

32 #
33 # Define the module and object file sets.
34 #
35 MODULE = hxge
36 HXGE_OBJECTS = $(HXGE_OBJS) $(HXGE_HPI_OBJS)
37 OBJECTS = $(HXGE_OBJECTS:%=$(OBJS_DIR)/%)
38 LINTS = $(HXGE_OBJECTS%.o=$(LINTS_DIR)/%.ln)
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
40 CONF_SRCDIR = $(UTSBASE)/common/io/hxge

42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel

47 #
48 # Override defaults to build a unique, local modstubs.o.
49 #
50 MODSTUBS_DIR = $(OBJS_DIR)

52 CLEANFILES += $(MODSTUBS_O)

54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint

```

```

59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

61 #
62 # Include hxge specific header files
63 #
64 INC_PATH += -I$(UTSBASE)/common/io/hxge
65 #
66 #
67 # lint pass one enforcement
68 #
69 CFLAGS += -DSOLARIS
70 #
71 # Debug flags
72 #
73 # CFLAGS += -DHXGE_DEBUG -DHPI_DEBUG
74 #
75 LINTFLAGS += -DSOLARIS
76 #
77 # STREAMS, DDI API limitations and other ON header file definitions such as ethe
78 # force us to turn off these lint checks.
79 #
80 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
81 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
82 LINTTAGS += -erroff=E_FALSE_LOGICAL_EXPR

84 CERRWARN += -_gcc=-Wno-unused-label
85 CERRWARN += -_gcc=-Wno-switch
86 CERRWARN += -_gcc=-Wno-uninitialized
87 CERRWARN += -_gcc=-Wno-parentheses

89 SMOFF += deref_check,logical_instead_of_bitwise

91 #
92 # Driver depends on mac & IP
93 #
94 LDFFLAGS += -dy -N misc/mac -N drv/ip

96 #
97 # Default build targets.
98 #
99 .KEEP_STATE:

101 def: $(DEF_DEPS)
103 all: $(ALL_DEPS)
105 clean: $(CLEAN_DEPS)
107 clobber: $(CLOBBER_DEPS)
109 lint: $(LINT_DEPS)
111 modlintlib: $(MODLINTLIB_DEPS)
113 clean.lint: $(CLEAN_LINT_DEPS)
115 install: $(INSTALL_DEPS)

117 #
118 # Include common targets.
119 #
120 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/i40e/Makefile

1

```
*****
1649 Mon Jan 7 13:09:01 2019
new/usr/src/uts/intel/i40e/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # This file and its contents are supplied under the terms of the
3 # Common Development and Distribution License ("CDDL"), version 1.0.
4 # You may only use this file in accordance with the terms of version
5 # 1.0 of the CDDL.
6 #
7 # A full copy of the text of the CDDL should have accompanied this
8 # source. A copy of the CDDL is also available via the Internet at
9 # http://www.illumos.org/license/CDDL.
10 #
12 #
13 # Copyright (c) 2018, Joyent, Inc.
14 #
16 UTSBASE = ../..
18 MODULE = i40e
19 OBJECTS = $(I40E_OBJS:%=$(OBJS_DIR)/%) $(I40E_INTC_OBJS:%=$(OBJS_DIR)/%)
20 LINTS = $(I40E_OBJS:%.o=$(LINTS_DIR)/%.ln)
21 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
22 CONF_SRCDIR = $(UTSBASE)/common/io/i40e
24 include $(UTSBASE)/intel/Makefile.intel
26 CPPFLAGS += -I$(UTSBASE)/common/io/i40e
27 CPPFLAGS += -I$(UTSBASE)/common/io/i40e/core
29 ALL_TARGET = $(BINARY) $(CONFMOD)
30 LINT_TARGET = $(MODULE).lint
31 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
33 #
34 # The Intel common code defines some values in header files that end up in all
35 # of our objects. Therefore we don't try to lint for that.
36 #
37 LINTTAGS += -erroff=E_STATIC_UNUSED
38 LINTTAGS += -erroff=E_ENUM_VAL_OVERFLOWS_INT_MAX
40 #
41 # The Intel common code defines some enums which are valid unsigned ints that
42 # GCC is fine with, but not Studio.
43 #
44 CERRWARN += -_cc=-erroff=E_ENUM_VAL_OVERFLOWS_INT_MAX
46 # 3rd party code
47 SMOFF += all_func_returns
49 LDFLAGS += -dy -N misc/mac
51 .KEEP_STATE:
53 def: $(DEF_DEPS)
55 all: $(ALL_DEPS)
57 clean: $(CLEAN_DEPS)
59 clobber: $(CLOBBER_DEPS)
```

new/usr/src/uts/intel/i40e/Makefile

2

```
61 lint: $(LINT_DEPS)
63 modlintlib: $(MODLINTLIB_DEPS)
65 clean.lint: $(CLEAN_LINT_DEPS)
67 install: $(INSTALL_DEPS)
69 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/i8042/Makefile

1

```
*****
2208 Mon Jan 7 13:09:02 2019
new/usr/src/uts/intel/i8042/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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19 # CDDL HEADER END
20 #
21 #
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23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # This makefile drives the production of the i8042 nexus driver
29 #
30 # intel implementation architecture dependent
31 #

33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../

38 #
39 # Define the module and object file sets.
40 #
41 MODULE = i8042
42 OBJECTS = $(I8042_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(I8042_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

46 #
47 # Include common rules.
48 #
49 include $(UTSBASE)/intel/Makefile.intel

51 #
52 # Define targets
53 #
54 ALL_TARGET = $(BINARY) # $(SRC_CONFILE)
55 LINT_TARGET = $(MODULE).lint
56 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

58 #
59 # For now, disable these lint checks; maintainers should endeavor
60 # to investigate and remove these for maximum lint coverage.
61 # Please do not carry these forward to new Makefiles.
```

new/usr/src/uts/intel/i8042/Makefile

2

```
62 #
63 LINTTAGS += -errorf=E_BAD_PTR_CAST_ALIGN
64 LINTTAGS += -errorf=E_STATIC_UNUSED

66 CERRWARN += -_gcc=-Wno-unused-function

68 # needs work
69 $(OBJS_DIR)/i8042.o := SMOFF += all_func_returns

71 #
72 # Default build targets.
73 #
74 .KEEP_STATE:

76 def: $(DEF_DEPS)

78 all: $(ALL_DEPS)

80 clean: $(CLEAN_DEPS)

82 clobber: $(CLOBBER_DEPS)

84 lint: $(LINT_DEPS)

86 modlintlib: $(MODLINTLIB_DEPS)

88 clean.lint: $(CLEAN_LINT_DEPS)

90 install: $(INSTALL_DEPS)

92 #
93 # Include common targets.
94 #
95 include $(UTSBASE)/intel/Makefile.targ
```



```

*****
2186 Mon Jan 7 13:09:02 2019
new/usr/src/uts/intel/i915/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
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19 # CDDL HEADER END
20 #
22 #
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
28 #
29 # uts/intel/drm/Makefile
30 #
31 # This makefile drives the production of i915 graphics device driver,
32 # which supports the DRI (Direct Rendering Infrastructure), with the help
33 # of drm common misc module.
34 #
35 # intel platform dependent
36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTSBASE = ../../
41 #
42 # Define the module and object file sets.
43 #
44 MODULE = i915
45 OBJECTS = $(I915_OBJS:%=$(OBJS_DIR)/%)
46 LINTS = $(I915_OBJS:%.o=$(LINTS_DIR)/%.ln)
47 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
49 # i915 driver depends on drm, agpmaster and gfx_private misc modules
50 INC_PATH += -I$(UTSBASE)/intel/io/drm -I$(UTSBASE)/common/io/drm
52 #
53 # dependency
54 LDFLAGS += -dy -Nmisc/drm
56 #
57 # Include common rules.
58 #
59 include $(UTSBASE)/intel/Makefile.intel
61 CERRWARN += -_gcc=-Wno-unused-label

```

```

63 # needs work
64 SMATCH=off
66 #
67 # Re-define targets
68 #
69 ALL_TARGET = $(BINARY)
70 LINT_TARGET = $(MODULE).lint
71 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
73 #
74 # Default build targets.
75 #
76 .KEEP_STATE:
78 def: $(DEF_DEPS)
80 all: $(ALL_DEPS)
82 clean: $(CLEAN_DEPS)
84 clobber: $(CLOBBER_DEPS)
86 lint: $(LINT_DEPS)
88 modlintlib: $(MODLINTLIB_DEPS)
90 clean.lint: $(CLEAN_LINT_DEPS)
92 install: $(INSTALL_DEPS)
94 #
95 # Include common targets.
96 #
97 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2531 Mon Jan 7 13:09:03 2019
new/usr/src/uts/intel/ibcm/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
25 # uts/intel/ibcm/Makefile
26 #
27 # This makefile drives the production of the ibcm kernel
28 # module. SPARC architecture dependent
29 #

27 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTSBASE = ../..

32 #
33 # Define the module and object file sets.
34 #
35 MODULE = ibcm
36 OBJECTS = $(IBCM_OBJS:%=$(OBJS_DIR)/%)
37 LINTS = $(IBCM_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)

40 #
41 # Include common rules.
42 #
43 include $(UTSBASE)/intel/Makefile.intel

45 #
46 # Define targets
47 #
48 ALL_TARGET = $(BINARY)
49 LINT_TARGET = $(MODULE).lint
50 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

52 #
53 # Overrides.
54 #

56 #

```

```

57 # lint pass one enforcement
58 #
59 CFLAGS += $(CCVERBOSE)

61 CERRWARN += -_gcc=-Wno-switch
62 CERRWARN += -_gcc=-Wno-parentheses
63 CERRWARN += -_gcc=-Wno-type-limits
64 CERRWARN += -_gcc=-Wno-unused-label
65 CERRWARN += -_gcc=-Wno-unused-function
66 CERRWARN += -_gcc=-Wno-uninitialized
67 CERRWARN += -_gcc=-Wno-unused-value

69 # needs work
70 SMATCH=off

72 #
73 # depends on misc/ibtl and misc/ibmf
74 #
75 LDFLAGS += -dy -Nmisc/ibtl -Nmisc/ibmf -Ndrv/ip

77 #
78 # For now, disable these lint checks; maintainers should endeavor
79 # to investigate and remove these for maximum lint coverage.
80 # Please do not carry these forward to new Makefiles.
81 #
82 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
83 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
84 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
85 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
86 LINTTAGS += -erroff=E_STATIC_UNUSED

88 #
89 # Default build targets.
90 #
91 .KEEP_STATE:

93 def: $(DEF_DEPS)

95 all: $(ALL_DEPS)

97 clean: $(CLEAN_DEPS)

99 clobber: $(CLOBBER_DEPS)

101 lint: $(LINT_DEPS)

103 modlintlib: $(MODLINTLIB_DEPS)

105 clean.lint: $(CLEAN_LINT_DEPS)

107 install: $(INSTALL_DEPS)

109 #
110 # Include common targets.
111 #
112 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2172 Mon Jan 7 13:09:04 2019
new/usr/src/uts/intel/ibmf/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
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22 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
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24 #
25 # Copyright (c) 2018, Joyent, Inc.

26 # This makefile drives the production of the ibmf kernel module.
27 #
28 # intel architecture dependent
29 #

31 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTSBASE = ../..

32 #
33 # Define the module and object file sets.
34 #
35 MODULE = ibmf
36 OBJECTS = $(IBMF_OBJS:%=$(OBJS_DIR)/%)
37 LINTS = $(IBMF_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
39 LDFLAGS += -dy -Nmisc/ibtl

41 #
42 # Include common rules.
43 #
44 include $(UTSBASE)/intel/Makefile.intel

46 #
47 # Define targets
48 #
49 ALL_TARGET = $(BINARY)
50 LINT_TARGET = $(MODULE).lint
51 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

53 #
54 # Overrides
55 #

```

```

57 #DEBUG_DEFS_DBG32 += -DTNF_DEBUG

59 #
60 # lint pass one enforcement
61 #
62 CFLAGS += $(CCVERBOSE)

64 #
65 # For now, disable these lint checks; maintainers should endeavor
66 # to investigate and remove these for maximum lint coverage.
67 # Please do not carry these forward to new Makefiles.
68 #
69 LINTTAGS += -errorf=E_BAD_PTR_CAST_ALIGN
70 LINTTAGS += -errorf=E_ASSIGN_NARROW_CONV

72 CERRWARN += -_gcc=-Wno-uninitialized

74 # needs work
75 SMATCH=off

77 #
78 # Default build targets.
79 #
80 .KEEP_STATE:

82 def: $(DEF_DEPS)

84 all: $(ALL_DEPS)

86 clean: $(CLEAN_DEPS)

88 clobber: $(CLOBBER_DEPS)

90 lint: $(LINT_DEPS)

92 modlintlib: $(MODLINTLIB_DEPS)

94 clean.lint: $(CLEAN_LINT_DEPS)

96 install: $(INSTALL_DEPS)

98 #
99 # Include common targets.
100 #
101 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2212 Mon Jan 7 13:09:05 2019
new/usr/src/uts/intel/ibp/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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19 # CDDL HEADER END
20 #
21 #
22 # Copyright (c) 2010, Oracle and/or its affiliates. All rights reserved.
23 #
24 # Copyright (c) 2018, Joyent, Inc.
24 #

26 #
27 # Path to the base of the uts directory tree (usually /usr/src/uts).
28 #
29 UTSBASE = ../../

31 #
32 # Define the module and object file sets.
33 #
34 MODULE = ibp
35 OBJECTS = $(IBD_OBJS:%=$(OBJDIR)/%)
36 LINTS = $(IBD_OBJS:%.o=$(LINTSDIR)/%.ln)
37 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
38 CONF_SRCDIR = $(UTSBASE)/common/io/ib/clients/ibd
39 LDFLAGS += -dy -Nmisc/mac -Nmisc/ibtl -Nmisc/ibcm -Nmisc/ibmf -Ndrv/ip \
40 -Nmisc/dls -Ndrv/dld

42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel

47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY) $(CONFMOD)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

54 #
55 # For now, disable these lint checks; maintainers should endeavor
56 # to investigate and remove these for maximum lint coverage.
57 # Please do not carry these forward to new Makefiles.
58 #
59 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
60 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW

```

```

62 CERRWARN += -_gcc=-Wno-uninitialized
63 CERRWARN += -_gcc=-Wno-switch

65 # needs work
66 SMATCH=off

68 #
69 # Default build targets.
70 #
71 .KEEP_STATE:

73 def: $(DEF_DEPS)

75 all: $(ALL_DEPS)

77 clean: $(CLEAN_DEPS)

79 clobber: $(CLOBBER_DEPS)

81 lint: $(LINT_DEPS)

83 modlintlib: $(MODLINTLIB_DEPS)

85 clean.lint: $(CLEAN_LINT_DEPS)

87 install: $(INSTALL_DEPS)

89 #
90 # Include common targets.
91 #
92 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2417 Mon Jan 7 13:09:07 2019
new/usr/src/uts/intel/ibtl/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
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9 # or http://www.opensolaris.org/os/licensing.
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
27 # This makefile drives the production of the ibtl kernel module.
28 #
29 # intel architecture dependent
30 #

32 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTSBASE = ../../

32 #
33 # Define the module and object file sets.
34 #
35 MODULE = ibtl
36 OBJECTS = $(IBTL_OBJS:%=$(OBJS_DIR)/%)
37 LINTS = $(IBTL_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)

40 #
41 # Include common rules.
42 #
43 include $(UTSBASE)/intel/Makefile.intel

45 #
46 # Define targets
47 #
48 ALL_TARGET = $(BINARY)
49 LINT_TARGET = $(MODULE).lint
50 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

52 #
53 # Overrides.
54 #

```

```

56 #
57 # lint pass one enforcement
58 #
59 CFLAGS += $(CCVERBOSE)

61 #
62 # For now, disable these lint checks; maintainers should endeavor
63 # to investigate and remove these for maximum lint coverage.
64 # Please do not carry these forward to new Makefiles.
65 #
66 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
67 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
68 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
69 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
70 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

72 CERRWARN += -_gcc=-Wno-type-limits
73 CERRWARN += -_gcc=-Wno-switch
74 CERRWARN += -_gcc=-Wno-uninitialized
75 CERRWARN += -_gcc=-Wno-parentheses
76 CERRWARN += -_gcc=-Wno-unused-value

78 # needs work
79 $(OBJS_DIR)/ibtl_impl.o := SMOFF += precedence

81 #
82 # Default build targets.
83 #
84 .KEEP_STATE:

86 def: $(DEF_DEPS)

88 all: $(ALL_DEPS)

90 clean: $(CLEAN_DEPS)

92 clobber: $(CLOBBER_DEPS)

94 lint: $(LINT_DEPS)

96 modlintlib: $(MODLINTLIB_DEPS)

98 clean.lint: $(CLEAN_LINT_DEPS)

100 install: $(INSTALL_DEPS)

102 #
103 # Include common targets.
104 #
105 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2808 Mon Jan 7 13:09:08 2019
new/usr/src/uts/intel/icmp/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
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20 #
21 #
22 # uts/intel/icmp/Makefile
23 #
24 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 # Copyright (c) 2018, Joyent, Inc.
27 #
28 # This makefile drives the production of the icmp IP driver
29 #
30 # intel implementation architecture dependent
31 #
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../

38 #
39 # Define the module and object file sets.
40 #
41 MODULE = icmp
42 OBJECTS = $(ICMP_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(ICMP_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
45 ROOTLINK = $(ROOT_STRMOD_DIR)/$(MODULE) $(ROOT SOCK_DIR)/$(MODULE)
46 CONF_SRCDIR = $(UTSBASE)/common/inet/ip

48 #
49 # Extra for $(MODULE).check target
50 #
51 # Need to remove ipddi.o since it has non-static defines for _init etc.
52 IP_CHECK_OBJS = $(IP_OBJS:ipddi.o=ip.o)
53 EXTRA_CHECK_OBJS = $(IP_CHECK_OBJS:%=../ip/$(OBJS_DIR)/%)

55 #
56 # Include common rules.
57 #
58 include $(UTSBASE)/intel/Makefile.intel

60 #
61 # Define targets

```

```

62 #
63 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
64 LINT_TARGET = $(MODULE).lint
65 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTLINK) $(ROOT_CONFFILE)

67 #
68 # depends on ip and sockfs
69 #
70 LDFLAGS += -dy -Ndrv/ip -Nfs/sockfs

72 #
73 # For now, disable these lint checks; maintainers should endeavor
74 # to investigate and remove these for maximum lint coverage.
75 # Please do not carry these forward to new Makefiles.
76 #
77 LINTTAGS += -eroff=E_BAD_PTR_CAST_ALIGN
78 LINTTAGS += -eroff=E_PTRDIFF_OVERFLOW
79 LINTTAGS += -eroff=E_ASSIGN_NARROW_CONV

81 # needs work
82 $(OBJS_DIR)/icmpddi.o := SMOFF += index_overflow

84 #
85 # Default build targets.
86 #
87 .KEEP_STATE:

89 def: $(DEF_DEPS)

91 all: $(ALL_DEPS) $(SISCHECK_DEPS)

93 clean: $(CLEAN_DEPS) $(SISCLEAN_DEPS)

95 clobber: $(CLOBBER_DEPS) $(SISCLEAN_DEPS)

97 lint: $(LINT_DEPS)

99 modlintlib: $(MODLINTLIB_DEPS)

101 clean.lint: $(CLEAN_LINT_DEPS)

103 install: $(INSTALL_DEPS) $(SISCHECK_DEPS)

105 $(ROOTLINK): $(ROOT_STRMOD_DIR) $(ROOT SOCK_DIR) $(ROOTMODULE)
106 -$(RM) $@; ln $(ROOTMODULE) $@

108 #
109 # Include common targets.
110 #
111 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/icmp6/Makefile

1

```
*****
1945 Mon Jan 7 13:09:11 2019
new/usr/src/uts/intel/icmp6/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
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21 #
22 #
23 # Copyright 2004 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # ident "%Z%M% %I% %E% SMI"
28 #
29 # This makefile drives the production of the icmp6 IP driver
30 #
31 # intel implementation architecture dependent
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../
37 #
38 # Define the module and object file sets.
39 #
40 MODULE = icmp6
41 OBJECTS = $(ICMP6_OBJS:%=$(OBJS_DIR)/%)
42 LINTS = $(ICMP6_OBJS:%.o=$(LINTS_DIR)/%.ln)
43 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
44 CONF_SRCDIR = $(UTSBASE)/common/inet/ip
45 #
46 # Include common rules.
47 #
48 include $(UTSBASE)/intel/Makefile.intel
49 #
50 # Define targets
51 #
52 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
53 LINT_TARGET = $(MODULE).lint
54 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
55 #
56 # depends on ip ip6 and icmp
```

new/usr/src/uts/intel/icmp6/Makefile

2

```
56 #
57 LDFLAGS += -dy -Ndrv/ip -Ndrv/ip6 -Ndrv/icmp
58 #
59 # needs work
60 $(OBJS_DIR)/icmp6ddi.o := SMOFF += index_overflow
61 #
62 # Default build targets.
63 #
64 .KEEP_STATE:
65 #
66 def: $(DEF_DEPS)
67 #
68 all: $(ALL_DEPS)
69 #
70 clean: $(CLEAN_DEPS)
71 #
72 clobber: $(CLOBBER_DEPS)
73 #
74 lint: $(LINT_DEPS)
75 #
76 modlintlib: $(MODLINTLIB_DEPS)
77 #
78 clean.lint: $(CLEAN_LINT_DEPS)
79 #
80 install: $(INSTALL_DEPS)
81 #
82 #
83 # Include common targets.
84 #
85 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/idm/Makefile

1

```
*****
2008 Mon Jan 7 13:09:11 2019
new/usr/src/uts/intel/idm/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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20 #
21 #
22 # uts/intel/io/idm/Makefile
23 #
24 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 #
28 # This makefile drives the production of the idm kernel module.
29 #
30 # intel architecture dependent
31 #
32 #
33 #
34 #
35 # Define the module and object file sets.
36 #
37 MODULE = idm
38 OBJECTS = $(IDM_OBJS:%=$(OBJS_DIR)/%)
39 LINTS = $(IDM_OBJS:%.o=$(LINTS_DIR)/%.ln)
40 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
41 #
42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel
46 #
47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
53 #
54 #
55 # Overrides.
56 #
```

new/usr/src/uts/intel/idm/Makefile

2

```
57 DEBUG_FLGS =
58 DEBUG_DEFS += $(DEBUG_FLGS)
59 LDPLAGS += -dy -Nfs/sockfs -Nmisc/ksocket
60 #
61 CERRWARN += -_gcc=-Wno-switch
62 CERRWARN += -_gcc=-Wno-parentheses
63 CERRWARN += -_gcc=-Wno-uninitialized
64 #
65 # needs work
66 SMATCH=off
67 #
68 # Include
69 INC_PATH += -I$(SRC)/common/hdrc
70 #
71 #
72 # Default build targets.
73 #
74 .KEEP_STATE:
75 #
76 def: $(DEF_DEPS)
77 #
78 all: $(ALL_DEPS)
79 #
80 clean: $(CLEAN_DEPS)
81 #
82 clobber: $(CLOBBER_DEPS)
83 #
84 lint: $(LINT_DEPS)
85 #
86 modlintlib: $(MODLINTLIB_DEPS)
87 #
88 clean.lint: $(CLEAN_LINT_DEPS)
89 #
90 install: $(INSTALL_DEPS)
91 #
92 #
93 # Include common targets.
94 #
95 include $(UTSBASE)/intel/Makefile.targ
```



```

*****
2177 Mon Jan 7 13:09:12 2019
new/usr/src/uts/intel/igb/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2010 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
25 # uts/intel/igb/Makefile
26 #
27 # This makefile drives the production of the igb
28 # network driver kernel module.
29 #
30 # intel architecture dependent
31 #

27 #
28 # Paths to the base of the uts directory trees
29 #
30 UTSBASE = ../../

32 #
33 # Define the module and object file sets.
34 #
35 MODULE = igb
36 OBJECTS = $(IGB_OBJS:%=$(OBJS_DIR)/%)
37 OBJECTS += $(E1000API_OBJS:%=$(OBJS_DIR)/%)
38 LINTS = $(IGB_OBJS:%.o=$(LINTS_DIR)/%.ln)
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
40 CONF_SRCDIR = $(UTSBASE)/common/io/igb

42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel

47 LINTFLAGS += \
48 -I$(UTSBASE)/common/io/igb \
49 -I$(UTSBASE)/common/io/e1000api

51 CERRWARN += _gcc=-Wno-switch
52 CERRWARN += _gcc=-Wno-uninitialized
53 CERRWARN += _cc=-erroff=E_STATEMENT_NOT_REACHED

```

```

55 # needs work
56 SMOFF += all_func_returns,indenting

58 CFLAGS += -I$(UTSBASE)/common/io/e1000api
59 CFLAGS += -I$(UTSBASE)/common/io/igb

61 #
62 # Define targets
63 #
64 ALL_TARGET = $(BINARY) $(CONFMOD)
65 LINT_TARGET = $(MODULE).lint
66 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

68 #
69 # Driver depends on MAC
70 #
71 LDFLAGS += -dy -N misc/mac

73 #
74 # Default build targets.
75 #
76 .KEEP_STATE:

78 def: $(DEF_DEPS)

80 all: $(ALL_DEPS)

82 clean: $(CLEAN_DEPS)

84 clobber: $(CLOBBER_DEPS)

86 lint: $(LINT_DEPS)

88 modlintlib: $(MODLINTLIB_DEPS)

90 clean.lint: $(CLEAN_LINT_DEPS)

92 install: $(INSTALL_DEPS)

94 #
95 # Include common targets.
96 #
97 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/intel\_nb5000/Makefile

1

```
*****
2245 Mon Jan 7 13:09:13 2019
new/usr/src/uts/intel/intel_nb5000/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
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16 # information: Portions Copyright [yyyy] [name of copyright owner]
17 #
18 # CDDL HEADER END
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21 # Use is subject to license terms.
22 # Copyright (c) 2011 Bayard G. Bell. All rights reserved.
23 #
24 # Copyright (c) 2018, Joyent, Inc.

26 #
27 # Intel 5000/5100/5400/7300 chipset memory controller hub (MCH) module
28 #

30 #
31 # Path to the base of the uts directory tree (usually /usr/src/uts).
32 #
33 UTSBASE = ../../

35 #
36 # Define the module and object file sets.
37 #
38 MODULE = intel_nb5000
39 #
40 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
41 SRCDIR = $(UTSBASE)/intel/io/intel_nb5000
42 CONF_SRCDIR = $(UTSBASE)/intel/io/intel_nb5000

44 #
45 # Include common rules.
46 #
47 include ../Makefile.intel

49 OBJECTS = $(INTEL_NB5000_OBJS:%=$(OBJS_DIR)/%)
50 LINTS = $(INTEL_NB5000_OBJS:%.o=$(LINTS_DIR)/%.ln)

52 #
53 # Define targets
54 #
55 ALL_TARGET = $(BINARY)
56 LINT_TARGET = $(LINT_MODULE).lint
57 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

59 #
60 # Depends on sbmbios
61 #
```

new/usr/src/uts/intel/intel\_nb5000/Makefile

2

```
62 LDFLAGS += -dy -N drv/smbios
64 CERRWARN += -_gcc=-Wno-parentheses
65 CERRWARN += -_gcc=-Wno-uninitialized

67 # needs work
68 SMATCH=off

70 #
71 # Default build targets.
72 #
73 .KEEP_STATE:

75 def: $(DEF_DEPS)

77 all: $(ALL_DEPS)

79 clean: $(CLEAN_DEPS)

81 clobber: $(CLOBBER_DEPS)

83 lint: $(LINT_DEPS)

85 modlintlib: $(MODLINTLIB_DEPS)

87 clean.lint: $(CLEAN_LINT_DEPS)

89 install: $(INSTALL_DEPS)

91 $(OBJECTS): $(OBJS_DIR) $(MCAMD_OFF_H)

93 #
94 # Include common targets.
95 #
96 include ../Makefile.targ
```

```

*****
4233 Mon Jan 7 13:09:14 2019
new/usr/src/uts/intel/ip/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
26 #
27 # This makefile drives the production of the ip driver
28 # kernel module.
29 #
30 # intel implementation architecture dependent
31 #
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../

38 #
39 # Define the module and object file sets.
40 #
41 MODULE = ip
42 OBJECTS = $(IP_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(IP_OBJS:%.o=$(LINTS_DIR)/%.lint)
44 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
45 ROOTLINK = $(ROOT_STRMOD_DIR)/$(MODULE)
46 CONF_SRCDIR = $(UTSBASE)/common/inet/ip

48 #
49 # Include common rules.
50 #
51 include $(UTSBASE)/intel/Makefile.intel

53 #
54 # Define targets
55 #
56 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
57 LINT_TARGET = $(MODULE).lint
58 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTLINK) $(ROOT_CONFFILE)

60 CINLINEFLAGS = -xinline=tcp_set_ws_value

```

```

62 CFLAGS += $(CINLINEFLAGS)

64 CERRWARN += _gcc=-Wno-parentheses
65 CERRWARN += _gcc=-Wno-unused-label
66 CERRWARN += _gcc=-Wno-unused-function
67 CERRWARN += _gcc=-Wno-unused-variable
68 CERRWARN += _gcc=-Wno-switch
69 CERRWARN += _gcc=-Wno-uninitialized
70 CERRWARN += _gcc=-Wno-type-limits

72 # false positives
73 SMOFF += index_overflow

75 # need work still
76 $(OBJS_DIR)/igmp.o := SMOFF += shift_to_zero
77 $(OBJS_DIR)/tnet.o := SMOFF += shift_to_zero
78 SMOFF += signed_all_func_returns
79 SMOFF += signed_integer_overflow_check

81 # definitely wrong
82 $(OBJS_DIR)/ip_input.o := SMOFF += or_vs_and
83 $(OBJS_DIR)/ip6_input.o := SMOFF += or_vs_and

85 #
86 # To get the BPF header files included by ipnet.h
87 #
88 INC_PATH += -I$(UTSBASE)/common/io/bpf

90 #
91 # Depends on md5 and swrand (for SCTP). SCTP needs to depend on
92 # swrand as it needs random numbers early on during boot before
93 # kCF subsystem can load swrand.
94 #
95 LDFLAGS += -dy -Nmisc/md5 -Ncrypto/swrand -Nmisc/hook -Nmisc/neti

97 #
98 # For now, disable these lint checks; maintainers should endeavor
99 # to investigate and remove these for maximum lint coverage.
100 # Please do not carry these forward to new Makefiles.
101 #
102 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
103 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
104 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
105 LINTTAGS += -erroff=E_STATIC_UNUSED
106 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
107 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

109 #
110 # Default build targets.
111 #
112 .KEEP_STATE:

114 def: $(DEF_DEPS)

116 all: $(ALL_DEPS) $(SISCHECK_DEPS)

118 clean: $(CLEAN_DEPS) $(SISCLEAN_DEPS)

120 # Need to clobber all build types due to iptcf.a
121 clobber: $(CLOBBER_DEPS) $(SISCLEAN_DEPS) \
122 clobber.obj64 clobber.debug64

124 lint: $(LINT_DEPS)

126 modlintlib: $(MODLINTLIB_DEPS)

```

**new/usr/src/uts/intel/ip/Makefile**

3

```
128 clean.lint:    $(CLEAN_LINT_DEPS)
130 install:      $(INSTALL_DEPS) $(SISCHECK_DEPS)
132 $(ROOTLINK):  $(ROOT_STRMOD_DIR) $(ROOTMODULE)
133              -$(RM) $@; ln $(ROOTMODULE) $@
135 #
136 #             Include common targets.
137 #
138 include $(UTSBASE)/intel/Makefile.targ
140 #
141 # The ip CTF data is merged into the genunix module because these types are
142 # complex and heavily shared. The genunix build will execute one of the
143 # rules below to create an archive, ipctf.a, containing the ip objects. The
144 # real ip will be uniquified against genunix later in the build, and will
145 # emerge containing very few types.
146 #
147 $(OBJS_DIR)/ipctf.a: $(OBJECTS)
148                      -$(RM) $@
149                      $(AR) -r $@ $(OBJECTS)
151 $(OBJECTS): $(OBJS_DIR)
153 CLOBBERFILES += $(OBJS_DIR)/ipctf.a
155 ipctf.obj64: FRC
156              @BUILD_TYPE=OBJ64 VERSION='$(VERSION)' $(MAKE) obj64/ipctf.a
158 ipctf.debug64: FRC
159              @BUILD_TYPE=DBG64 VERSION='$(VERSION)' $(MAKE) debug64/ipctf.a
```

new/usr/src/uts/intel/ip6/Makefile

1

```
*****
1904 Mon Jan 7 13:09:15 2019
new/usr/src/uts/intel/ip6/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # fields enclosed by brackets "[]" replaced with your own identifying
18 # information: Portions Copyright [yyyy] [name of copyright owner]
19 #
20 # CDDL HEADER END
21 #
22 #
23 # Copyright 2004 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # ident "%Z%M% %I% %E% SMI"
28 #
29 # This makefile drives the production of the ip6 driver
30 # kernel module.
31 # intel implementation architecture dependent
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../
37 #
38 # Define the module and object file sets.
39 #
40 MODULE = ip6
41 OBJECTS = $(IP6_OBJS:%=$(OBJS_DIR)/%)
42 LINTS = $(IP6_OBJS:%.o=$(LINTS_DIR)/%.ln)
43 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
44 CONF_SRCDIR = $(UTSBASE)/common/inet/ip
45 #
46 # Include common rules.
47 #
48 include $(UTSBASE)/intel/Makefile.intel
49 #
50 # Define targets
51 #
52 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
53 LINT_TARGET = $(MODULE).lint
54 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
55 #
```

new/usr/src/uts/intel/ip6/Makefile

2

```
55 # depends on ip
56 #
57 LDLFLAGS += -dy -Ndrv/ip
58 #
59 # needs work
60 $(OBJS_DIR)/ip6ddi.o := SMOFF += index_overflow
61 #
62 #
63 # Default build targets.
64 #
65 .KEEP_STATE:
66 #
67 def: $(DEF_DEPS)
68 #
69 all: $(ALL_DEPS)
70 #
71 clean: $(CLEAN_DEPS)
72 #
73 clobber: $(CLOBBER_DEPS)
74 #
75 lint: $(LINT_DEPS)
76 #
77 modlintlib: $(MODLINTLIB_DEPS)
78 #
79 clean.lint: $(CLEAN_LINT_DEPS)
80 #
81 install: $(INSTALL_DEPS)
82 #
83 #
84 # Include common targets.
85 #
86 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2643 Mon Jan 7 13:09:16 2019
new/usr/src/uts/intel/ipf/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
26 #
27 # This makefile drives the production of the ipf driver
28 # kernel module.
29 #
30 # intel architecture dependent
31 #

27 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTSBASE = ../../

32 #
33 # Define the module and object file sets.
34 #
35 MODULE = ipf
36 OBJECTS = $(IPF_OBJS:%=$(OBJS_DIR)/%)
37 LINTS = $(IPF_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(USR_DRV_DIR)/$(MODULE)
39 CONF_SRCDIR = $(UTSBASE)/common/inet/ipf

41 #
42 # Include common rules.
43 #
44 include $(UTSBASE)/intel/Makefile.intel

46 #
47 # Define targets
48 #
49 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
50 LINT_TARGET = $(MODULE).lint
51 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

53 CPPFLAGS += -DIPFILTER_LKM -DIPFILTER_LOG -DIPFILTER_LOOKUP -DUSE_INET6
54 CPPFLAGS += -DSUNDDI -DSOLARIS2=$(RELEASE_MINOR) -DIRE_ILL_CN

```

```

55 LDFLAGS += -dy -Ndrv/ip -Nmisc/md5 -Nmisc/neti -Nmisc/hook -Nmisc/kof
57 INC_PATH += -I$(UTSBASE)/common/inet/ipf

59 #
60 # For now, disable these lint checks; maintainers should endeavor
61 # to investigate and remove these for maximum lint coverage.
62 # Please do not carry these forward to new Makefiles.
63 #
64 LINTTAGS += -erroff=E_STATIC_UNUSED
65 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
66 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
67 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

69 CERRWARN += _gcc=-Wno-unused-function
70 CERRWARN += _gcc=-Wno-unused-variable
71 CERRWARN += _gcc=-Wno-parentheses
72 CERRWARN += _gcc=-Wno-uninitialized
73 CERRWARN += _gcc=-Wno-empty-body

75 # needs work
76 SMATCH=off

78 #
79 # Default build targets.
80 #
81 .KEEP_STATE:

83 def: $(DEF_DEPS)

85 all: $(ALL_DEPS) $(SISCHECK_DEPS)

87 clean: $(CLEAN_DEPS) $(SISCLEAN_DEPS)

89 clobber: $(CLOBBER_DEPS) $(SISCLEAN_DEPS)

91 lint: $(LINT_DEPS)

93 modlintlib: $(MODLINTLIB_DEPS)

95 clean.lint: $(CLEAN_LINT_DEPS)

97 install: $(INSTALL_DEPS) $(SISCHECK_DEPS)

99 #
100 # Include common targets.
101 #
102 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/ipgpc/Makefile

1

```
*****
2267 Mon Jan 7 13:09:17 2019
new/usr/src/uts/intel/ipgpc/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
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20 #
21 #
22 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # This makefile drives the production of the IP generic packet classifier
29 # ipp kernel module.
30 #
31 # intel architecture dependent
32 #

34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../..

39 #
40 # Define the module and object file sets.
41 #
42 MODULE = ipgpc
43 OBJECTS = ${IPGPC_OBJS:%=${OBJS_DIR}/%}
44 LINTS = ${IPGPC_OBJS:%.o=${LINTS_DIR}/%.ln}
45 ROOTMODULE = ${ROOT_IPP_DIR}/${MODULE}

47 #
48 # Include common rules.
49 #
50 include ${UTSBASE}/intel/Makefile.intel

52 #
53 # Override defaults
54 #

57 #
58 # Define targets
59 #
60 ALL_TARGET = ${BINARY}
61 LINT_TARGET = ${MODULE}.lint
```

new/usr/src/uts/intel/ipgpc/Makefile

2

```
62 INSTALL_TARGET = ${BINARY} ${ROOTMODULE}

64 LDFLAGS += -dy -Ndrv/ip

66 #
67 # For now, disable these lint checks; maintainers should endeavor
68 # to investigate and remove these for maximum lint coverage.
69 # Please do not carry these forward to new Makefiles.
70 #
71 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
72 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN

74 CERRWARN += -_gcc=-Wno-uninitialized

76 # needs work
77 ${OBJS_DIR}/filters.o := SMOFF += deref_check

79 #
80 # Default build targets.
81 #
82 .KEEP_STATE:

84 def: ${DEF_DEPS}

86 all: ${ALL_DEPS}

88 clean: ${CLEAN_DEPS}

90 clobber: ${CLOBBER_DEPS}

92 lint: ${LINT_DEPS}

94 modlintlib: ${MODLINTLIB_DEPS}

96 clean.lint: ${CLEAN_LINT_DEPS}

98 install: ${INSTALL_DEPS}

100 #
101 # Include common targets.
102 #
103 include ${UTSBASE}/intel/Makefile.targ
```

```

*****
2851 Mon Jan 7 13:09:18 2019
new/usr/src/uts/intel/ipsecah/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/ipsecah/Makefile
23 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.

28 #
29 # This makefile drives the production of the ipsecah driver
30 # kernel module.
31 #
32 # intel implementation architecture dependent
33 #

35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../

40 #
41 # Define the module and object file sets.
42 #
43 MODULE = ipsecah
44 OBJECTS = $(IPSECAH_OBJS:%=$(OBJS_DIR)/%)
45 LINTS = $(IPSECAH_OBJS:%.o=$(LINTS_DIR)/%.ln)
46 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
47 ROOTLINK = $(ROOT_STRMOD_DIR)/$(MODULE)
48 CONF_SRCDIR = $(UTSBASE)/common/inet/ip

50 #
51 # Include common rules.
52 #
53 include $(UTSBASE)/intel/Makefile.intel

55 #
56 # Define targets
57 #
58 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
59 LINT_TARGET = $(MODULE).lint
60 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTLINK) $(ROOT_CONFFILE)

```

```

62 #
63 # Linkage dependencies
64 #
65 LDFLAGS += -dy -Ndrv/ip -Ndrv/tcp -Nmisc/kcf

67 #
68 # For now, disable these lint checks; maintainers should endeavor
69 # to investigate and remove these for maximum lint coverage.
70 # Please do not carry these forward to new Makefiles.
71 #
72 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
73 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
74 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
75 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

77 CERRWARN += -_gcc=-Wno-parentheses
78 CERRWARN += -_gcc=-Wno-uninitialized

80 # needs work
81 $(OBJS_DIR)/ipsecahddi.o := SMOFF += index_overflow
82 $(OBJS_DIR)/ipsecah.o := SMOFF += deref_check
83 $(OBJS_DIR)/sadb.o := SMOFF += signed_integer_overflow_check,deref_check,indent

85 #
86 # Default build targets.
87 #
88 .KEEP_STATE:

90 def: $(DEF_DEPS)

92 all: $(ALL_DEPS) $(SISCHECK_DEPS)

94 clean: $(CLEAN_DEPS) $(SISCLEAN_DEPS)

96 clobber: $(CLOBBER_DEPS) $(SISCLEAN_DEPS)

98 lint: $(LINT_DEPS)

100 modlintlib: $(MODLINTLIB_DEPS)

102 clean.lint: $(CLEAN_LINT_DEPS)

104 install: $(INSTALL_DEPS) $(SISCHECK_DEPS)

106 $(ROOTLINK): $(ROOT_STRMOD_DIR) $(ROOTMODULE)
107 -$(RM) $@; ln $(ROOTMODULE) $@

109 #
110 # Include common targets.
111 #
112 include $(UTSBASE)/intel/Makefile.targ

```



new/usr/src/uts/intel/ipsecesp/Makefile

1

```
*****
2687 Mon Jan 7 13:09:18 2019
new/usr/src/uts/intel/ipsecesp/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
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22 # uts/intel/ipsecesp/Makefile
23 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
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26 # Copyright (c) 2018, Joyent, Inc.

28 #
29 # This makefile drives the production of the ipsecesp driver
30 # kernel module.
31 #
32 # intel implementation architecture dependent
33 #

35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../

40 #
41 # Define the module and object file sets.
42 #
43 MODULE = ipsecesp
44 OBJECTS = $(IPSECESP_OBJS:%=$(OBJS_DIR)/%)
45 LINTS = $(IPSECESP_OBJS:%.o=$(LINTS_DIR)/%.ln)
46 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
47 ROOTLINK = $(ROOT_STRMOD_DIR)/$(MODULE)
48 CONF_SRCDIR = $(UTSBASE)/common/inet/ip

50 #
51 # Include common rules.
52 #
53 include $(UTSBASE)/intel/Makefile.intel

55 #
56 # Define targets
57 #
58 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
59 LINT_TARGET = $(MODULE).lint
60 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTLINK) $(ROOT_CONFFILE)
```

new/usr/src/uts/intel/ipsecesp/Makefile

2

```
62 #
63 # Linkage dependencies
64 #
65 LDFLAGS += -dy -Ndrv/ip -Ndrv/ipsecah -Nmisc/kcf

67 #
68 # For now, disable these lint checks; maintainers should endeavor
69 # to investigate and remove these for maximum lint coverage.
70 # Please do not carry these forward to new Makefiles.
71 #
72 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
73 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
74 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

76 CERRWARN += _gcc=-Wno-uninitialized

78 # needs work
79 $(OBJS_DIR)/ipsecespddi.o := SMOFF += index_overflow
80 $(OBJS_DIR)/ipsecesp.o := SMOFF += deref_check

82 #
83 # Default build targets.
84 #
85 .KEEP_STATE:

87 def: $(DEF_DEPS)

89 all: $(ALL_DEPS) $(SISCHECK_DEPS)

91 clean: $(CLEAN_DEPS) $(SISCLEAN_DEPS)

93 clobber: $(CLOBBER_DEPS) $(SISCLEAN_DEPS)

95 lint: $(LINT_DEPS)

97 modlintlib: $(MODLINTLIB_DEPS)

99 clean.lint: $(CLEAN_LINT_DEPS)

101 install: $(INSTALL_DEPS) $(SISCHECK_DEPS)

103 $(ROOTLINK): $(ROOT_STRMOD_DIR) $(ROOTMODULE)
104 -$(RM) $@; ln $(ROOTMODULE) $@

106 #
107 # Include common targets.
108 #
109 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/ipw/Makefile

1

```
*****
2130 Mon Jan 7 13:09:19 2019
new/usr/src/uts/intel/ipw/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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20 #
21 #
22 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # This makefile drives the production of the ipw driver kernel module.
29 #
30 # i86pc architecture dependent
31 #

33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../

38 #
39 # Define the module and object file sets.
40 #
41 MODULE = ipw
42 OBJECTS = $(IPW_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(IPW_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

46 #
47 # Include common rules.
48 #
49 include $(UTSBASE)/intel/Makefile.intel

51 CERRWARN += _gcc=-Wno-switch
52 CERRWARN += _gcc=-Wno-unused-label
53 CERRWARN += _gcc=-Wno-parentheses
54 CERRWARN += _gcc=-Wno-uninitialized

56 # needs work
57 $(OBJS_DIR)/ipw2100.o := SMOFF += deref_check

59 #
60 # Define targets
61 #
```

new/usr/src/uts/intel/ipw/Makefile

2

```
62 ALL_TARGET = $(BINARY) $(CONFMOD) $(ITUMOD)
63 LINT_TARGET = $(MODULE).lint
64 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

66 CPPFLAGS += -I. -D_KERNEL
67 LDFLAGS += -dy -Nmisc/mac -Nmisc/net80211 -Ndrv/random -Ndrv/ip

69 #
70 # Default build targets.
71 #
72 .KEEP_STATE:

74 def: $(DEF_DEPS)

76 all: $(ALL_DEPS)

78 clean: $(CLEAN_DEPS)

80 clobber: $(CLOBBER_DEPS)

82 lint: $(LINT_DEPS)

84 modlintlib: $(MODLINTLIB_DEPS)

86 clean.lint: $(CLEAN_LINT_DEPS)

88 install: $(INSTALL_DEPS)

90 #
91 # Include common targets.
92 #
93 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2492 Mon Jan 7 13:09:19 2019
new/usr/src/uts/intel/iscsi/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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22 #
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24 # Use is subject to license terms.
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26 # Copyright (c) 2018, Joyent, Inc.
27 #
28 #
29 # This makefile drives the production of iSCSI initiator Driver
30 # intel architecture dependent
31 #
32 #
33 #
34 # Paths to the base of the uts directory trees
35 #
36 UTBASE = ../../
37 #
38 #
39 # Define the module and object file sets.
40 #
41 MODULE = iscsi
42 OBJECTS = $(ISCSI_INITIATOR_OBJS:%=$(OBJSDIR)/%)
43 LINTS = $(ISCSI_INITIATOR_OBJS:%.o=$(LINTSDIR)/%.ln)
44 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
45 CONF_SRCDIR = $(UTBASE)/common/io/scsi/adapters/iscsi
46 #
47 #
48 # Include common rules.
49 #
50 include $(UTBASE)/intel/Makefile.intel
51 #
52 #
53 # Define targets.
54 #
55 ALL_TARGET = $(BINARY) $(SRC_CONFILE)
56 LINT_TARGET = $(MODULE).lint
57 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
58 #
59 # includes
60 INC_PATH += -I$(UTBASE)/common/io/scsi/adapters/iscsi
61 INC_PATH += -I$(SRC)/common/hdrcrc

```

```

63 #
64 # Note dependancy on misc/scsi.
65 #
66 LDFFLAGS += -dy -N"misc/scsi" -N"fs/sockfs" -N"sys/doorfs" -Nmisc/md5 -Nmisc/ksoc
67 LDFFLAGS += -N"misc/idm"
68 #
69 LINTFLAGS += -a -erroff=E_BAD_PTR_CAST_ALIGN -erroff=E_PTRDIFF_OVERFLOW
70 LINTFLAGS64 += -a -erroff=E_BAD_PTR_CAST_ALIGN -erroff=E_PTRDIFF_OVERFLOW
71 #
72 CERRWARN += -_gcc=-Wno-switch
73 CERRWARN += -_gcc=-Wno-unused-function
74 CERRWARN += -_gcc=-Wno-parentheses
75 CERRWARN += -_gcc=-Wno-type-limits
76 CERRWARN += -_gcc=-Wno-uninitialized
77 #
78 # needs work
79 SMATCH=off
80 #
81 #
82 # Default build targets.
83 #
84 .KEEP_STATE:
85 #
86 def: $(DEF_DEPS)
87 #
88 all: $(ALL_DEPS)
89 #
90 clean: $(CLEAN_DEPS)
91 #
92 clobber: $(CLOBBER_DEPS)
93 #
94 lint: $(LINT_DEPS)
95 #
96 modlintlib: $(MODLINTLIB_DEPS)
97 #
98 clean.lint: $(CLEAN_LINT_DEPS)
99 #
100 install: $(INSTALL_DEPS)
101 #
102 #
103 # Include common targets.
104 #
105 include $(UTBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/iscsit/Makefile

1

```
*****
2107 Mon Jan 7 13:09:19 2019
new/usr/src/uts/intel/iscsit/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright (c) 2008, 2010, Oracle and/or its affiliates. All rights reserved.
23 #
24 # Copyright (c) 2018, Joyent, Inc.
25 # This makefile drives the production of the iscsit pseudo-driver for
    COMSTAR.

26 #
27 # Path to the base of the uts directory tree (usually /usr/src/uts).
28 #

29 UTBASE = ../..

31 ARCHDIR:sh = cd ../; basename `pwd`

33 #
34 # Define the module and object file sets.
35 #
36 MODULE = iscsit
37 OBJECTS = $(ISCSIT_OBJS:%=$(OBJS_DIR)/%)
38 LINTS = $(ISCSIT_OBJS:%.o=$(LINTS_DIR)/%.ln)
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
40 CONF_SRCDIR = $(UTSBASE)/common/io/comstar/port/iscsit

42 #
43 # Include common rules.
44 #
45 include ../Makefile.$(ARCHDIR)

47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

54 #
55 # Overrides and depends_on
56 #
57 MODSTUBS_DIR = $(OBJS_DIR)
```

new/usr/src/uts/intel/iscsit/Makefile

2

```
58 LDFFLAGS += -dy -Ndrv/stmf -Nmisc/idm -Nfs/sockfs -Nmisc/md5 -Nmisc/ksock
60 C99LMODE= -Xc99=%all

62 CERRWARN += -_gcc=-Wno-switch
63 CERRWARN += -_gcc=-Wno-uninitialized
64 CERRWARN += -_gcc=-Wno-unused-label

66 # needs work
67 SMOFF += cast_assign,strcpy_overflow

70 #
71 # Default build targets.
72 #
73 .KEEP_STATE:

75 def: $(DEF_DEPS)

77 all: $(ALL_DEPS)

79 clean: $(CLEAN_DEPS)

81 clobber: $(CLOBBER_DEPS)

83 lint: $(LINT_DEPS)

85 modlintlib: $(MODLINTLIB_DEPS)

87 clean.lint: $(CLEAN_LINT_DEPS)

89 install: $(INSTALL_DEPS)

91 #
92 # Include common targets.
93 #
94 include ../Makefile.targ
```

new/usr/src/uts/intel/iser/Makefile

1

```
*****
1971 Mon Jan 7 13:09:20 2019
new/usr/src/uts/intel/iser/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 # usr/src/uts/intel/iser/Makefile
22 #
23 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.

28 #
29 # Path to the base of the uts directory tree (usually /usr/src/uts).
30 #
31 UTBASE = ../..

33 #
34 # Define the module and object file sets.
35 #
36 MODULE = iser
37 OBJECTS = $(ISER_OBJS:%=$(OBJDIR)/%)
38 LINTS = $(ISER_OBJS:%.o=$(LINTSDIR)/%.ln)
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
40 LDFLAGS += -dy -Nmisc/ibt1 -Nmisc/ibcm -Nmisc/idm
41 CONF_SRCDIR = $(UTBASE)/common/io/ib/clients/iser

43 #
44 # Include common rules.
45 #
46 include $(UTBASE)/intel/Makefile.intel

48 CERRWARN += -_gcc=-Wno-uninitialized

50 # needs work
51 $(OBJDIR)/iser_ib.o := SMOFF += deref_check

53 #
54 # Define targets
55 #
56 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
57 LINT_TARGET = $(MODULE).lint
58 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

60 #
61 # Default build targets.
```

new/usr/src/uts/intel/iser/Makefile

2

```
62 #
63 .KEEP_STATE:

65 def: $(DEF_DEPS)

67 all: $(ALL_DEPS)

69 clean: $(CLEAN_DEPS)

71 clobber: $(CLOBBER_DEPS)

73 lint: $(LINT_DEPS)

75 modlintlib: $(MODLINTLIB_DEPS)

77 clean.lint: $(CLEAN_LINT_DEPS)

79 install: $(INSTALL_DEPS)

81 #
82 # Include common targets.
83 #
84 include $(UTBASE)/intel/Makefile.targ
```

```

*****
1882 Mon Jan 7 13:09:21 2019
new/usr/src/uts/intel/iwi/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
26 # ident "%Z%M% %I% %E% SMI"
27 #
28 # This makefile drives the production of the ipw driver kernel module.
29 #
30 # i86pc architecture dependent
31 #
32 #
33 # Path to the base of the uts directory tree (usually /usr/src/uts).
34 #
35 UTSBASE = ../../
36 #
37 # Define the module and object file sets.
38 #
39 MODULE = iwi
40 OBJECTS = $(IWI_OBJS:%=$(OBJS_DIR)/%)
41 LINTS = $(IWI_OBJS:%.o=$(LINTS_DIR)/%.ln)
42 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
43 #
44 # Include common rules.
45 #
46 include $(UTSBASE)/intel/Makefile.intel
47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY) $(CONFMOD) $(ITUMOD)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
53 #
54 CPPFLAGS += -I. -D_KERNEL
55 LDFLAGS += -dy -Nmisc/mac -Nmisc/net80211 -Ndrv/random -Ndrv/ip

```

```

55 # needs work
56 $(OBJS_DIR)/ipw2200.o := SMOFF += deref_check
57 #
58 #
59 # Default build targets.
60 #
61 .KEEP_STATE:
62 #
63 def: $(DEF_DEPS)
64 #
65 all: $(ALL_DEPS)
66 #
67 clean: $(CLEAN_DEPS)
68 #
69 clobber: $(CLOBBER_DEPS)
70 #
71 lint: $(LINT_DEPS)
72 #
73 modlintlib: $(MODLINTLIB_DEPS)
74 #
75 clean.lint: $(CLEAN_LINT_DEPS)
76 #
77 install: $(INSTALL_DEPS)
78 #
79 #
80 # Include common targets.
81 #
82 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/iwk/Makefile

1

```
*****
2131 Mon Jan 7 13:09:22 2019
new/usr/src/uts/intel/iwk/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
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24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # This makefile drives the production of the iwkc driver kernel module.
29 #
30 # i86pc architecture dependent
31 #

33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../

38 #
39 # Define the module and object file sets.
40 #
41 MODULE = iwkc
42 OBJECTS = $(IWKC_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(IWKC_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

46 #
47 # Include common rules.
48 #
49 include $(UTSBASE)/intel/Makefile.intel

51 #
52 # Define targets
53 #
54 ALL_TARGET = $(BINARY) $(CONFMOD) $(ITUMOD)
55 LINT_TARGET = $(MODULE).lint
56 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

58 #
59 # Overrides
60 #
61 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
```

new/usr/src/uts/intel/iwk/Makefile

2

```
63 CPPFLAGS += -I. -D_KERNEL -DIWL=4965
65 LDFLAGS += -dy -Nmisc/mac -Nmisc/net80211 -Ndrv/random -Ndrv/ip

67 CERRWARN += -_gcc=-Wno-unused-label
68 CERRWARN += -_gcc=-Wno-uninitialized

70 # needs work
71 $(OBJS_DIR)/iwkc2.o := SMOFF += precedence

73 #
74 # Default build targets.
75 #
76 .KEEP_STATE:

78 def: $(DEF_DEPS)

80 all: $(ALL_DEPS)

82 clean: $(CLEAN_DEPS)

84 clobber: $(CLOBBER_DEPS)

86 lint: $(LINT_DEPS)

88 modlintlib: $(MODLINTLIB_DEPS)

90 clean.lint: $(CLEAN_LINT_DEPS)

92 install: $(INSTALL_DEPS)

94 #
95 # Include common targets.
96 #
97 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/iwn/Makefile

1

```
*****
1845 Mon Jan 7 13:09:24 2019
new/usr/src/uts/intel/iwn/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # This file and its contents are supplied under the terms of the
3 # Common Development and Distribution License ("CDDL"), version 1.0.
4 # You may only use this file in accordance with the terms of version
5 # 1.0 of the CDDL.
6 #
7 # A full copy of the text of the CDDL should have accompanied this
8 # source. A copy of the CDDL is also available via the Internet at
9 # http://www.illumos.org/license/CDDL.
10 #
11 #
12 #
13 # Copyright 2016 Hans Rosenfeld <rosenfeld@grumpf.hope-2000.org>
14 #
15 # Copyright (c) 2018, Joyent, Inc.
16 #
17 #
18 # This makefile drives the production of the iwn driver kernel module.
19 #
20 # intel architecture dependent
21 #
22 #
23 #
24 # Path to the base of the uts directory tree (usually /usr/src/uts).
25 #
26 UTSBASE = ../../
27 #
28 #
29 # Define firmware location & files
30 #
31 FWDIR = $(UTSBASE)/common/io/iwn/fw-iw
32 FWFILES = iwlwifi-100-5.ucode iwlwifi-1000-3.ucode iwlwifi-105-6.ucode \
33 iwlwifi-135-6.ucode iwlwifi-2000-6.ucode iwlwifi-2030-6.ucode \
34 iwlwifi-4965-2.ucode iwlwifi-5000-2.ucode iwlwifi-5150-2.ucode \
35 iwlwifi-6000-4.ucode iwlwifi-6000g2a-6.ucode iwlwifi-6000g2b-6.ucode \
36 iwlwifi-6050-5.ucode
37 #
38 #
39 # Define the module and object file sets.
40 #
41 MODULE = iwn
42 OBJECTS = $(IWN_OBJS:%=$(OBJS_DIR)/%)
43 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
44 ROOTFIRMWARE = $(FWFILES:%=$(ROOT_FIRMWARE_DIR)/$(MODULE)/%)
45 #
46 #
47 # Include common rules.
48 #
49 include $(UTSBASE)/intel/Makefile.intel
50 #
51 #
52 # Define targets
53 #
54 ALL_TARGET = $(BINARY) $(CONFMOD) $(ITUMOD)
55 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTFIRMWARE)
56 #
57 LDFLAGS += -dy -Nmisc/mac -Nmisc/net80211 -Ndrv/random -Ndrv/ip
58 #
59 # needs work
60 SMOFF += all_func_returns
```

new/usr/src/uts/intel/iwn/Makefile

2

```
62 #
63 # Default build targets.
64 #
65 .KEEP_STATE:
66 #
67 def: $(DEF_DEPS)
68 #
69 all: $(ALL_DEPS)
70 #
71 clean: $(CLEAN_DEPS)
72 #
73 clobber: $(CLOBBER_DEPS)
74 #
75 lint:
76 modlintlib:
77 clean.lint:
78 #
79 install: $(INSTALL_DEPS)
80 #
81 #
82 # Include common targets.
83 #
84 include $(UTSBASE)/intel/Makefile.targ
```



new/usr/src/uts/intel/ixgbe/Makefile

1

```
*****
2398 Mon Jan 7 13:09:25 2019
new/usr/src/uts/intel/ixgbe/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 # Copyright 2016 OmniTI Computer Consulting, Inc. All rights reserved.
25 # Copyright (c) 2018, Joyent, Inc.
26 #
27 # uts/intel/ixgbe/Makefile
28 #
29 # This makefile drives the production of the ixgbe
30 # network driver kernel module.
31 #
32 # intel architecture dependent
33 #
34 #
35 #
36 # Paths to the base of the uts directory trees
37 #
38 UTSBASE = ../../

40 #
41 # Define the module and object file sets.
42 #
43 MODULE = ixgbe
44 OBJECTS = $(IXGBE_OBJS:%=$(OBJS_DIR)/%)
45 OBJECTS += $(IXGBE_INTC_OBJS:%=$(OBJS_DIR)/%)
46 LINTS = $(IXGBE_OBJS:%.o=$(LINTS_DIR)/%.ln)
47 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
48 CONF_SRCDIR = $(UTSBASE)/common/io/ixgbe

50 #
51 # Include common rules.
52 #
53 include $(UTSBASE)/intel/Makefile.intel

55 INC_PATH += -I$(UTSBASE)/common/io/ixgbe
56 INC_PATH += -I$(UTSBASE)/common/io/ixgbe/core

58 CERRWARN += _gcc=-Wno-parentheses
59 CERRWARN += _gcc=-Wno-switch
60 CERRWARN += _gcc=-Wno-uninitialized
61 CERRWARN += _gcc=-Wno-unused-value
```

new/usr/src/uts/intel/ixgbe/Makefile

2

```
62 CERRWARN += _cc=-erroff=E_STATEMENT_NOT_REACHED

64 # 3rd party code
65 SMOFF += all_func_returns

67 #
68 # Define targets
69 #
70 ALL_TARGET = $(BINARY) $(CONFMOD)
71 LINT_TARGET = $(MODULE).lint
72 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

74 #
75 # Driver depends on MAC
76 #
77 LDFLAGS += -dy -N misc/mac

79 #
80 # Default build targets.
81 #
82 .KEEP_STATE:

84 def: $(DEF_DEPS)

86 all: $(ALL_DEPS)

88 clean: $(CLEAN_DEPS)

90 clobber: $(CLOBBER_DEPS)

92 lint: $(LINT_DEPS)

94 modlintlib: $(MODLINTLIB_DEPS)

96 clean.lint: $(CLEAN_LINT_DEPS)

98 install: $(INSTALL_DEPS)

100 #
101 # Include common targets.
102 #
103 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/kcf/Makefile

1

```
*****
2408 Mon Jan 7 13:09:25 2019
new/usr/src/uts/intel/kcf/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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4 # The contents of this file are subject to the terms of the
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2010 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
26 # This makefile drives the production of the Kernel Cryptographic
27 # Framework core module.
28 # Intel implementation architecture dependent
29 #
30 #
31 # Path to the base of the uts directory tree (usually /usr/src/uts).
32 #
33 # UTSBASE = ../..
34 # COM_DIR = $(COMMONBASE)/crypto
35 #
36 # Define the module and object file sets.
37 #
38 # MODULE = kcf
39 # LINTS = $(KCF_OBJS:%.o=$(LINTS_DIR)/%.ln)
40 # KCF_OBJS_32 =
41 # KCF_OBJS_64 = gcm_intel.o
42 # KCF_OBJS += $(KCF_OBJS_$(CLASS))
43 # OBJECTS = $(KCF_OBJS:%=$(OBJDIR)/%)
44 # ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
45 #
46 # Include common rules.
47 #
48 # include $(UTSBASE)/intel/Makefile.intel
49 #
50 # Define targets
51 #
52 # ALL_TARGET = $(BINARY)
53 # LINT_TARGET = $(MODULE).lint
54 # INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
55 #
```

new/usr/src/uts/intel/kcf/Makefile

2

```
57 # Linkage dependencies
58 #
59 #
60 # lint pass one enforcement
61 #
62 #
63 # CFLAGS += $(CVERBOSE) -I$(COM_DIR)
64 # AS_CPPFLAGS += -I../..$(PLATFORM)
65 #
66 # LINTTAGS += -I$(COM_DIR)
67 #
68 # CERRWARN += -_gcc=-Wno-switch
69 # CERRWARN += -_gcc=-Wno-uninitialized
70 # CERRWARN += -_gcc=-Wno-parentheses
71 # CERRWARN += -_gcc=-Wno-unused-label
72 #
73 # needs work
74 # SMOFF += all_func_returns,signed_integer_overflow_check
75 #
76 # Default build targets.
77 #
78 # .KEEP_STATE:
79 #
80 # def: $(DEF_DEPS)
81 #
82 # all: $(ALL_DEPS)
83 #
84 # clean: $(CLEAN_DEPS)
85 #
86 # clobber: $(CLOBBER_DEPS)
87 #
88 # lint: $(LINT_DEPS)
89 #
90 # modlintlib: $(MODLINTLIB_DEPS)
91 #
92 # clean.lint: $(CLEAN_LINT_DEPS)
93 #
94 # install: $(INSTALL_DEPS)
95 #
96 #
97 # Include common targets.
98 #
99 #
100 # include $(UTSBASE)/intel/Makefile.targ
101 #
102 # $(OBJDIR)/%.o: $(COM_DIR)/modes/amd64/%.s
103 # $(COMPILE.s) -o @$ $(COM_DIR)/modes/amd64/$(@F:.o=.s)
104 # $(POST_PROCESS_O)
105 #
106 # $(OBJDIR)/%.ln: $(COM_DIR)/modes/amd64/%.s
107 # @$(LHEAD) $(LINT.s) $(COM_DIR)/modes/amd64/$(@F:.ln=.s) $(LTAIL))
```

```

*****
2646 Mon Jan 7 13:09:25 2019
new/usr/src/uts/intel/keysock/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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20 #
21 #
22 # uts/intel/keysock/Makefile
23 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.

28 #
29 # This makefile drives the production of the keysock driver
30 # kernel module.
31 #
32 # intel implementation architecture dependent
33 #

35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../

40 #
41 # Define the module and object file sets.
42 #
43 MODULE = keysock
44 OBJECTS = $(KEYSOCK_OBJS:%=$(OBJS_DIR)/%)
45 LINTS = $(KEYSOCK_OBJS:%.o=$(LINTS_DIR)/%.ln)
46 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
47 ROOTLINK = $(ROOT_STRMOD_DIR)/$(MODULE)
48 CONF_SRCDIR = $(UTSBASE)/common/inet/ip

50 #
51 # Include common rules.
52 #
53 include $(UTSBASE)/intel/Makefile.intel

55 #
56 # Define targets
57 #
58 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
59 LINT_TARGET = $(MODULE).lint
60 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTLINK) $(ROOT_CONFFILE)

```

```

62 #
63 # Linkage dependencies
64 #
65 LDFLAGS += -dy -Ndrv/ip

67 #
68 # For now, disable these lint checks; maintainers should endeavor
69 # to investigate and remove these for maximum lint coverage.
70 # Please do not carry these forward to new Makefiles.
71 #
72 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
73 LINTTAGS += -erroff=E_STATIC_UNUSED
74 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
75 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

77 CERRWARN += -_gcc=-Wno-unused-variable

79 # needs work
80 $(OBJS_DIR)/keysockddi.o := SMOFF += index_overflow

82 #
83 # Default build targets.
84 #
85 .KEEP_STATE:

87 def: $(DEF_DEPS)

89 all: $(ALL_DEPS) $(SISCHECK_DEPS)

91 clean: $(CLEAN_DEPS) $(SISCLEAN_DEPS)

93 clobber: $(CLOBBER_DEPS) $(SISCLEAN_DEPS)

95 lint: $(LINT_DEPS)

97 modlintlib: $(MODLINTLIB_DEPS)

99 clean.lint: $(CLEAN_LINT_DEPS)

101 install: $(INSTALL_DEPS) $(SISCHECK_DEPS)

103 $(ROOTLINK): $(ROOT_STRMOD_DIR) $(ROOTMODULE)
104 -$(RM) $@; ln $(ROOTMODULE) $@

106 #
107 # Include common targets.
108 #
109 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/kgssapi/Makefile

1

```
*****
2408 Mon Jan 7 13:09:25 2019
new/usr/src/uts/intel/kgssapi/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/kgssapi/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 # Copyright (c) 2011 Bayard G. Bell. All rights reserved.
27 #
28 # Copyright (c) 2018, Joyent, Inc.
29 #
30 # This makefile drives the production of the kgssapi kernel module.
31 #
32 # intel implementation architecture dependent
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 # UTSBASE = ../../
37 #
38 # Define the module and object file sets.
39 #
40 # MODULE = kgssapi
41 # OBJECTS = $(KGSSD_OBJS:%=$(OBJS_DIR)/%)
42 # OBJECTS += $(KGSSD_DERIVED_OBJS:%=$(OBJS_DIR)/%)
43 # LINTS = $(KGSSD_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 # ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
45 #
46 # Include common rules.
47 #
48 # include $(UTSBASE)/intel/Makefile.intel
49 #
50 # Define targets
51 #
52 # ALL_TARGET = $(BINARY)
53 # LINT_TARGET = $(MODULE).lint
54 # INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_KGSS_DIR)
55 #
56 # Define dependencies on rpcmod, rpcsec, and tlimod
57 #
```

new/usr/src/uts/intel/kgssapi/Makefile

2

```
58 LDFLAGS += -dy -N strmod/rpcmod -N misc/rpcsec -N misc/tlimod
60 #
61 # For now, disable these lint checks; maintainers should endeavor
62 # to investigate and remove these for maximum lint coverage.
63 # Please do not carry these forward to new Makefiles.
64 #
65 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
66 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
67 #
68 CERRWARN += -_gcc=-Wno-unused-variable
69 CERRWARN += -_gcc=-Wno-uninitialized
70 #
71 # needs work
72 $(OBJS_DIR)/gssd_clnt_stubs.o := SMOFF += indenting,deref_check
73 #
74 #
75 # Default build targets.
76 #
77 .KEEP_STATE:
78 #
79 def: $(DEF_DEPS)
80 #
81 all: $(ALL_DEPS)
82 #
83 clean: $(CLEAN_DEPS)
84 #
85 clobber: $(CLOBBER_DEPS)
86 #
87 lint: $(LINT_DEPS)
88 #
89 modlintlib: $(MODLINTLIB_DEPS)
90 #
91 clean.lint: $(CLEAN_LINT_DEPS)
92 #
93 install: $(INSTALL_DEPS)
94 #
95 #
96 # Include common targets.
97 #
98 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/kiconv\_ja/Makefile

1

```
*****
2071 Mon Jan 7 13:09:26 2019
new/usr/src/uts/intel/kiconv_ja/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/kiconv_ja/Makefile
23 #
24 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # ident "%Z%M% %I% %E% SMI"
28 #
29 # This makefile drives the production of the kiconv_ja kernel module.
30 #
31 # intel architecture dependent
32 #
33 #
29 #
30 # Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 UTSBASE = ../../

34 #
35 # Define the module and object file sets.
36 #
37 MODULE = kiconv_ja
38 OBJECTS = $(KICONV_JA_OBJS:%=$(OBJSDIR)/%)
39 LINTS = $(KICONV_JA_OBJS:%.o=$(LINTSDIR)/%.ln)
40 ROOTMODULE = $(ROOT_KICONV_DIR)/$(MODULE)

42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel

47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

54 #
55 # Overrides.
```

new/usr/src/uts/intel/kiconv\_ja/Makefile

2

```
56 #
57 CFLAGS += $(CCVERBOSE)

59 #
60 # For now, disable these lint checks; maintainers should endeavor
61 # to investigate and remove these for maximum lint coverage.
62 # Please do not carry these forward to new Makefiles.
63 #
64 LINTTAGS += -errorf=E_PTRDIFF_OVERFLOW

66 # smatch not happy with this source
67 SMATCH=off

69 #
70 # Default build targets.
71 #
72 .KEEP_STATE:

74 def: $(DEF_DEPS)

76 all: $(ALL_DEPS)

78 clean: $(CLEAN_DEPS)

80 clobber: $(CLOBBER_DEPS)

82 lint: $(LINT_DEPS)

84 modlintlib: $(MODLINTLIB_DEPS)

86 clean.lint: $(CLEAN_LINT_DEPS)

88 install: $(INSTALL_DEPS)

90 #
91 # Include common targets.
92 #
93 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/klmmod/Makefile

1

```
*****
2069 Mon Jan 7 13:09:26 2019
new/usr/src/uts/intel/klmmod/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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11 # and limitations under the License.
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2012 by Delphix. All rights reserved.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the network lock manager server
28 # specific kernel module.
29 #
30 # intel implementation architecture dependent
31 #

29 #
30 # Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 UTSBASE = ../../

34 #
35 # Define the module and object file sets.
36 #
37 MODULE = klmmod
38 OBJECTS = ${KLMOD_OBJS:%=${OBJSDIR}/%}
39 LINTS = ${KLMOD_OBJS:%.o=${LINTSDIR}/%.ln}
40 ROOTMODULE = ${ROOT_MISC_DIR}/${MODULE}

42 #
43 # Include common rules.
44 #
45 include ${UTSBASE}/intel/Makefile.intel

47 #
48 # Define targets
49 #
50 ALL_TARGET = ${BINARY}
51 LINT_TARGET = ${MODULE}.lint
52 INSTALL_TARGET = ${BINARY} ${ROOTMODULE}

54 #
55 # Overrides.
56 #
```

new/usr/src/uts/intel/klmmod/Makefile

2

```
57 LDPLAGS += -dy -Nstrmod/rpcmod -Nfs/nfs
58 LDPLAGS += -M ${UTSBASE}/common/klm/mapfile-mod
59 CTFMRGFLAGS += -f

61 #
62 # Code generated by rpcgen triggers the -Wswitch warning.
63 #
64 CERWARN += -_gcc=-Wno-switch

66 # needs work
67 $(OBJSDIR)/nlm_service.o := SMOFF += signed_integer_overflow_check

69 #
70 # Default build targets.
71 #
72 .KEEP_STATE:

74 def: $(DEF_DEPS)

76 all: $(ALL_DEPS)

78 clean: $(CLEAN_DEPS)

80 clobber: $(CLOBBER_DEPS)

82 lint: $(LINT_DEPS)

84 modlintlib: $(MODLINTLIB_DEPS)

86 clean.lint: $(CLEAN_LINT_DEPS)

88 install: $(INSTALL_DEPS)

90 #
91 # Include common targets.
92 #
93 include ${UTSBASE}/intel/Makefile.targ
```

```

*****
3053 Mon Jan 7 13:09:27 2019
new/usr/src/uts/intel/kmech_krb5/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 # Copyright (c) 2011 Bayard G. Bell. All rights reserved.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
28 #
29 # This makefile drives the production of the gl_kmech_krb5 kernel module.
30 #
31 # intel implementation architecture dependent
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../
37 #
38 #
39 # Define the module and object file sets.
40 #
41 MODULE = kmech_krb5
42 OBJECTS = $(KGSS_KRB5_OBJS:%=$(OBJDIR)/%)
43 LINTS = $(KGSS_KRB5_OBJS:%.o=$(LINTSDIR)/%.ln)
44 ROOTMODULE = $(ROOT_KGSS_DIR)/$(MODULE)
45 #
46 #
47 # Include common rules.
48 #
49 include $(UTSBASE)/intel/Makefile.intel
50 #
51 #
52 # Define targets
53 #
54 ALL_TARGET = $(BINARY)
55 LINT_TARGET = $(MODULE).lint
56 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
57 #
58 #
59 # Defined kgssapi and md5 as dependencies
60 #
61 LDFLAGS += -dy -N misc/kgssapi -N misc/md5

```

```

63 #
64 # For now, disable these lint checks; maintainers should endeavor
65 # to investigate and remove these for maximum lint coverage.
66 # Please do not carry these forward to new Makefiles.
67 #
68 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
69 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
70 LINTTAGS += -erroff=E_STATIC_UNUSED
71 #
72 CERRWARN += -_gcc=-Wno-unused-function
73 CERRWARN += -_gcc=-Wno-unused-label
74 CERRWARN += -_gcc=-Wno-uninitialized
75 CERRWARN += -_gcc=-Wno-parentheses
76 #
77 # needs work
78 SMOFF += indenting
79 #
80 #
81 # Default build targets.
82 #
83 .KEEP_STATE:
84 #
85 def: $(DEF_DEPS)
86 #
87 all: $(ALL_DEPS)
88 #
89 clean: $(CLEAN_DEPS)
90 #
91 clobber: $(CLOBBER_DEPS)
92 #
93 lint: $(LINT_DEPS)
94 #
95 modlintlib: $(MODLINTLIB_DEPS)
96 #
97 clean.lint: $(CLEAN_LINT_DEPS)
98 #
99 install: $(INSTALL_DEPS)
100 #
101 KMECHKRB5_BASE=$(UTSBASE)/common/gssapi/mechs/krb5
102 KGSSDFLAGS=-I $(UTSBASE)/common/gssapi/include
103 KGSSDFLAGS += $(KRB5_DEFS)
104 #
105 $(OBJDIR)/%.o: $(KMECHKRB5_BASE)/crypto/crc32/%.c
106 $(COMPILE.c) $(KGSSDFLAGS) -o $@ $<
107 $(CTFCONVERT_O)
108 #
109 $(LINTSDIR)/%.ln: $(KMECHKRB5_BASE)/crypto/crc32/%.c
110 @$(LHEAD) $(LINT.c) $(KGSSDFLAGS) $< $(LTAIL))
111 #
112 #
113 # Include common targets.
114 #
115 include $(UTSBASE)/intel/Makefile.targ
116 #
117 INC_PATH += \
118 -I$(UTSBASE)/common/gssapi \
119 -I$(UTSBASE)/common/gssapi/include \
120 -I$(UTSBASE)/common/gssapi/mechs/krb5/include \
121 -I$(SRC)/lib/gss/mechs/mech_krb5/include \
122 -I$(SRC)/lib/gss/mechs/mech_krb5/krb5/krb

```

```

*****
2295 Mon Jan 7 13:09:28 2019
new/usr/src/uts/intel/llc1/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/llc1/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the Sound-Blaster driver
31 # kernel module.
32 #
33 # intel architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTSBASE = ../..

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = llc1
45 OBJECTS = $(LLC1_OBJS:%=$(OBJS_DIR)/%)
46 LINTS = $(LLC1_OBJS:%.o=$(LINTS_DIR)/%.ln)
47 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
48 CONF_SRCDIR = $(UTSBASE)/common/io

50 #
51 # Include common rules.
52 #
53 include $(UTSBASE)/intel/Makefile.intel

55 #
56 # Define targets
57 #
58 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
59 LINT_TARGET = $(MODULE).lint
60 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

```

```

62 #
63 # For now, disable these lint checks; maintainers should endeavor
64 # to investigate and remove these for maximum lint coverage.
65 # Please do not carry these forward to new Makefiles.
66 #
67 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
68 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW

70 CERRWARN += -_gcc=-Wno-uninitialized

72 # needs work
73 $(OBJS_DIR)/llc1.o := SMOFF += no_if_block,deref_check

75 #
76 # Default build targets.
77 #
78 .KEEP_STATE:

80 def: $(DEF_DEPS)

82 all: $(ALL_DEPS)

84 clean: $(CLEAN_DEPS)

86 clobber: $(CLOBBER_DEPS)

88 lint: $(LINT_DEPS)

90 modlintlib: $(MODLINTLIB_DEPS)

92 clean.lint: $(CLEAN_LINT_DEPS)

94 install: $(INSTALL_DEPS)

96 #
97 # Include common targets.
98 #
99 include $(UTSBASE)/intel/Makefile.targ

```



```

*****
2254 Mon Jan 7 13:09:28 2019
new/usr/src/uts/intel/mac/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

26 # This makefile drives the production of the mac driver
27 # kernel module.
28 #
29 #
30 # Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 UTSBASE = ../..

32 #
33 # Define the module and object file sets.
34 #
35 MODULE = mac
36 OBJECTS = $(MAC_OBJS:%=$(OBJS_DIR)/%)
37 LINTS = $(MAC_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)

40 #
41 # Include common rules.
42 #
43 include $(UTSBASE)/intel/Makefile.intel

45 #
46 # Define targets
47 #
48 ALL_TARGET = $(BINARY)
49 LINT_TARGET = $(MODULE).lint
50 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

52 #
53 # Overrides.
54 #
55 CFLAGS += $(CCVERBOSE)
56 LDFLAGS += -dy
57 INC_PATH += -I$(UTSBASE)/common/io/bpf

```

```

59 LINTTAGS += -eroff=E_PTRDIFF_OVERFLOW
60 LINTTAGS += -eroff=E_BAD_PTR_CAST_ALIGN

62 CERRWARN += -_gcc=-Wno-unused-label
63 CERRWARN += -_gcc=-Wno-uninitialized
64 CERRWARN += -_gcc=-Wno-type-limits
65 CERRWARN += -_gcc=-Wno-switch
66 CERRWARN += -_gcc=-Wno-unused-variable

68 # needs work
69 SMOFF += all_func_returns
70 $(OBJS_DIR)/mac.o := SMOFF += deref_check
71 $(OBJS_DIR)/mac_util.o := SMOFF += signed

73 # false positive
74 $(OBJS_DIR)/mac_sched.o := SMOFF += assign_vs_compare
75 #
76 # Default build targets.
77 #
78 .KEEP_STATE:

80 def: $(DEF_DEPS)

82 all: $(ALL_DEPS)

84 clean: $(CLEAN_DEPS)

86 clobber: $(CLOBBER_DEPS)

88 lint: $(LINT_DEPS)

90 modlintlib: $(MODLINTLIB_DEPS)

92 clean.lint: $(CLEAN_LINT_DEPS)

94 install: $(INSTALL_DEPS)

96 #
97 # Include common targets.
98 #
99 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/mega\_sas/Makefile

1

\*\*\*\*\*

2141 Mon Jan 7 13:09:28 2019

new/usr/src/uts/intel/mega\_sas/Makefile

10077 usr/src/uts/ Makefile changes for smatch

\*\*\*\*\*

```
1 #
2 # CDDL HEADER START
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19 # CDDL HEADER END
20 #
21 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
22 # Use is subject to license terms.
23 #
24 # Copyright (c) 2018, Joyent, Inc.

26 #
27 # uts/intel/mega_sas/Makefile
28 #
29 # This makefile drives the production of the mega_sas driver kernel module
30 #
31 # intel implementation architecture dependent
32 #

34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../../

39 #
40 # Define the module and object file sets.
41 #
42 MODULE = mega_sas
43 OBJECTS = $(MEGA_SAS_OBJS:%=$(OBJS_DIR)/%)
44 LINTS = $(MEGA_SAS_OBJS:%.o=$(LINTS_DIR)/%.ln)
45 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
46 CONF_SRCDIR = $(UTSBASE)/common/io/mega_sas

48 #
49 # Include common rules.
50 #
51 include $(UTSBASE)/intel/Makefile.intel

53 #
54 # Define targets
55 #
56 ALL_TARGET = $(BINARY) $(CONFMOD)
57 LINT_TARGET = $(MODULE).lint
58 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

60 #
61 # Kernel Module Dependencies
```

new/usr/src/uts/intel/mega\_sas/Makefile

2

```
62 #
63 LDFLAGS += -dy -Nmisc/scsi

65 CERRWARN += _gcc=-Wno-uninitialized

67 # needs work
68 $(OBJS_DIR)/megaraid_sas.o := SMOFF += snprintf_overflow,all_func_returns

70 #
71 # Default build targets.
72 #
73 .KEEP_STATE:

75 def: $(DEF_DEPS)

77 all: $(ALL_DEPS)

79 clean: $(CLEAN_DEPS)

81 clobber: $(CLOBBER_DEPS)

83 lint: $(LINT_DEPS)

85 modlintlib: $(MODLINTLIB_DEPS)

87 clean.lint: $(CLEAN_LINT_DEPS)

89 install: $(INSTALL_DEPS)

91 #
92 # Include common targets.
93 #
94 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
1867 Mon Jan 7 13:09:29 2019
new/usr/src/uts/intel/mii/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/mii/Makefile
23 #
24 # Copyright 2010 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the mii support module.
28 #

29 #
30 # Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 UTSBASE = ../../

34 #
35 # Define the module and object file sets.
36 #
37 MODULE = mii
38 OBJECTS = $(MII_OBJS:%=$(OBJS_DIR)/%)
39 LINTS = $(MII_OBJS:%.o=$(LINTS_DIR)/%.ln)
40 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)

42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel

47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

54 LDFLAGS += -dy -N misc/mac

56 CERRWARN += _gcc=-Wno-switch
57 CERRWARN += _gcc=-Wno-uninitialized

59 # needs work

```

```

60 SMOFF += all_func_returns

62 #
63 # Default build targets.
64 #
65 .KEEP_STATE:

67 def: $(DEF_DEPS)

69 all: $(ALL_DEPS)

71 clean: $(CLEAN_DEPS)

73 clobber: $(CLOBBER_DEPS)

75 lint: $(LINT_DEPS)

77 modlintlib: $(MODLINTLIB_DEPS)

79 clean.lint: $(CLEAN_LINT_DEPS)

81 install: $(INSTALL_DEPS)

83 #
84 # Include common targets.
85 #
86 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2029 Mon Jan 7 13:09:30 2019
new/usr/src/uts/intel/mpt_sas/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
22 # Use is subject to license terms.
23 #
24 # Copyright (c) 2018, Joyent, Inc.
25 # This makefile drives the production of the mpt_sas driver kernel module.
26 # intel architecture dependent
27 #

26 #
27 # Paths to the base of the uts directory trees
28 #
29 UTSBASE = ../../../../src/uts

31 #
32 # Define the module and object file sets.
33 #
34 MODULE = mpt_sas
35 OBJECTS = $(MPTSAS_OBJS:%=$(OBJS_DIR)/%)
36 LINTS = $(MPTSAS_OBJS%.o=$(LINTS_DIR)/%.ln)
37 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
38 CONF_SRCDIR = $(UTSBASE)/common/io/scsi/adapters/mpt_sas/

40 #
41 # Kernel Module Dependencies
42 #
43 LDFLAGS += -dy -Nmisc/scsi -Ndrv/scsi_vhci -Nmisc/sata

45 #
46 # Define targets
47 #
48 ALL_TARGET = $(BINARY) $(CONFMOD)
49 LINT_TARGET = $(MODULE).lint
50 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

52 #
53 # Include common rules.
54 #
55 include $(UTSBASE)/intel/Makefile.intel

57 CERRWARN += -_gcc=-Wno-uninitialized

```

```

59 # needs work
60 $(OBJS_DIR)/mptsas_raid.o := SMOFF += index_overflow
61 $(OBJS_DIR)/mptsas.o := SMOFF += deref_check

63 #
64 # Default build targets.
65 #
66 .KEEP_STATE:

68 all: $(ALL_DEPS)

70 def: $(DEF_DEPS)

72 clean: $(CLEAN_DEPS)

74 clobber: $(CLOBBER_DEPS)

76 lint: $(LINT_DEPS)

78 modlintlib: $(MODLINTLIB_DEPS)

80 clean.lint: $(CLEAN_LINT_DEPS)

82 install: $(INSTALL_DEPS)

84 #
85 # Include common targets.
86 #
87 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2005 Mon Jan 7 13:09:31 2019
new/usr/src/uts/intel/mr_sas/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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22 # Use is subject to license terms.
23 #
24 # uts/intel/mr_sas/Makefile
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the mr_sas driver kernel module.
28 # intel implementation architecture dependent
29 #

28 #
29 # Path to the base of the uts directory tree (usually /usr/src/uts).
30 #
31 UTSBASE = ../../

33 #
34 # Define the module and object file sets.
35 #
36 MODULE = mr_sas
37 OBJECTS = $(MR_SAS_OBJS:%=$(OBJDIR)/%)
38 LINTS = $(MR_SAS_OBJS:%.o=$(LINTSDIR)/%.ln)
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
40 CONF_SRCDIR = $(UTSBASE)/common/io/mr_sas

42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel

47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY) $(CONFMOD)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

54 #
55 # Kernel Module Dependencies
56 #
57 LDFLAGS += -dy -Nmisc/scsi

```

```

59 CERRWARN += -_gcc=-Wno-unused-label
60 CERRWARN += -_gcc=-Wno-switch
61 CERRWARN += -_gcc=-Wno-uninitialized

63 # needs work
64 SMATCH=off

66 #
67 # Default build targets.
68 #
69 .KEEP_STATE:

71 def: $(DEF_DEPS)

73 all: $(ALL_DEPS)

75 clean: $(CLEAN_DEPS)

77 clobber: $(CLOBBER_DEPS)

79 lint: $(LINT_DEPS)

81 modlintlib: $(MODLINTLIB_DEPS)

83 clean.lint: $(CLEAN_LINT_DEPS)

85 install: $(INSTALL_DEPS)

87 #
88 # Include common targets.
89 #
90 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/mwl/Makefile

1

```
*****
2201 Mon Jan 7 13:09:32 2019
new/usr/src/uts/intel/mwl/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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20 #
21 #
22 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # This makefile drives the production of the rwd driver kernel module.
29 #
30 # i86pc architecture dependent
31 #

33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../

38 #
39 # Define the module and object file sets.
40 #
41 MODULE = mwl
42 OBJECTS = $(MWL_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(MWL_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

46 #
47 # Include common rules.
48 #
49 include $(UTSBASE)/intel/Makefile.intel

51 #
52 # Define targets
53 #
54 ALL_TARGET = $(BINARY)
55 LINT_TARGET = $(MODULE).lint
56 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

58 #
59 # Driver depends on GLDv3 & wifi kernel support module.
60 #
61 LDFLAGS += -dy -Nmisc/mac -Nmisc/net80211
```

new/usr/src/uts/intel/mwl/Makefile

2

```
63 #
64 # Overrides
65 #
66 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN

68 CERRWARN += -_gcc=-Wno-uninitialized
69 CERRWARN += -_gcc=-Wno-unused-value

71 # needs work
72 SMOFF += return_cast

74 #
75 # Default build targets.
76 #
77 .KEEP_STATE:

79 def: $(DEF_DEPS)

81 all: $(ALL_DEPS)

83 clean: $(CLEAN_DEPS)

85 clobber: $(CLOBBER_DEPS)

87 lint: $(LINT_DEPS)

89 modlintlib: $(MODLINTLIB_DEPS)

91 clean.lint: $(CLEAN_LINT_DEPS)

93 install: $(INSTALL_DEPS)

95 #
96 # Include common targets.
97 #
98 include $(UTSBASE)/intel/Makefile.targ

100 #
101 # If you have any special case that general
102 # Makefile rules don't serve for you, just do
103 # it yourself.
104 #
```

```

*****
2011 Mon Jan 7 13:09:32 2019
new/usr/src/uts/intel/myril0ge/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright (c) 2009, 2010, Oracle and/or its affiliates. All rights reserved.
23 #
24 # Copyright (c) 2018, Joyent, Inc.

26 #
27 # Path to the base of the uts directory tree (usually /usr/src/uts).
28 #
29 UTSBASE = ../../

31 #
32 # Define the module and object file sets.
33 #
34 MODULE = myril0ge

36 OBJECTS += $(MYRI10GE_OBJS:%=$(OBJS_DIR)/%)

38 LINTS += $(MYRI10GE_OBJS:%.o=$(LINTS_DIR)/%.ln)

40 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel

47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

54 #
55 # Overrides
56 #
57 LINTTAGS += -DSOLARIS_S11 -erroff=E_STATIC_UNUSED

59 # needs work
60 SMATCH=off
58 #COPTIMIZE = -xO4

```

```

62 INC_PATH += -I$(UTSBASE)/common/io/myril0ge -I$(UTSBASE)/common/io/myril0ge/fir
64 CFLAGS += -DSOLARIS_S11

66 #
67 # Driver depends on MAC
68 #
69 LDFLAGS += -dy -N misc/mac -N drv/ip

71 #
72 # Default build targets.
73 #
74 .KEEP_STATE:

76 def: $(DEF_DEPS)

78 all: $(ALL_DEPS)

80 clean: $(CLEAN_DEPS)

82 clobber: $(CLOBBER_DEPS)

84 lint: $(LINT_DEPS)

86 modlintlib: $(MODLINTLIB_DEPS)

88 clean.lint: $(CLEAN_LINT_DEPS)

90 install: $(INSTALL_DEPS)

92 #
93 # Include common targets.
94 #
95 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2276 Mon Jan 7 13:09:32 2019
new/usr/src/uts/intel/nca/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/nca/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # ident "%Z%M% %I% %E% SMI"
28 #
29 # This makefile drives the production of the nca driver
30 # kernel module.
31 #
32 # Intel architecture dependent
33 #

29 #
30 # Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 UTBASE = ../../

34 #
35 # Define the module and object file sets.
36 #
37 MODULE = nca
38 OBJECTS = $(NCA_OBJS:%=$(OBJS_DIR)/%)
39 LINTS = $(NCA_OBJS:%.o=$(LINTS_DIR)/%.ln)
40 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
41 ROOTLINK = $(ROOT_STRMOD_DIR)/$(MODULE)
42 CONF_SRCDIR = $(UTSBASE)/common/inet/nca

44 #
45 # Include common rules.
46 #
47 include $(UTSBASE)/intel/Makefile.intel

49 #
50 # Define targets
51 #
52 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
53 LINT_TARGET = $(MODULE).lint
54 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTLINK) $(ROOT_CONFFILE)

```

```

56 #
57 # Overrides
58 #
59 LDFLAGS += -dy -Nfs/sockfs -Ndrv/ip

61 #
62 # For now, disable these lint checks; maintainers should endeavor
63 # to investigate and remove these for maximum lint coverage.
64 # Please do not carry these forward to new Makefiles.
65 #
66 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN

68 # needs work
69 $(OBJS_DIR)/ncaddi.o := SMOFF += index_overflow

71 #
72 # Default build targets.
73 #
74 .KEEP_STATE:

76 def: $(DEF_DEPS)

78 all: $(ALL_DEPS)

80 clean: $(CLEAN_DEPS)

82 clobber: $(CLOBBER_DEPS)

84 lint: $(LINT_DEPS)

86 modlintlib: $(MODLINTLIB_DEPS)

88 clean.lint: $(CLEAN_LINT_DEPS)

90 install: $(INSTALL_DEPS)

92 $(ROOTLINK): $(ROOT_STRMOD_DIR) $(ROOTMODULE)
93 -$(RM) $@; ln $(ROOTMODULE) $@

95 #
96 # Include common targets.
97 #
98 include $(UTSBASE)/intel/Makefile.targ

```



new/usr/src/uts/intel/net80211/Makefile

1

```
*****
2145 Mon Jan 7 13:09:32 2019
new/usr/src/uts/intel/net80211/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 #
28 #
29 # This file makes the atheros driver for an intel system
30 #
31 # intel architecture dependent
32 #
33 #
34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../../
38 #
39 # Define the module and object file sets.
40 #
41 MODULE = net80211
42 OBJECTS = $(NET80211_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(NET80211_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
45 #
46 #
47 # Include common rules.
48 #
49 include $(UTSBASE)/intel/Makefile.intel
50 #
51 #
52 # Define targets
53 #
54 ALL_TARGET = $(BINARY)
55 LINT_TARGET = $(MODULE).lint
56 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
57 #
58 #
59 # Overrides
60 #
61 CFLAGS += $(CCVERBOSE)
```

new/usr/src/uts/intel/net80211/Makefile

2

```
62 LDFLAGS += -dy -Nmisc/mac -Nmac/mac_wifi -Ndrv/ip
63 #
64 #
65 # STREAMS API limitations force us to turn off these lint checks.
66 #
67 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
68 #
69 CERRWARN += -_gcc=-Wno-switch
70 CERRWARN += -_gcc=-Wno-uninitialized
71 CERRWARN += -_gcc=-Wno-parentheses
72 #
73 # needs work
74 SMATCH=off
75 #
76 #
77 # Default build targets.
78 #
79 .KEEP_STATE:
80 #
81 def: $(DEF_DEPS)
82 #
83 all: $(ALL_DEPS)
84 #
85 clean: $(CLEAN_DEPS)
86 #
87 clobber: $(CLOBBER_DEPS)
88 #
89 lint: $(LINT_DEPS)
90 #
91 modlintlib: $(MODLINTLIB_DEPS)
92 #
93 clean.lint: $(CLEAN_LINT_DEPS)
94 #
95 install: $(INSTALL_DEPS)
96 #
97 #
98 # Include common targets.
99 #
100 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2896 Mon Jan 7 13:09:33 2019
new/usr/src/uts/intel/nfs/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/nfs/Makefile
23 #
24 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 # Copyright (c) 2011 Bayard G. Bell. All rights reserved.
27 #
28 # Copyright (c) 2018, Joyent, Inc.
29 # This makefile drives the production of the nfs file system
30 # kernel module.
31 #
32 # intel implementation architecture dependent
33 #
34 #
35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../
39 #
40 # Define the module and object file sets.
41 #
42 MODULE = nfs
43 OBJECTS = $(NFS_OBJS:%=$(OBJS_DIR)/%)
44 LINTS = $(NFS_OBJS:%.o=$(LINTS_DIR)/%.ln)
45 ROOTMODULE = $(ROOT_FS_DIR)/$(MODULE)
46 ROOTLINK = $(ROOT_SYS_DIR)/$(MODULE)
47 #
48 # Include common rules.
49 #
50 #
51 # Define targets
52 #
53 ALL_TARGET = $(BINARY)
54 LINT_TARGET = $(MODULE).lint
55 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTLINK)
56 #

```

```

57 # Overrides.
58 #
59 MODSTUBS_DIR = $(OBJS_DIR)
60 $(MODSTUBS_O) := AS_CPPFLAGS += -DNFS_MODULE
61 CLEANFILES += $(MODSTUBS_O)
62 #
63 #
64 # Define dependencies on specfs, rpcmod, and rpcsec
65 #
66 LDFLAGS += -dy -N fs/specfs -N strmod/rpcmod -N misc/rpcsec
67 #
68 #
69 # For now, disable these lint checks; maintainers should endeavor
70 # to investigate and remove these for maximum lint coverage.
71 # Please do not carry these forward to new Makefiles.
72 #
73 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
74 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
75 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
76 LINTTAGS += -erroff=E_STATIC_UNUSED
77 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
78 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
79 #
80 CERRWARN += -_gcc=-Wno-parentheses
81 CERRWARN += -_gcc=-Wno-unused-label
82 CERRWARN += -_gcc=-Wno-type-limits
83 CERRWARN += -_gcc=-Wno-uninitialized
84 CERRWARN += -_gcc=-Wno-switch
85 CERRWARN += -_gcc=-Wno-unused-function
86 CERRWARN += -_gcc=-Wno-unused-variable
87 CERRWARN += -_gcc=-Wno-empty-body
88 #
89 # needs work
90 SMATCH=off
91 #
92 #
93 # Default build targets.
94 #
95 .KEEP_STATE:
96 #
97 def: $(DEF_DEPS)
98 #
99 all: $(ALL_DEPS)
100 #
101 clean: $(CLEAN_DEPS)
102 #
103 clobber: $(CLOBBER_DEPS)
104 #
105 lint: $(LINT_DEPS)
106 #
107 modlintlib: $(MODLINTLIB_DEPS)
108 #
109 clean.lint: $(CLEAN_LINT_DEPS)
110 #
111 install: $(INSTALL_DEPS)
112 #
113 $(ROOTLINK): $(ROOT_SYS_DIR) $(ROOTMODULE)
114 -$(RM) $@; ln $(ROOTMODULE) $@
115 #
116 #
117 # Include common targets.
118 #
119 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/nfs\_dlboot/Makefile

1

```
*****
2422 Mon Jan 7 13:09:33 2019
new/usr/src/uts/intel/nfs_dlboot/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/nfs_dlboot/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
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26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the dlboot_proto
31 # kernel module.
32 #
33 # intel architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTSBASE = ../../
40 COM_DIR = $(SRC)/common/net/dhcp

42 #
43 # Define the module and object file sets.
44 #
45 MODULE = nfs_dlboot
46 OBJECTS = $(DLBOOT_OBJS:%=$(OBJS_DIR)/%)
47 LINTS = $(DLBOOT_OBJS:%.o=$(LINTS_DIR)/%.ln)
48 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)

50 #
51 # Include common rules.
52 #
53 include $(UTSBASE)/intel/Makefile.intel

55 #
56 # Define targets
57 #
58 ALL_TARGET = $(BINARY)
59 LINT_TARGET = $(MODULE).lint
60 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
```

new/usr/src/uts/intel/nfs\_dlboot/Makefile

2

```
62 #
63 # Overrides.
64 #
65 CPPFLAGS += -DDHCP_CLIENT -I$(COM_DIR)
66 LDFLAGS += -dy -Nmisc/strplumb -Nstrmod/rpcmod

68 #
69 # For now, disable these lint checks; maintainers should endeavor
70 # to investigate and remove these for maximum lint coverage.
71 # Please do not carry these forward to new Makefiles.
72 #
73 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
74 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
75 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

77 CERRWARN += -_gcc=-Wno-parentheses

79 # needs work
80 $(OBJS_DIR)/nfs_dlinet.o := SMOFF += indenting,all_func_returns

82 #
83 # Default build targets.
84 #
85 .KEEP_STATE:

87 def: $(DEF_DEPS)

89 all: $(ALL_DEPS)

91 clean: $(CLEAN_DEPS)

93 clobber: $(CLOBBER_DEPS)

95 lint: $(LINT_DEPS)

97 modlintlib: $(MODLINTLIB_DEPS)

99 clean.lint: $(CLEAN_LINT_DEPS)

101 install: $(INSTALL_DEPS)

103 #
104 # Include common targets.
105 #
106 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2647 Mon Jan 7 13:09:33 2019
new/usr/src/uts/intel/nfssrv/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/nfssrv/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
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26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the nfs server-specific
31 # kernel module.
32 #
33 # intel implementation architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTBASE = ../..

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = nfssrv
45 OBJECTS = $(NFSSRV_OBJS:%=$(OBJDIR)/%)
46 LINTS = $(NFSSRV_OBJS:%.o=$(LINTS_DIR)/%.ln)
47 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)

49 #
50 # Include common rules.
51 #
52 include $(UTBASE)/intel/Makefile.intel

54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

61 #

```

```

62 # Overrides.
63 #
64 LDFLAGS += -dy -Nstrmod/rpcmod -Nfs/nfs -Nmisc/rpcsec
65 LDFLAGS += -Nmisc/klmmod

67 #
68 # For now, disable these lint checks; maintainers should endeavor
69 # to investigate and remove these for maximum lint coverage.
70 # Please do not carry these forward to new Makefiles.
71 #
72 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
73 LINTTAGS += -erroff=E_STATIC_UNUSED
74 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
75 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
76 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON

78 CERRWARN += -_gcc=-Wno-parentheses
79 CERRWARN += -_gcc=-Wno-type-limits
80 CERRWARN += -_gcc=-Wno-unused-variable
81 CERRWARN += -_gcc=-Wno-unused-label
82 CERRWARN += -_gcc=-Wno-unused-function
83 CERRWARN += -_gcc=-Wno-uninitialized
84 CERRWARN += -_gcc=-Wno-switch

86 # needs work
87 SMATCH=off

89 #
90 # Default build targets.
91 #
92 .KEEP_STATE:

94 def: $(DEF_DEPS)

96 all: $(ALL_DEPS)

98 clean: $(CLEAN_DEPS)

100 clobber: $(CLOBBER_DEPS)

102 lint: $(LINT_DEPS)

104 modlintlib: $(MODLINTLIB_DEPS)

106 clean.lint: $(CLEAN_LINT_DEPS)

108 install: $(INSTALL_DEPS)

110 #
111 # Include common targets.
112 #
113 include $(UTBASE)/intel/Makefile.targ

```

```

*****
2254 Mon Jan 7 13:09:33 2019
new/usr/src/uts/intel/nge/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
26 # This makefile drives the production of the nvidia toe/ge
27 # driver.
28 #
29 # Path to the base of the uts directory tree (usually /usr/src/uts).
30 UTSBASE = ../../
31 #
32 #
33 # Define the module and object file sets.
34 #
35 MODULE = nge
36 OBJECTS = $(NGE_OBJS:%=$(OBJS_DIR)/%)
37 LINTS = $(NGE_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
39 CONF_SRCDIR = $(UTSBASE)/common/io/nge
40 #
41 #
42 # Include common rules.
43 #
44 include $(UTSBASE)/intel/Makefile.intel
45 #
46 #
47 # Define targets
48 #
49 ALL_TARGET = $(BINARY) $(SRC_CONFILE)
50 LINT_TARGET = $(MODULE).lint
51 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
52 #
53 #
54 # Override defaults
55 #
56 INC_PATH += -I$(CONF_SRCDIR)
57 #
58 # CFLAGS += $(CINLINEFLAGS)

```

```

60 #
61 # Driver depends on GLD
62 #
63 LDFLAGS += -dy -N misc/mac
64 #
65 #
66 # For now, disable these lint checks; maintainers should endeavor
67 # to investigate and remove these for maximum lint coverage.
68 # Please do not carry these forward to new Makefiles.
69 #
70 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
71 #
72 CERRWARN += -_gcc=-Wno-switch
73 CERRWARN += -_gcc=-Wno-uninitialized
74 #
75 # needs work
76 SMOFF += all_func_returns
77 #
78 #
79 # Default build targets.
80 #
81 .KEEP_STATE:
82 #
83 def: $(DEF_DEPS)
84 #
85 all: $(ALL_DEPS)
86 #
87 clean: $(CLEAN_DEPS)
88 #
89 clobber: $(CLOBBER_DEPS)
90 #
91 lint: $(LINT_DEPS)
92 #
93 modlintlib: $(MODLINTLIB_DEPS)
94 #
95 clean.lint: $(CLEAN_LINT_DEPS)
96 #
97 install: $(INSTALL_DEPS)
98 #
99 #
100 # Include common targets.
101 #
102 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
3640 Mon Jan 7 13:09:33 2019
new/usr/src/uts/intel/nsmb/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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9 # or http://www.opensolaris.org/os/licensing.
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11 # and limitations under the License.
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14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/nsmb/Makefile
23 #
24 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # intel architecture dependent
31 #

33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../

38 #
39 # Define the module and object file sets.
40 #
41 MODULE = nsmb
42 OBJECTS = $(NSMB_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(NSMB_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(USR_DRV_DIR)/$(MODULE)
45 CONF_SRCDIR = $(UTSBASE)/common/fs/smbclnt/netnsmb
46 OFFSETS_SRC = $(CONF_SRCDIR)/offsets.in
47 IOC_CHECK_H = $(OBJS_DIR)/ioc_check.h

49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 #
55 # Define targets
56 #
57 ALL_TARGET = $(ALL_TARGET_$(OBJS_DIR))
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(INSTALL_TARGET_$(OBJS_DIR))

61 #

```

```

62 # Overrides.
63 #
64 # We need some unusual overrides here so we'll
65 # build ioc_check.h for both 32-bit/64-bit,
66 # but only build 64-bit binaries.
67 #

69 # Build 32-bit also...
70 DEF_BUILDS = $(DEF_BUILDS64) $(DEF_BUILDS32)
71 ALL_BUILDS = $(ALL_BUILDS64) $(ALL_BUILDS32)
72 # ... but don't build any 32-bit objects
73 ALL_TARGET_debug32 = $(IOC_CHECK_H)
74 ALL_TARGET_debug64 = $(BINARY) $(SRC_CONFILE)
75 ALL_TARGET_obj32 = $(IOC_CHECK_H)
76 ALL_TARGET_obj64 = $(BINARY) $(SRC_CONFILE)
77 # ... and remove -xcg92 (not supported by cw)
78 CFLAGS_32=
79 # ... same deal for install targets
80 INSTALL_TARGET_debug32 = $(IOC_CHECK_H)
81 INSTALL_TARGET_debug64 = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
82 INSTALL_TARGET_obj32 = $(IOC_CHECK_H)
83 INSTALL_TARGET_obj64 = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

85 #
86 # Now the normal overrides...
87 #
88 MODSTUBS_DIR = $(OBJS_DIR)
89 $(MODSTUBS_O) := AS_CPPFLAGS += -DNSMB_MODULE
90 CLEANFILES += $(MODSTUBS_O) $(IOC_CHECK_H)
91 CERRWARN += -erroff=E_STATEMENT_NOT_REACHED
92 INC_PATH += -I$(UTSBASE)/common/fs/smbclnt
93 LDFLAGS += -dy -Ncrypto/md4 -Ncrypto/md5 -Nmisc/tlimod

95 # Until CR 4994570 is fixed..
96 LINTTAGS += -erroff=E_BAD_FORMAT_ARG_TYPE2

98 # The mb_put/md_get functions are intentionally used with and without
99 # return value checks, so filter those out like LGREP.2 does.
100 LGREP.nsmc = egrep -v ' ignored: (mb_init|mb_put|md_get)'
101 LTAIL += 2>&1 | $(LGREP.2) | $(LGREP.nsmc) || true

103 # same for smatch
104 SMOFF += all_func_returns

106 # needs work
107 SMOFF += signed,deref_check

109 #
110 # Default build targets.
111 #
112 .KEEP_STATE:

114 def: $(DEF_DEPS)

116 all: $(ALL_DEPS)

118 clean: $(CLEAN_DEPS)

120 clobber: $(CLOBBER_DEPS)

122 lint: $(LINT_DEPS)

124 modlintlib: $(MODLINTLIB_DEPS)

126 clean.lint: $(CLEAN_LINT_DEPS)

```

**new/usr/src/uts/intel/nsmb/Makefile**

3

```
128 install:          $(INSTALL_DEPS)

130 #
131 # Create ioc_check.h and compare with the saved
132 # ioc_check.ref to ensure 32/64-bit invariance.
133 #
134 $(OBJECTS) : $(IOC_CHECK_H)
135 $(IOC_CHECK_H): $(OFFSETS_SRC)
136     $(OFFSETS_CREATE) <$(OFFSETS_SRC) >$.tmp
137     cmp -s ioc_check.ref $.tmp && \
138         mv -f $.tmp $@

140 #
141 #     Include common targets.
142 #
143 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2157 Mon Jan 7 13:09:34 2019
new/usr/src/uts/intel/ntxn/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the Neterion Xframe
28 # 10G Ethernet (XGE) driver module in x86 systems
29 #
30 #
31 # Paths to the base of the uts directory trees
32 #
33 UTSBASE = ../../
34 #
35 # Define the module and object file sets.
36 #
37 MODULE = ntxn
38 OBJECTS = $(NTXN_OBJS:%=$(OBJDIR)/%)
39 LINTS = $(NTXN_OBJS:%.o=$(LINTSDIR)/%.ln)
40 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
41 CONF_SRCDIR = $(UTSBASE)/common/io/ntxn
42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel
46 #
47 # Define targets
48 #
49 #
50 ALL_TARGET = $(BINARY) $(SRC_CONF_FILE)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONF_FILE)
53 #
54 INC_PATH += -I$(CONF_SRCDIR)
55 #
56 CFLAGS += -DSOLARIS11 -xO4
57 CFLAGS64 += -DSOLARIS11 -xO -I./

```

```

59 #
60 # Driver depends on MAC & IP
61 #
62 LDFLAGS += -dy -N misc/mac -N drv/ip
63 #
64 # Lint flag
65 #
66 LINTFLAGS += $(NTXN_DFLAGS) $(NTXN_KFLAGS)
67 LINTFLAGS += -DSOLARIS11
68 #
69 CERRWARN += -_gcc=-Wno-parentheses
70 CERRWARN += -_gcc=-Wno-unused-label
71 CERRWARN += -_gcc=-Wno-type-limits
72 #
73 # needs work
74 SMOFF += all_func_returns
75 #
76 #
77 #
78 # Default build targets.
79 #
80 .KEEP_STATE:
81 #
82 def: $(DEF_DEPS)
83 #
84 all: $(ALL_DEPS)
85 #
86 clean: $(CLEAN_DEPS)
87 #
88 clobber: $(CLOBBER_DEPS)
89 #
90 lint: $(LINT_DEPS)
91 #
92 modlintlib: $(MODLINTLIB_DEPS)
93 #
94 clean.lint: $(CLEAN_LINT_DEPS)
95 #
96 install: $(INSTALL_DEPS)
97 #
98 #
99 # Include common targets.
100 #
101 include $(UTSBASE)/intel/Makefile.targ

```



new/usr/src/uts/intel/nv\_sata/Makefile

1

```
*****
2329 Mon Jan 7 13:09:34 2019
new/usr/src/uts/intel/nv_sata/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
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20 #
21 #
22 #
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 #
28 #
29 # Path to the base of the uts directory tree (usually /usr/src/uts).
30 #
31 UTSBASE = ../../
32 #
33 #
34 # Define the module and object file sets.
35 #
36 MODULE = nv_sata
37 OBJECTS = $(NV_SATA_OBJS:%=$(OBJS_DIR)/%)
38 LINTS = $(NV_SATA_OBJS:%.o=$(LINTS_DIR)/%.ln)
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
40 CONF_SRCDIR = $(UTSBASE)/common/io/sata/adapters/nv_sata
41 #
42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel
46 #
47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
53 #
54 #
55 # Overrides.
56 #
57 DEBUG_FLGS =
58 DEBUG_DEFS += $(DEBUG_FLGS)
59 CPPFLAGS += -DSGPIO_SUPPORT
60 #
61 #
```

new/usr/src/uts/intel/nv\_sata/Makefile

2

```
62 # lint pass one enforcement
63 #
64 CFLAGS += $(CCVERBOSE)
65 #
66 #
67 # dependency on sata module
68 #
69 LDFLAGS += -dy -N misc/sata
70 #
71 #
72 # For now, disable these lint checks; maintainers should endeavor
73 # to investigate and remove these for maximum lint coverage.
74 # Please do not carry these forward to new Makefiles.
75 #
76 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
77 #
78 CERRWARN += -_gcc=-Wno-uninitialized
79 #
80 # needs work
81 $(OBJS_DIR)/nv_sata.o := SMOFF += deref_check
82 #
83 #
84 # Default build targets.
85 #
86 .KEEP_STATE:
87 #
88 def: $(DEF_DEPS)
89 #
90 all: $(ALL_DEPS)
91 #
92 clean: $(CLEAN_DEPS)
93 #
94 clobber: $(CLOBBER_DEPS)
95 #
96 lint: $(LINT_DEPS)
97 #
98 modlintlib: $(MODLINTLIB_DEPS)
99 #
100 clean.lint: $(CLEAN_LINT_DEPS)
101 #
102 install: $(INSTALL_DEPS)
103 #
104 #
105 # Include common targets.
106 #
107 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2842 Mon Jan 7 13:09:34 2019
new/usr/src/uts/intel/nxge/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 # uts/intel/nxge/Makefile
22 #
23 # Copyright (c) 2007, 2010, Oracle and/or its affiliates. All rights reserved.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

26 #
27 # This makefile drives the production of the Sun NIU
28 # 10G/1G Ethernet leaf driver kernel module.
29 #
30 # Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 UTSBASE = ../..

32 #
33 # Define the module and object file sets.
34 #
35 MODULE = nxge
36 NXGE_OBJECTS = $(NXGE_OBJS) $(NXGE_NPI_OBJS)
37 OBJECTS = $(NXGE_OBJECTS:%=$(OBJS_DIR)/%)
38 LINTS = $(NXGE_OBJECTS:%.o=$(LINTS_DIR)/%.ln)
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
40 CONF_SRCDIR = $(UTSBASE)/common/io/nxge

42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel

47 #
48 # Override defaults to build a unique, local modstubs.o.
49 #
50 MODSTUBS_DIR = $(OBJS_DIR)

52 CLEANFILES += $(MODSTUBS_O)

54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)

```

```

58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

61 #
62 # Include nxge specific header files
63 #
64 INC_PATH += -I$(UTSBASE)/common
65 INC_PATH += -I$(UTSBASE)/common/io/nxge/npi
66 INC_PATH += -I$(UTSBASE)/common/sys/nxge
67 INC_PATH += -I$(UTSBASE)/sun4v
68 #
69 #
70 # lint pass one enforcement
71 #
72 CFLAGS += -DSOLARIS
73 #
74 #ALL_BUILDS = $(ALL_BUILDS64)
75 #DEF_BUILDS = $(DEF_BUILDS64)
76 #CLEANLINTFILES += $(LINT64_FILES)
77 #
78 LINTFLAGS += -DSOLARIS
79 #
80 # STREAMS, DDI API limitations and other ON header file definitions such as ethe
81 # force us to turn off these lint checks.
82 #
83 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
84 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
85 LINTTAGS += -erroff=E_FALSE_LOGICAL_EXPR

87 CERRWARN += -_gcc=-Wno-unused-label
88 CERRWARN += -_gcc=-Wno-parentheses
89 CERRWARN += -_gcc=-Wno-switch
90 CERRWARN += -_gcc=-Wno-uninitialized
91 CERRWARN += -_gcc=-Wno-type-limits

93 # needs work
94 SMATCH=off

96 #
97 # Driver depends on mac & IP
98 #
99 LDFLAGS += -dy -N misc/mac -N drv/ip

101 #
102 # Default build targets.
103 #
104 .KEEP_STATE:

106 def: $(DEF_DEPS)

108 all: $(ALL_DEPS)

110 clean: $(CLEAN_DEPS)

112 clobber: $(CLOBBER_DEPS)

114 lint: $(LINT_DEPS)

116 modlintlib: $(MODLINTLIB_DEPS)

118 clean.lint: $(CLEAN_LINT_DEPS)

120 install: $(INSTALL_DEPS)

122 #
123 # Include common targets.

```

**new/usr/src/uts/intel/nxge/Makefile**

**3**

```
124 #  
125 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/objfs/Makefile

1

```
*****
2338 Mon Jan 7 13:09:34 2019
new/usr/src/uts/intel/objfs/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
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14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # This makefile drives the production of the object file system
29 # kernel module.
30 #
31 # intel implementation architecture dependent
32 #

34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../../

39 #
40 # Define the module and object file sets.
41 #
42 MODULE = objfs
43 OBJECTS = $(OBJFS_OBJS:%=$(OBJFS_DIR)/%)
44 LINTS = $(OBJFS_OBJS:%.o=$(LINTS_DIR)/%.ln)
45 ROOTMODULE = $(ROOT_FS_DIR)/$(MODULE)

47 #
48 # Include common rules.
49 #
50 include $(UTSBASE)/intel/Makefile.intel

52 #
53 # Define targets
54 #
55 ALL_TARGET = $(BINARY)
56 LINT_TARGET = $(MODULE).lint
57 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

59 #
60 # Overrides.
61 #
```

new/usr/src/uts/intel/objfs/Makefile

2

```
62 CFLAGS += $(CCVERBOSE)

64 #
65 # For now, disable these lint checks; maintainers should endeavor
66 # to investigate and remove these for maximum lint coverage.
67 # Please do not carry these forward to new Makefiles.
68 #
69 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
70 LINTTAGS += -erroff=E_STATIC_UNUSED

72 CERRWARN += -_gcc=-Wno-parentheses
73 CERRWARN += -_gcc=-Wno-unused-function
74 CERRWARN += -_gcc=-Wno-uninitialized

76 # needs work
77 $(OBJS_DIR)/objfs_vfs.o := SMOFF += signed_integer_overflow_check

79 #
80 # Default build targets.
81 #
82 .KEEP_STATE:

84 def: $(DEF_DEPS)

86 all: $(ALL_DEPS)

88 clean: $(CLEAN_DEPS)

90 clobber: $(CLOBBER_DEPS)

92 lint: $(LINT_DEPS)

94 modlintlib: $(MODLINTLIB_DEPS)

96 clean.lint: $(CLEAN_LINT_DEPS)

98 install: $(INSTALL_DEPS)

100 #
101 # Include common targets.
102 #
103 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/openeepr/Makefile

1

```
*****
2346 Mon Jan 7 13:09:34 2019
new/usr/src/uts/intel/openeepr/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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9 # or http://www.opensolaris.org/os/licensing.
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11 # and limitations under the License.
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14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/openeepr/Makefile
23 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.

28 #
29 # This makefile drives the production of the openeepr driver
30 #
31 # intel implementation architecture independent
32 #

34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../../

39 #
40 # Define the module and object file sets.
41 #
42 MODULE = openeepr
43 OBJECTS = ${OPENEPR_OBJS:%=${OBJS_DIR}/%}
44 LINTS = ${OPENEPR_OBJS:%.o=${LINTS_DIR}/%.ln}
45 ROOTMODULE = ${ROOT_DRV_DIR}/${MODULE}
46 CONF_SRCDIR = ${UTSBASE}/common/io

48 #
49 # Include common rules.
50 #
51 include ${UTSBASE}/intel/Makefile.intel

53 #
54 # Define targets
55 #
56 ALL_TARGET = ${BINARY} ${SRC_CONFFILE}
57 LINT_TARGET = ${MODULE}.lint
58 INSTALL_TARGET = ${BINARY} ${ROOTMODULE} ${ROOT_CONFFILE}

60 LDFLAGS += -dy -Ndacf/consconfig_dacf
```

new/usr/src/uts/intel/openeepr/Makefile

2

```
62 #
63 # For now, disable these lint checks; maintainers should endeavor
64 # to investigate and remove these for maximum lint coverage.
65 # Please do not carry these forward to new Makefiles.
66 #
67 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
68 #
69 CERRWARN += -_gcc=-Wno-parentheses
70 CERRWARN += -_gcc=-Wno-uninitialized

72 # needs work
73 ${OBJS_DIR}/openprom.o := SMOFF += cast_assign,strcpy_overflow

75 #
76 # Default build targets.
77 #
78 .KEEP_STATE:

80 def: $(DEF_DEPS)

82 all: $(ALL_DEPS)

84 clean: $(CLEAN_DEPS)

86 clobber: $(CLOBBER_DEPS)

88 lint: $(LINT_DEPS)

90 modlintlib: $(MODLINTLIB_DEPS)

92 clean.lint: $(CLEAN_LINT_DEPS)

94 install: $(INSTALL_DEPS)

96 #
97 # Include common targets.
98 #
99 include ${UTSBASE}/intel/Makefile.targ
```

```

*****
2493 Mon Jan 7 13:09:34 2019
new/usr/src/uts/intel/pcfs/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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11 # and limitations under the License.
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/pcfs/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the DOS file system
31 # kernel module.
32 #
33 # intel architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTBASE = ../..

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = pcfs
45 OBJECTS = $(PCFS_OBJS:%=$(OBJS_DIR)/%)
46 LINTS = $(PCFS_OBJS:%.o=$(LINTS_DIR)/%.ln)
47 ROOTMODULE = $(USR_FS_DIR)/$(MODULE)

49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 #
55 # For now, disable these lint checks; maintainers should endeavor
56 # to investigate and remove these for maximum lint coverage.
57 # Please do not carry these forward to new Makefiles.
58 #
59 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
60 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
61 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED

```

```

62 LINTTAGS += -erroff=E_STATIC_UNUSED
63 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
64 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

66 CERRWARN += -_gcc=-Wno-parentheses
67 CERRWARN += -_gcc=-Wno-type-limits
68 CERRWARN += -_gcc=-Wno-unused-label
69 CERRWARN += -_gcc=-Wno-uninitialized
70 CERRWARN += -_gcc=-Wno-unused-function

72 # needs work
73 SMATCH=off

75 #
76 # Define targets
77 #
78 ALL_TARGET = $(BINARY)
79 LINT_TARGET = $(MODULE).lint
80 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

82 #
83 # Default build targets.
84 #
85 .KEEP_STATE:

87 def: $(DEF_DEPS)

89 all: $(ALL_DEPS)

91 clean: $(CLEAN_DEPS)

93 clobber: $(CLOBBER_DEPS)

95 lint: $(LINT_DEPS)

97 modlintlib: $(MODLINTLIB_DEPS)

99 clean.lint: $(CLEAN_LINT_DEPS)

101 install: $(INSTALL_DEPS)

103 #
104 # Include common targets.
105 #
106 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2545 Mon Jan 7 13:09:35 2019
new/usr/src/uts/intel/pcic/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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11 # and limitations under the License.
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 #
28 #
29 # This makefile drives the PCIC style PCMCIA adapter
30 # It is mostly a standard driver
31 #
32 # intel architecture dependent
33 #
34 #
35 #
36 # Paths to the base of the uts directory trees
37 #
38 UTSBASE = ../../
39 #
40 #
41 # Define the module and object file sets.
42 #
43 MODULE = pcic
44 OBJECTS = $(PCIC_OBJS:%=$(OBJS_DIR)/%)
45 LINTS = $(PCIC_OBJS:%.o=$(LINTS_DIR)/%.ln)
46 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
47 CONF_SRCDIR = $(UTSBASE)/common/io
48 #
49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel
53 #
54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY) $(SRC_CONFILE)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFILE)
60 #
61 CPPFLAGS += -DCARDBUS -DHOTPLUG

```

```

62 #
63 # Dependency
64 #
65 LDFLAGS += -dy -Nmisc/busra -Nmisc/pcmcia -Nmisc/cardbus
66 #
67 #
68 # For now, disable these lint checks; maintainers should endeavor
69 # to investigate and remove these for maximum lint coverage.
70 # Please do not carry these forward to new Makefiles.
71 #
72 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
73 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
74 LINTTAGS += -erroff=E_STATIC_UNUSED
75 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
76 #
77 CERRWARN += -_gcc=-Wno-parentheses
78 CERRWARN += -_gcc=-Wno-unused-variable
79 CERRWARN += -_gcc=-Wno-unused-function
80 CERRWARN += -_gcc=-Wno-uninitialized
81 #
82 # needs work
83 SMOFF += no_if_block,indenting,all_func_returns
84 #
85 #
86 # Default build targets.
87 #
88 .KEEP_STATE:
89 #
90 def: $(DEF_DEPS)
91 #
92 all: $(ALL_DEPS)
93 #
94 clean: $(CLEAN_DEPS)
95 #
96 clobber: $(CLOBBER_DEPS)
97 #
98 lint: $(LINT_DEPS)
99 #
100 modlintlib: $(MODLINTLIB_DEPS)
101 #
102 clean.lint: $(CLEAN_LINT_DEPS)
103 #
104 install: $(INSTALL_DEPS)
105 #
106 #
107 # Include common targets.
108 #
109 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2444 Mon Jan 7 13:09:35 2019
new/usr/src/uts/intel/pcicfg/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
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20 #
21 #
22 # uts/intel/pcicfg/Makefile
23 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the kernel/misc/pcicfg module
28 # (PCI configurator module) for PCI hotplug support in PCI nexus
29 # drivers.
30 # intel implementation architecture dependent
31 #

28 #
29 # Path to the base of the uts directory tree (usually /usr/src/uts).
30 #
31 UTSBASE = ../..

33 #
34 # Define the module and object file sets.
35 #
36 MODULE = pcicfg
37 OBJECTS = ${PCICFG_OBJS:%=${OBJSDIR}/%}
38 LINTS = ${PCICFG_OBJS:%.o=${LINTSDIR}/%.ln}
39 ROOTMODULE = ${ROOT_MISC_DIR}/${MODULE}

41 #
42 # Include common rules.
43 #
44 include ${UTSBASE}/intel/Makefile.intel

46 #
47 # Define targets
48 #
49 ALL_TARGET = ${BINARY}
50 LINT_TARGET = ${MODULE}.lint
51 INSTALL_TARGET = ${BINARY} ${ROOTMODULE}

53 #
54 # Override defaults to build a unique, local modstubs.o.
55 #

```

```

56 MODSTUBS_DIR = ${OBJSDIR}
57 ${MODSTUBS_O} := AS_CPPFLAGS += -DPCICFG_MODULE
58 CLEANFILES += ${MODSTUBS_O}

60 #
61 # Dependency
62 #
63 LDPLAGS += -dy -Nmisc/busra -Nmisc/pcie

65 #
66 # For now, disable these lint checks; maintainers should endeavor
67 # to investigate and remove these for maximum lint coverage.
68 # Please do not carry these forward to new Makefiles.
69 #
70 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
71 LINTTAGS += -erroff=E_STATIC_UNUSED
72 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
73 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

75 CERRWARN += -_gcc=-Wno-unused-function
76 CERRWARN += -_gcc=-Wno-uninitialized

78 # needs work
79 ${OBJSDIR}/pcicfg.o := SMOFF += deref_check

81 #
82 # Default build targets.
83 #
84 .KEEP_STATE:

86 def: ${DEF_DEPS}

88 all: ${ALL_DEPS}

90 clean: ${CLEAN_DEPS}

92 clobber: ${CLOBBER_DEPS}

94 lint: ${LINT_DEPS}

96 modlintlib: ${MODLINTLIB_DEPS}

98 clean.lint: ${CLEAN_LINT_DEPS}

100 install: ${INSTALL_DEPS}

102 #
103 # Include common targets.
104 #
105 include ${UTSBASE}/intel/Makefile.targ

```



```

*****
2413 Mon Jan 7 13:09:35 2019
new/usr/src/uts/intel/pcieb/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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20 #
21 #
22 # uts/intel/pcieb/Makefile
23 #
24 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the pcieb driver kernel
28 # module.
29 #
29 #
30 # Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 UTSBASE = ../..

34 #
35 # Define the module and object file sets.
36 #
37 MODULE = pcieb
38 OBJECTS = $(PCIEB_OBJS:%=$(OBJS_DIR)/%)
39 LINTS = $(PCIEB_OBJS:%.o=$(LINTS_DIR)/%.ln)
40 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
41 CONF_SRCDIR = $(UTSBASE)/common/io/pciex

43 #
44 # Include common rules.
45 #
46 include $(UTSBASE)/intel/Makefile.intel

48 #
49 # Define targets
50 #
51 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
52 LINT_TARGET = $(MODULE).lint
53 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

55 #
56 # depends on misc/pcie
57 #
58 LDFLAGS += -dy -Nmisc/pcie

```

```

60 #
61 # Override defaults to build a unique, local modstubs.o.
62 #
63 MODSTUBS_DIR = $(OBJS_DIR)
64 CLEANFILES += $(MODSTUBS_O)

66 #
67 # For now, disable these lint checks; maintainers should endeavor
68 # to investigate and remove these for maximum lint coverage.
69 # Please do not carry these forward to new Makefiles.
70 #
71 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
72 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
73 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
74 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

76 CERRWARN += -_gcc=-Who-unused-value

78 # needs work
79 SMOFF += all_func_returns

81 #
82 # Default build targets.
83 #
84 .KEEP_STATE:

86 def: $(DEF_DEPS)

88 all: $(ALL_DEPS)

90 clean: $(CLEAN_DEPS)

92 clobber: $(CLOBBER_DEPS)

94 lint: $(LINT_DEPS)

96 modlintlib: $(MODLINTLIB_DEPS)

98 clean.lint: $(CLEAN_LINT_DEPS)

100 install: $(INSTALL_DEPS)

102 #
103 # Include common targets.
104 #
105 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/pcihp/Makefile

1

```
*****
2111 Mon Jan 7 13:09:35 2019
new/usr/src/uts/intel/pcihp/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/pcihp/Makefile
23 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
26 #ident "%Z%M% %I% %E% SMI"
27 #
28 # This makefile drives the production of the kernel/misc/pcihp module
29 # for PCI hotplug support in PCI nexus drivers.
30 #
31 # intel implementation architecture dependent
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../..
37 #
38 # Define the module and object file sets.
39 #
40 #
41 MODULE = pcihp
42 OBJECTS = $(PCIHPNEXUS_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(PCIHPNEXUS_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
45 #
46 # Include common rules.
47 #
48 include $(UTSBASE)/intel/Makefile.intel
49 #
50 # Define targets
51 #
52 ALL_TARGET = $(BINARY)
53 LINT_TARGET = $(MODULE).lint
54 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
55 #
56 # Dependency
```

new/usr/src/uts/intel/pcihp/Makefile

2

```
55 #
56 LDFLAGS += -dy -Nmisc/busra -Nmisc/hpcsvc
57 #
58 #
59 # For now, disable these lint checks; maintainers should endeavor
60 # to investigate and remove these for maximum lint coverage.
61 # Please do not carry these forward to new Makefiles.
62 #
63 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
64 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
65 #
66 # needs work
67 SMOFF += indenting
68 #
69 # Default build targets.
70 #
71 #
72 .KEEP_STATE:
73 #
74 def: $(DEF_DEPS)
75 #
76 all: $(ALL_DEPS)
77 #
78 clean: $(CLEAN_DEPS)
79 #
80 clobber: $(CLOBBER_DEPS)
81 #
82 lint: $(LINT_DEPS)
83 #
84 modlintlib: $(MODLINTLIB_DEPS)
85 #
86 clean.lint: $(CLEAN_LINT_DEPS)
87 #
88 install: $(INSTALL_DEPS)
89 #
90 # Include common targets.
91 #
92 #
93 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/pcmcia/Makefile

1

```
*****
2515 Mon Jan 7 13:09:35 2019
new/usr/src/uts/intel/pcmcia/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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20 #
21 #
22 # uts/intel/pcmcia/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This file makes the pcmcia nexus for an intel system
31 #
32 # intel architecture dependent
33 #

35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../
39 #
40 # Define the module and object file sets.
41 #
42 MODULE = pcmcia
43 OBJECTS = $(PCMCIA_OBJS:%=$(OBJS_DIR)/%)
44 LINTS = $(PCMCIA_OBJS:%.o=$(LINTS_DIR)/%.ln)
45 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)

47 #
48 # Include common rules.
49 #
50 include $(UTSBASE)/intel/Makefile.intel

52 #
53 # Define targets
54 #
55 ALL_TARGET = $(BINARY)
56 LINT_TARGET = $(MODULE).lint
57 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

59 #
60 # Override defaults to build a unique, local modstubs.o.
61 #
```

new/usr/src/uts/intel/pcmcia/Makefile

2

```
62 MODSTUBS_DIR = $(OBJS_DIR)
63 $(MODSTUBS_O) := AS_CPPFLAGS += -DPCMCIA_MODULE
64 CLEANFILES += $(MODSTUBS_O)

66 #
67 # Include i86pc specific header files
68 #
69 INC_PATH += -I$(UTSBASE)/i86pc

71 #
72 # dependency
73 #
74 LDFLAGS += -dy -Nmisc/busra -Nmisc/pci_autoconfig

76 #
77 # For now, disable these lint checks; maintainers should endeavor
78 # to investigate and remove these for maximum lint coverage.
79 # Please do not carry these forward to new Makefiles.
80 #
81 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
82 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

84 CERRWARN += -_gcc=-Wno-uninitialized
85 CERRWARN += -_gcc=-Wno-parentheses

87 # needs work
88 SMATCH=off

90 #
91 # Default build targets.
92 #
93 .KEEP_STATE:

95 def: $(DEF_DEPS)

97 all: $(ALL_DEPS)

99 clean: $(CLEAN_DEPS)

101 clobber: $(CLOBBER_DEPS)

103 lint: $(LINT_DEPS)

105 modlintlib: $(MODLINTLIB_DEPS)

107 clean.lint: $(CLEAN_LINT_DEPS)

109 install: $(INSTALL_DEPS)

111 #
112 # Include common targets.
113 #
114 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/pmcs/Makefile

1

```
*****
2287 Mon Jan 7 13:09:35 2019
new/usr/src/uts/intel/pmcs/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 # CDDL HEADER START
2 #
3 # The contents of this file are subject to the terms of the
4 # Common Development and Distribution License (the "License").
5 # You may not use this file except in compliance with the License.
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9 # See the License for the specific language governing permissions
10 # and limitations under the License.
11 #
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13 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
14 # If applicable, add the following below this CDDL HEADER, with the
15 # fields enclosed by brackets "[]" replaced with your own identifying
16 # information: Portions Copyright [yyyy] [name of copyright owner]
17 #
18 # CDDL HEADER END
19 #
20 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
21 # Use is subject to license terms.
22 #
23 # Copyright (c) 2018, Joyent, Inc.
24 # This makefile drives the production of the pmcs driver kernel module.
25 #
26 # intel architecture dependent
27 #
28 #
29 #
30 #
31 # Paths to the base of the uts directory trees
32 #
33 UTSBASE = ../../../../src/uts
34 #
35 # Define the module and object file sets.
36 #
37 MODULE = pmcs
38 OBJECTS = $(PMCS_OBJS:%=$(OBJDIR)/%)
39 LINTS = $(PMCS_OBJS:%.o=$(LINTSDIR)/%.ln)
40 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
41 CONF_SRCDIR = $(UTSBASE)/common/io/scsi/adapters/pmcs
42 #
43 # Kernel Module Dependencies
44 #
45 LDFLAGS += -dy -Nmisc/scsi
46 #
47 # Define targets
48 #
49 ALL_TARGET = $(BINARY) $(CONFMOD)
50 LINT_TARGET = $(MODULE).lint
51 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
52 #
53 # Include common rules.
54 include $(UTSBASE)/intel/Makefile.intel
55 include $(CONF_SRCDIR)/pmcs8001fw.version
```

new/usr/src/uts/intel/pmcs/Makefile

2

```
57 #
58 # Add additional flags
59 #
60 PMCS_DRV_FLGS = -DMODNAME=\"${MODULE}\"
61 CPPFLAGS += $(PMCS_DRV_FLGS) \
62 -DPMCS_FIRMWARE_VERSION=${PMCS_FW_VERSION} \
63 -DPMCS_FIRMWARE_VERSION_STRING=\"${PMCS_FW_VERSION_STRING}\"
64 #
65 CERRWARN += _gcc=-Wno-switch
66 CERRWARN += _gcc=-Wno-uninitialized
67 CERRWARN += _gcc=-Wno-unused-value
68 CERRWARN += _gcc=-Wno-unused-label
69 CERRWARN += _gcc=-Wno-parentheses
70 #
71 # needs work
72 SMATCH=off
73 #
74 #
75 # Default build targets.
76 #
77 .KEEP_STATE:
78 #
79 all: $(ALL_DEPS)
80 #
81 def: $(DEF_DEPS)
82 #
83 clean: $(CLEAN_DEPS)
84 #
85 clobber: $(CLOBBER_DEPS)
86 #
87 lint: $(LINT_DEPS)
88 #
89 modlintlib: $(MODLINTLIB_DEPS)
90 #
91 clean.lint: $(CLEAN_LINT_DEPS)
92 #
93 install: $(INSTALL_DEPS)
94 #
95 #
96 # Include common targets.
97 #
98 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2377 Mon Jan 7 13:09:36 2019
new/usr/src/uts/intel/portfs/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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9 # or http://www.opensolaris.org/os/licensing.
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11 # and limitations under the License.
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15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/portfs/Makefile
23 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the portfs driver kernel module.
31 #
32 # intel architecture dependent
33 #
34 #
35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../

40 PORTFS_OBJS += port.o port_vnops.o port_fd.o port_fop.o

42 #
43 # Define the module and object file sets.
44 #
45 MODULE = portfs
46 OBJECTS = $(PORTFS_OBJS:%=$(OBJS_DIR)/%)
47 LINTS = $(PORTFS_OBJS:%.o=$(LINTS_DIR)/%.ln)
48 ROOTMODULE = $(ROOT_SYS_DIR)/$(MODULE)

50 #
51 # Include common rules.
52 #

54 include $(UTSBASE)/intel/Makefile.intel

56 #
57 # Define targets
58 #
59 ALL_TARGET = $(BINARY)
60 LINT_TARGET = $(MODULE).lint
61 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

```

```

63 #
64 # Overrides.
65 #
66 MODSTUBS_DIR = $(OBJS_DIR)
67 $(MODSTUBS_O) := AS_CPPFLAGS +=
68 CLEANFILES += $(MODSTUBS_O)

70 #
71 # For now, disable these lint checks; maintainers should endeavor
72 # to investigate and remove these for maximum lint coverage.
73 # Please do not carry these forward to new Makefiles.
74 #
75 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

77 CERRWARN += -_gcc=-Wno-uninitialized
78 CERRWARN += -_gcc=-Wno-parentheses

80 # needs work
81 $(OBJS_DIR)/port_fop.o := SMOFF += deref_check

83 #
84 # Default build targets.
85 #
86 .KEEP_STATE:

88 def: $(DEF_DEPS)

90 all: $(ALL_DEPS)

92 clean: $(CLEAN_DEPS)

94 clobber: $(CLOBBER_DEPS)

96 lint: $(LINT_DEPS)

98 modlintlib: $(MODLINTLIB_DEPS)

100 clean.lint: $(CLEAN_LINT_DEPS)

102 install: $(INSTALL_DEPS)

104 #
105 # Include common targets.
106 #
107 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
2612 Mon Jan 7 13:09:36 2019
new/usr/src/uts/intel/procfs/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
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15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/procfs/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the procfs file system
31 # kernel module.
32 #
33 # intel architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTBASE = ../..

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = procfs
45 OBJECTS = $(PROC_OBJS:%=$(OBJS_DIR)/%)
46 LINTS = $(PROC_OBJS:%.o=$(LINTS_DIR)/%.ln)
47 ROOTMODULE = $(ROOT_FS_DIR)/$(MODULE)

49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

61 #

```

```

62 # Overrides.
63 #
64 MODSTUBS_DIR = $(OBJS_DIR)
65 $(MODSTUBS_O) := AS_CPPFLAGS += -DPROC_MODULE
66 CLEANFILES += $(MODSTUBS_O)

68 #
69 # For now, disable these lint checks; maintainers should endeavor
70 # to investigate and remove these for maximum lint coverage.
71 # Please do not carry these forward to new Makefiles.
72 #
73 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
74 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
75 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

77 CERRWARN += -_gcc=-Wno-parentheses
78 CERRWARN += -_gcc=-Wno-switch
79 CERRWARN += -_gcc=-Wno-uninitialized

81 # needs work
82 $(OBJS_DIR)/prsubr.o := SMOFF += all_func_returns
83 $(OBJS_DIR)/prcontrol.o := SMOFF += all_func_returns
84 $(OBJS_DIR)/prioctl.o := SMOFF += signed

86 # false positives
87 $(OBJS_DIR)/prvnops.o := SMOFF += strcpy_overflow

89 #
90 # Default build targets.
91 #
92 .KEEP_STATE:

94 def: $(DEF_DEPS)

96 all: $(ALL_DEPS)

98 clean: $(CLEAN_DEPS)

100 clobber: $(CLOBBER_DEPS)

102 lint: $(LINT_DEPS)

104 modlintlib: $(MODLINTLIB_DEPS)

106 clean.lint: $(CLEAN_LINT_DEPS)

108 install: $(INSTALL_DEPS)

110 #
111 # Include common targets.
112 #
113 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
1921 Mon Jan 7 13:09:36 2019
new/usr/src/uts/intel/qede/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # This file and its contents are supplied under the terms of the
3 # Common Development and Distribution License ("CDDL"), version 1.0.
4 # You may only use this file in accordance with the terms of version
5 # 1.0 of the CDDL.
6 #
7 # A full copy of the text of the CDDL should have accompanied this
8 # source. A copy of the CDDL is also available via the Internet at
9 # http://www.illumos.org/license/CDDL.
10 #

12 #
13 # Copyright (c) 2018, Joyent, Inc.
13 # Copyright (c) 2017 Joyent, Inc.
14 #

16 UTSBASE = ../..

18 MODULE      = qede
19 OBJECTS     = $(QEDE_OBJS:%=$(OBJSDIR)/%) $(QEDE_GEN_OBJS:%=$(OBJSDIR)/%)
20 ROOTMODULE  = $(ROOT_DRV_DIR)/$(MODULE)
21 CONF_SRCDIR = $(UTSBASE)/common/io/qede

23 include $(UTSBASE)/intel/Makefile.intel

25 #
26 # Common definitions that are used by QLogic.
27 #
28 CPPFLAGS    += -D_inline=inline
29 CPPFLAGS    += -D_inline=inline
30 CPPFLAGS    += -DILLUMOS
31 CPPFLAGS    += -DECORE_CONFIG_DIRECT_HWFN
32 CPPFLAGS    += -DCONFIG_ECORE_L2

34 #
35 # Includes that are needed
36 #
37 CPPFLAGS    += -I$(UTSBASE)/common/io/qede
38 CPPFLAGS    += -I$(UTSBASE)/common/io/qede/579xx/drivers/ecore
39 CPPFLAGS    += -I$(UTSBASE)/common/io/qede/579xx/drivers/ecore/hsi_repositor
40 CPPFLAGS    += -I$(UTSBASE)/common/io/qede/579xx/hsi/
41 CPPFLAGS    += -I$(UTSBASE)/common/io/qede/579xx/hsi/hw
42 CPPFLAGS    += -I$(UTSBASE)/common/io/qede/579xx/hsi/mcp

44 #
45 # Temporarily gag these warnings for the moment. We'll work with
46 # upstream to get them clean.
47 #
48 CERRWARN    += -_gcc=-Wno-unused-variable
49 CERRWARN    += -_gcc=-Wno-unused-function

51 # 3rd party module
52 SMOFF += all_func_returns,indenting,no_if_block,deref_check,testing_index_after_

54 # real bug in qede_multicast()
55 $(OBJSDIR)/qede_gld.o := SMOFF += assign_vs_compare

57 ALL_TARGET = $(BINARY) $(CONFMOD)
58 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

60 LDFLAGS    += -dy -N misc/mac

```

```

62 .KEEP_STATE:

64 def:          $(DEF_DEPS)

66 all:          $(ALL_DEPS)

68 clean:        $(CLEAN_DEPS)

70 clobber:      $(CLOBBER_DEPS)

72 lint:
73 modlintlib:
74 clean.lint:

76 install:      $(INSTALL_DEPS)

78 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/qlc/Makefile

1

```
*****
2634 Mon Jan 7 13:09:36 2019
new/usr/src/uts/intel/qlc/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
12 #
13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

25 # This makefile drives the production of qlc driver kernel module.
27 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTSBASE = ../../

32 #
33 # Define the module and object file sets.
34 #
35 MODULE = qlc
36 OBJECTS = $(QLC_OBJS:%=$(OBJS_DIR)/%)
37 LINTS = $(QLC_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
39 CONF_SRCDIR = $(UTSBASE)/common/io/fibre-channel/fca/qlc

41 #
42 # Include common rules.
43 #
44 include $(UTSBASE)/Makefile.uts
45 include $(UTSBASE)/intel/Makefile.intel

47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY) $(CONFMOD) $(ITUMOD)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

54 #
55 # header file directories
56 #
57 INC_PATH += -I$(ROOT)/usr/include
58 INC_PATH += -I$(UTSBASE)/common/sys/fibre-channel
59 INC_PATH += -I$(UTSBASE)/common/sys/fibre-channel/ulp
60 INC_PATH += -I$(UTSBASE)/common/sys/fibre-channel/fca/qlc
```

new/usr/src/uts/intel/qlc/Makefile

2

```
61 INC_PATH += -I$(UTSBASE)/common/sys/fibre-channel/impl
63 LDFLAGS += -dy -Nmisc/fctl

65 #
66 # Lint tags
67 #
68 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
69 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED

71 FWIMAGES = 2200
72 FWIMAGES += 2300
73 FWIMAGES += 2400
74 FWIMAGES += 2500
75 FWIMAGES += 6322
76 FWIMAGES += 8100

78 FWMODULES = $(FWIMAGES:%=$(MODULE)_fw%)
79 FWMODULES_SRC = $(FWIMAGES:%=$(CONF_SRCDIR)/ql_fw%.c)

81 CERRWARN += _gcc=-Wno-uninitialized
82 CERRWARN += _gcc=-Wno-type-limits
83 CERRWARN += _gcc=-Wno-parentheses

85 # needs work
86 SMATCH=off

88 #
89 # Default build targets.
90 #
91 .KEEP_STATE:

93 all: $(ALL_DEPS)

95 def: $(DEF_DEPS)

97 clean: $(CLEAN_DEPS)

99 clobber: $(CLOBBER_DEPS)

101 lint: $(LINT_DEPS)

103 modlintlib: $(MODLINTLIB_DEPS)

105 clean.lint: $(CLEAN_LINT_DEPS)

107 install: $(INSTALL_DEPS)

109 #
110 # Include common targets.
111 #
112 include $(UTSBASE)/intel/Makefile.targ
```



new/usr/src/uts/intel/qlge/Makefile

1

\*\*\*\*\*

2054 Mon Jan 7 13:09:36 2019

new/usr/src/uts/intel/qlge/Makefile

10077 usr/src/uts/ Makefile changes for smatch

\*\*\*\*\*

```
1 #
2 # CDDL HEADER START
3 #
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9 # or http://www.opensolaris.org/os/licensing.
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19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2010 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
```

25 # This makefile drives the production of the qlge driver kernel module.

```
27 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTSBASE = ../../
31 COMMON_BASE = ../../../common
```

```
34 ARCHDIR:sh = cd ../; basename `pwd`
```

```
36 #
37 # Define the module and object file sets.
38 #
39 MODULE = qlge
40 OBJECTS = $(QLGE_OBJS:%=$(OBJS_DIR)/%)
41 LINTS = $(QLGE_OBJS:%.o=$(LINTS_DIR)/%.ln)
42 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
```

```
44 #
45 # Include common rules.
46 #
47 include ../Makefile.$(ARCHDIR)
```

```
49 #
50 # Define targets
51 #
52 ALL_TARGET = $(BINARY)
53 LINT_TARGET = $(MODULE).lint
54 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
```

```
56 #
57 # Overrides and depends_on
58 #
59 MODSTUBS_DIR = $(OBJS_DIR)
```

new/usr/src/uts/intel/qlge/Makefile

2

```
61 INC_PATH += -I$(UTSBASE)/common/sys/fibre-channel/fca/qlge
63 LDFLAGS += -dy -Nmisc/mac -Ndrv/ip
65 C99LMODE= -Xc99=%all
67 CERRWARN += -_gcc=-Wno-switch
68 CERRWARN += -_gcc=-Wno-uninitialized
```

```
70 # needs work
71 $(OBJS_DIR)/qlge.o := SMOFF += deref_check
```

```
73 #
74 # Default build targets.
75 #
76 .KEEP_STATE:
```

```
78 def: $(DEF_DEPS)
80 all: $(ALL_DEPS)
82 clean: $(CLEAN_DEPS)
84 clobber: $(CLOBBER_DEPS)
86 lint: $(LINT_DEPS)
88 modlintlib: $(MODLINTLIB_DEPS)
90 clean.lint: $(CLEAN_LINT_DEPS)
92 install: $(INSTALL_DEPS)
```

```
94 #
95 # Include common targets.
96 #
97 include ../Makefile.targ
```

```

*****
2776 Mon Jan 7 13:09:36 2019
new/usr/src/uts/intel/rdsv3/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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4 # The contents of this file are subject to the terms of the
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16 # fields enclosed by brackets "[]" replaced with your own identifying
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright (c) 2010, Oracle and/or its affiliates. All rights reserved.
23 #
24 # Copyright (c) 2018, Joyent, Inc.

26 #
27 # Path to the base of the uts directory tree (usually /usr/src/uts).
28 #
29 UTBASE = ../..

31 #
32 # Define the module and object file sets.
33 #
34 MODULE = rdsv3
35 OBJECTS = $(RDSV3_OBJS:%=$(OBJS_DIR)/%)
36 LINTS = $(RDSV3_OBJS:%.o=$(LINTS_DIR)/%.ln)
37 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
38 CFLAGS += $(CVERBOSE) $_XPG4_2
39 LDFLAGS += -dy -Nfs/sockfs -Nmisc/ksocket -Ndrv/ip -Nmisc/ibtl -Nmisc/ib
40 CONF_SRCDIR = $(UTSBASE)/common/io/ib/clients/rdsv3
41 #
42 # Include common rules.
43 #
44 include $(UTSBASE)/intel/Makefile.intel

46 # CFLAGS += -DOFA_SOLARIS

48 #
49 # Disable these lint checks since some errors suppressed here are
50 # in the OFED code, but we'd like to keep it as is as much as possible.
51 # Note. maintainers should endeavor to investigate and remove these for
52 # maximum lint coverage, but please do not carry these forward to new
53 # Makefiles blindly.
54 #
55 LINTTAGS += -erroff=E_STATIC_UNUSED
56 LINTTAGS += -erroff=E_CONSTANT_CONDITION
57 LINTTAGS += -erroff=E_FUNC_VAR_UNUSED
58 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
59 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
60 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
61 LINTTAGS += -erroff=E_FUNC_SET_NOT_USED

```

```

63 CERRWARN += -_gcc=-Wno-unused-label
64 CERRWARN += -_gcc=-Wno-unused-variable
65 CERRWARN += -_gcc=-Wno-unused-function
66 CERRWARN += -_gcc=-Wno-uninitialized
67 CERRWARN += -_gcc=-Wno-parentheses

69 # needs work
70 SMATCH=off

72 #
73 # Define targets
74 #
75 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
76 LINT_TARGET = $(MODULE).lint
77 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

79 #
80 # Default build targets.
81 #
82 .KEEP_STATE:

84 def: $(DEF_DEPS)

86 all: $(ALL_DEPS)

88 clean: $(CLEAN_DEPS)

90 clobber: $(CLOBBER_DEPS)

92 lint: $(LINT_DEPS)

94 modlintlib: $(MODLINTLIB_DEPS)

96 clean.lint: $(CLEAN_LINT_DEPS)

98 install: $(INSTALL_DEPS)

100 #
101 # Include common targets.
102 #
103 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/rge/Makefile

1

```
*****
2179 Mon Jan 7 13:09:37 2019
new/usr/src/uts/intel/rge/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
26 #
27 # This makefile drives the production of the Realtek
28 # Gigabit Ethernet (BGE) driver module in intel systems

27 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTBASE = ../..

32 #
33 # Define the module and object file sets.
34 #
35 MODULE = rge
36 OBJECTS = $(RGE_OBJS:%=$(OBJS_DIR)/%)
37 LINTS = $(RGE_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

40 #
41 # Include common rules.
42 #
43 include $(UTSBASE)/intel/Makefile.intel

45 #
46 # Define targets
47 #
48 ALL_TARGET = $(BINARY)
49 LINT_TARGET = $(MODULE).lint
50 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

52 #
53 # Overrides
54 #

56 #
57 # Driver depends on GLD & IP
```

new/usr/src/uts/intel/rge/Makefile

2

```
58 #
59 LDFLAGS += -dy -N misc/mac -N drv/ip

61 #
62 # For now, disable these lint checks; maintainers should endeavor
63 # to investigate and remove these for maximum lint coverage.
64 # Please do not carry these forward to new Makefiles.
65 #
66 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN

68 CERRWARN += -_gcc=-Wno-switch
69 CERRWARN += -_gcc=-Wno-uninitialized

71 # looks like a real bug in rge_phy_update() !
72 $(OBJS_DIR)/rge_chip.o := SMOFF += indenting

74 #
75 # Default build targets.
76 #
77 .KEEP_STATE:

79 def: $(DEF_DEPS)

81 all: $(ALL_DEPS)

83 clean: $(CLEAN_DEPS)

85 clobber: $(CLOBBER_DEPS)

87 lint: $(LINT_DEPS)

89 modlintlib: $(MODLINTLIB_DEPS)

91 clean.lint: $(CLEAN_LINT_DEPS)

93 install: $(INSTALL_DEPS)

95 #
96 # Include common targets.
97 #
98 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/rpcib/Makefile

1

```
*****
2441 Mon Jan 7 13:09:37 2019
new/usr/src/uts/intel/rpcib/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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9 # or http://www.opensolaris.org/os/licensing.
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11 # and limitations under the License.
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # This makefile drives the production of the kernel
29 # rpcib module.
30 #
31 # intel implementation architecture dependent
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../

38 #
39 # Define the module and object file sets.
40 #
41 MODULE = rpcib
42 OBJECTS = $(RPCIB_OBJS:%=$(OBJSDIR)/%)
43 LINTS = $(RPCIB_OBJS:%.o=$(LINTSDIR)/%.ln)
44 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
45 CONF_SRCDIR = $(UTSBASE)/common/rpc

47 #
48 # Include common rules.
49 #
50 include $(UTSBASE)/intel/Makefile.intel

52 #
53 # Define targets
54 #
55 ALL_TARGET = $(BINARY) $(SRC_CONFILE)
56 LINT_TARGET = $(MODULE).lint
57 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

59 #
60 # lint pass one enforcement
61 #
```

new/usr/src/uts/intel/rpcib/Makefile

2

```
62 CFLAGS += $(CCVERBOSE)

64 #
65 # depends on misc/ibtl and misc/ibcm
66 #
67 LDFLAGS += -dy -Nmisc/ibtl -Nmisc/ibcm

69 #
70 # For now, disable these lint checks; maintainers should endeavor
71 # to investigate and remove these for maximum lint coverage.
72 # Please do not carry these forward to new Makefiles.
73 #
74 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
75 LINTTAGS += -erroff=E_STATIC_UNUSED

77 CERRWARN += -_gcc=-Wno-unused-variable
78 CERRWARN += -_gcc=-Wno-unused-function
79 CERRWARN += -_gcc=-Wno-uninitialized

81 # needs work
82 SMATCH=off

84 #
85 # Default build targets.
86 #
87 .KEEP_STATE:

89 def: $(DEF_DEPS)

91 all: $(ALL_DEPS)

93 clean: $(CLEAN_DEPS)

95 clobber: $(CLOBBER_DEPS)

97 lint: $(LINT_DEPS)

99 modlintlib: $(MODLINTLIB_DEPS)

101 clean.lint: $(CLEAN_LINT_DEPS)

103 install: $(INSTALL_DEPS)

105 #
106 # Include common targets.
107 #
108 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2884 Mon Jan 7 13:09:37 2019
new/usr/src/uts/intel/rpcmod/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
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2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/rpcmod/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
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26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the rpcmod streams kernel
31 # module.
32 #
33 # intel architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTBASE = ../../

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = rpcmod
45 OBJECTS = $(RPCMOD_OBJS:%=$(OBJSDIR)/%)
46 LINTS = $(RPCMOD_OBJS%.o=$(LINTSDIR)/%.ln)
47 ROOTMODULE = $(ROOT_STRMODDIR)/$(MODULE)
48 ROOTLINK = $(ROOT_SYS_DIR)/$(MODULE)

50 #
51 # Include common rules.
52 #
53 include $(UTBASE)/intel/Makefile.intel

55 #
56 # Define targets
57 #
58 ALL_TARGET = $(BINARY)
59 LINT_TARGET = $(MODULE).lint
60 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTLINK)

```

```

62 #
63 # Overrides.
64 #
65 MODSTUBS_DIR = $(OBJSDIR)
66 $(MODSTUBS_O) := AS_CPPFLAGS += -DRPCMOD_MODULE
67 CLEANFILES += $(MODSTUBS_O)

69 #
70 # depends_on
71 #
72 LDFLAGS += -dy -Nmisc/tlimod

74 #
75 # For now, disable these lint checks; maintainers should endeavor
76 # to investigate and remove these for maximum lint coverage.
77 # Please do not carry these forward to new Makefiles.
78 #
79 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
80 LINTTAGS += -erroff=E_STATIC_UNUSED
81 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
82 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
83 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
84 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED

86 CERRWARN += -_gcc=-Wno-parentheses
87 CERRWARN += -_gcc=-Wno-uninitialized
88 CERRWARN += -_gcc=-Wno-switch
89 CERRWARN += -_gcc=-Wno-unused-variable
90 CERRWARN += -_gcc=-Wno-unused-label
91 CERRWARN += -_gcc=-Wno-unused-function

93 # needs work
94 SMOFF += all_func_returns,indenting,no_if_block,deref_check

96 #
97 # Default build targets.
98 #
99 .KEEP_STATE:

101 def: $(DEF_DEPS)
103 all: $(ALL_DEPS)

105 clean: $(CLEAN_DEPS)

107 clobber: $(CLOBBER_DEPS)

109 lint: $(LINT_DEPS)

111 modlintlib: $(MODLINTLIB_DEPS)

113 clean.lint: $(CLEAN_LINT_DEPS)

115 install: $(INSTALL_DEPS)

117 $(ROOTLINK): $(ROOT_SYS_DIR) $(ROOTMODULE)
118 -$(RM) $@; ln $(ROOTMODULE) $@

120 #
121 # Include common targets.
122 #
123 include $(UTBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/rpcsec/Makefile

1

```
*****
2336 Mon Jan 7 13:09:37 2019
new/usr/src/uts/intel/rpcsec/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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20 #
21 #
22 # uts/intel/rpcsec/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 # Copyright (c) 2011 Bayard G. Bell. All rights reserved.
27 #
28 # Copyright (c) 2018, Joyent, Inc.
29 #
30 #
31 #
32 #
33 # Path to the base of the uts directory tree (usually /usr/src/uts).
34 #
35 UTSBASE = ../../
36 #
37 #
38 # Define the module and object file sets.
39 #
40 #
41 #
42 #
43 #
44 # Include common rules.
45 #
46 include $(UTSBASE)/intel/Makefile.intel
47 #
48 #
49 # Define targets
50 #
51 #
52 #
53 #
54 #
55 #
56 # Define dependencies on rpcmod and tlimod
57 #
```

new/usr/src/uts/intel/rpcsec/Makefile

2

```
58 LDFLAGS += -dy -N strmod/rpcmod -N misc/tlimod
59 #
60 #
61 # For now, disable these lint checks; maintainers should endeavor
62 # to investigate and remove these for maximum lint coverage.
63 # Please do not carry these forward to new Makefiles.
64 #
65 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
66 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
67 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
68 #
69 CERRWARN += -_gcc=-Wno-parentheses
70 CERRWARN += -_gcc=-Wno-uninitialized
71 #
72 # needs work
73 $(OBJS_DIR)/auth_des.o := SMOFF += deref_check
74 #
75 #
76 # Default build targets.
77 #
78 .KEEP_STATE:
79 #
80 def: $(DEF_DEPS)
81 #
82 all: $(ALL_DEPS)
83 #
84 clean: $(CLEAN_DEPS)
85 #
86 clobber: $(CLOBBER_DEPS)
87 #
88 lint: $(LINT_DEPS)
89 #
90 modlintlib: $(MODLINTLIB_DEPS)
91 #
92 clean.lint: $(CLEAN_LINT_DEPS)
93 #
94 install: $(INSTALL_DEPS)
95 #
96 #
97 # Include common targets.
98 #
99 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/rsm/Makefile

1

```
*****
2503 Mon Jan 7 13:09:37 2019
new/usr/src/uts/intel/rsm/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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20 #
21 #
22 # uts/intel/rsm/Makefile
23 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.

28 #
29 # This makefile drives the production of the rsm kernel agent

31 #
32 # Path to the base of the uts directory tree (usually /usr/src/uts).
33 #
34 UTBASE = ../..

36 #
37 # Define the module and object file sets.
38 #
39 MODULE = rsm
40 OBJECTS = $(RSM_OBJS:%=$(OBJS_DIR)/%)
41 LINTS = $(RSM_OBJS:%.o=$(LINTS_DIR)/%.ln)
42 ROOTMODULE = $(USR_DRV_DIR)/$(MODULE)
43 CONF_SRCDIR = $(UTBASE)/common/io/rsm

45 #
46 # Include common rules.
47 #
48 include $(UTBASE)/intel/Makefile.intel

50 #
51 # Define targets
52 #
53 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
54 LINT_TARGET = $(MODULE).lint
55 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

57 #
58 # path for rsm_common.h
59 #
60 INC_PATH += -I $(UTBASE)/common/sys/rsm
```

new/usr/src/uts/intel/rsm/Makefile

2

```
62 #
63 # depends on rsmops
64 #
65 LDFLAGS += -dy -Nmisc/rsmops

67 #
68 # Flag to enable for DR testing via drtest driver
69 #
70 #CFLAGS += -DRSM_DRTEST
71 #LDFLAGS += -Ndrv/rsm_drtest

73 #
74 # For now, disable these lint checks; maintainers should endeavor
75 # to investigate and remove these for maximum lint coverage.
76 # Please do not carry these forward to new Makefiles.
77 #
78 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
79 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
80 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

82 CERRWARN += -_gcc=-Wno-uninitialized
83 CERRWARN += -_gcc=-Wno-parentheses

85 # needs work
86 SMATCH=off

88 #
89 # Default build targets.
90 #
91 .KEEP_STATE:

93 def: $(DEF_DEPS)

95 all: $(ALL_DEPS)

97 clean: $(CLEAN_DEPS)

99 clobber: $(CLOBBER_DEPS)

101 lint: $(LINT_DEPS)

103 modlintlib: $(MODLINTLIB_DEPS)

105 clean.lint: $(CLEAN_LINT_DEPS)

107 install: $(INSTALL_DEPS)

109 #
110 # Include common targets.
111 #
112 include $(UTBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/rts/Makefile

1

```
*****
2664 Mon Jan 7 13:09:38 2019
new/usr/src/uts/intel/rts/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/rts/Makefile
23 #
24 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
28 #   This makefile drives the production of the rts IP driver
29 #   intel implementation architecture dependent
30 #
31 #
32 # Path to the base of the uts directory tree (usually /usr/src/uts).
33 #
34 # UTSBASE = ../../
35 #
36 # Define the module and object file sets.
37 #
38 # MODULE          = rts
39 # OBJECTS         = $(RTS_OBJS:%=$(OBJS_DIR)/%)
40 # LINTS           = $(RTS_OBJS:%.o=$(LINTS_DIR)/%.ln)
41 # ROOTMODULE     = $(ROOT_DRV_DIR)/$(MODULE)
42 # ROOTLINK       = $(ROOT SOCK_DIR)/$(MODULE)
43 # CONF_SRCDIR    = $(UTSBASE)/common/inet/ip
44 #
45 # Extra for $(MODULE).check target
46 #
47 # Need to remove ipddi.o since it has non-static defines for _init etc.
48 # IP_CHECK_OBJS  = $(IP_OBJS:ipddi.o=ip.o)
49 # EXTRA_CHECK_OBJS = $(IP_CHECK_OBJS:%=../ip/$(OBJS_DIR)/%)
50 #
51 #
52 # Include common rules.
53 #
54 # include $(UTSBASE)/intel/Makefile.intel
55 #
56 #
57 # Define targets
```

new/usr/src/uts/intel/rts/Makefile

2

```
58 #
59 ALL_TARGET      = $(BINARY) $(SRC_CONFFILE)
60 LINT_TARGET     = $(MODULE).lint
61 INSTALL_TARGET  = $(BINARY) $(ROOTMODULE) $(ROOTLINK) $(ROOT_CONFFILE)
62 #
63 #
64 #     depends on ip and sockfs
65 #
66 LDFLAGS         += -dy -Ndrv/ip -Nfs/sockfs
67 #
68 #
69 # For now, disable these lint checks; maintainers should endeavor
70 # to investigate and remove these for maximum lint coverage.
71 # Please do not carry these forward to new Makefiles.
72 #
73 LINTTAGS        += -erroff=E_BAD_PTR_CAST_ALIGN
74 LINTTAGS        += -erroff=E_PTRDIFF_OVERFLOW
75 LINTTAGS        += -erroff=E_ASSIGN_NARROW_CONV
76 #
77 # needs work
78 $(OBJS_DIR)/rtsddi.o := SMOFF += index_overflow
79 #
80 #
81 #     Default build targets.
82 #
83 .KEEP_STATE:
84 #
85 def:            $(DEF_DEPS)
86 #
87 all:            $(ALL_DEPS) $(SISCHECK_DEPS)
88 #
89 clean:          $(CLEAN_DEPS) $(SISCLEAN_DEPS)
90 #
91 clobber:        $(CLOBBER_DEPS) $(SISCLEAN_DEPS)
92 #
93 lint:           $(LINT_DEPS)
94 #
95 modlintlib:    $(MODLINTLIB_DEPS)
96 #
97 clean.lint:    $(CLEAN_LINT_DEPS)
98 #
99 install:       $(INSTALL_DEPS) $(SISCHECK_DEPS)
100 #
101 $(ROOTLINK):   $(ROOT_STRMOD_DIR) $(ROOT SOCK_DIR) $(ROOTMODULE)
102               -$(RM) $@; ln $(ROOTMODULE) $@
103 #
104 #
105 #     Include common targets.
106 #
107 include $(UTSBASE)/intel/Makefile.targ
```



new/usr/src/uts/intel/s1394/Makefile

1

```
*****
2646 Mon Jan 7 13:09:38 2019
new/usr/src/uts/intel/s1394/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/l394/s1394/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the s1394 kernel module.
31 #
32 # intel architecture dependent
33 #

35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTBASE = ../../

40 #
41 # Define the module and object file sets.
42 #
43 MODULE = s1394
44 OBJECTS = $(S1394_OBJS:%=$(OBJS_DIR)/%)
45 LINTS = $(S1394_OBJS:%.o=$(LINTS_DIR)/%.ln)
46 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)

48 #
49 # Include common rules.
50 #
51 include $(UTSBASE)/intel/Makefile.intel

53 #
54 # Define targets
55 #
56 ALL_TARGET = $(BINARY)
57 LINT_TARGET = $(MODULE).lint
58 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

60 #
61 # Overrides
```

new/usr/src/uts/intel/s1394/Makefile

2

```
62 #
63 #ALL_BUILDS = $(ALL_BUILDSONLY64)
64 #DEF_BUILDS = $(DEF_BUILDSONLY64)
65 #CLEANLINTFILES += $(LINT32_FILES)

67 DEBUG_DEFS_DBG32 += -DTNF_DEBUG
68 #DEBUG_DEFS_DBG64 += -DTNF_DEBUG

70 #
71 # For now, disable these lint checks; maintainers should endeavor
72 # to investigate and remove these for maximum lint coverage.
73 # Please do not carry these forward to new Makefiles.
74 #
75 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
76 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
77 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
78 LINTTAGS += -erroff=E_STATIC_UNUSED
79 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
80 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

82 CERRWARN += _gcc=-Wno-switch
83 CERRWARN += _gcc=-Wno-uninitialized
84 CERRWARN += _gcc=-Wno-unused-variable
85 CERRWARN += _gcc=-Wno-parentheses

87 # needs work
88 SMOFF += indenting

90 #
91 # Default build targets.
92 #
93 .KEEP_STATE:

95 def: $(DEF_DEPS)

97 all: $(ALL_DEPS)

99 clean: $(CLEAN_DEPS)

101 clobber: $(CLOBBER_DEPS)

103 lint: $(LINT_DEPS)

105 modlintlib: $(MODLINTLIB_DEPS)

107 clean.lint: $(CLEAN_LINT_DEPS)

109 install: $(INSTALL_DEPS)

111 #
112 # Include common targets.
113 #
114 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2617 Mon Jan 7 13:09:38 2019
new/usr/src/uts/intel/sata/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
12 #
13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
22 #
23 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
29 #
30 # uts/intel/io/sata/Makefile
31 #
32 # This makefile drives the production of the sata "misc"
33 # kernel module.
34 #
35 # intel architecture dependent
36 #
38 #
39 # Path to the base of the uts directory tree (usually /usr/src/uts).
40 #
41 UTBASE = ../..
43 #
44 # Define the module and object file sets.
45 #
46 MODULE = sata
47 OBJECTS = $(SATA_OBJS:%=$(OBJDIR)/%)
48 LINTS = $(SATA_OBJS:%.o=$(LINTSDIR)/%.ln)
49 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
51 #
52 # Include common rules.
53 #
54 include $(UTSBASE)/intel/Makefile.intel
56 #
57 # Define targets
58 #
59 ALL_TARGET = $(BINARY)
60 LINT_TARGET = $(MODULE).lint
61 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

```

```

63 #
64 # Overrides.
65 #
66 DEBUG_FLGS =
67 DEBUG_DEFS += $(DEBUG_FLGS)
69 #
70 # lint pass one enforcement
71 #
72 CFLAGS += $(CCVERBOSE)
74 #
75 # dependency on scsi module
76 #
77 LDFLAGS += -dy -Nmisc/scsi
79 #
80 # For now, disable these lint checks; maintainers should endeavor
81 # to investigate and remove these for maximum lint coverage.
82 # Please do not carry these forward to new Makefiles.
83 #
84 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
85 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
86 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
87 LINTTAGS += -erroff=E_STATIC_UNUSED
89 CERRWARN += -_gcc=-Wno-parentheses
90 CERRWARN += -_gcc=-Wno-unused-label
91 CERRWARN += -_gcc=-Wno-unused-function
92 CERRWARN += -_gcc=-Wno-unused-variable
93 CERRWARN += -_gcc=-Wno-uninitialized
94 CERRWARN += -_gcc=-Wno-empty-body
96 # needs work
97 SMATCH=off
99 #
100 # Default build targets.
101 #
102 .KEEP_STATE:
104 def: $(DEF_DEPS)
106 all: $(ALL_DEPS)
108 clean: $(CLEAN_DEPS)
110 clobber: $(CLOBBER_DEPS)
112 lint: $(LINT_DEPS)
114 modlintlib: $(MODLINTLIB_DEPS)
116 clean.lint: $(CLEAN_LINT_DEPS)
118 install: $(INSTALL_DEPS)
120 #
121 # Include common targets.
122 #
123 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/scsa1394/Makefile

1

```
*****
2211 Mon Jan 7 13:09:39 2019
new/usr/src/uts/intel/scsa1394/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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7 #
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9 # or http://www.opensolaris.org/os/licensing.
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11 # and limitations under the License.
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

28 #
29 # Path to the base of the uts directory tree.
30 #
31 UTBASE = ../..

33 #
34 # Define the module and object file sets.
35 #
36 MODULE = scsa1394
37 OBJECTS = $(SCSA1394_OBJS:%=$(OBJS_DIR)/%)
38 LINTS = $(SCSA1394_OBJS:%.o=$(LINTS_DIR)/%.ln)
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

41 #
42 # Include common rules.
43 #
44 include $(UTBASE)/intel/Makefile.intel

46 #
47 # Define targets
48 #
49 ALL_TARGET = $(BINARY)
50 LINT_TARGET = $(MODULE).lint
51 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

53 #
54 # depends on
55 #
56 LDFLAGS += -dy -Nmisc/scsi -Nmisc/sl394 -Nmisc/sbp2

58 #
59 # For now, disable these lint checks; maintainers should endeavor
60 # to investigate and remove these for maximum lint coverage.
61 # Please do not carry these forward to new Makefiles.
```

new/usr/src/uts/intel/scsa1394/Makefile

2

```
62 #
63 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
64 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
65 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

67 CERRWARN += -_gcc=-Wno-parentheses
68 CERRWARN += -_gcc=-Wno-uninitialized

70 # needs work
71 $(OBJS_DIR)/sbp2_driver.o := SMOFF += indenting

73 # Default build targets.
74 #
75 .KEEP_STATE:

77 def: $(DEF_DEPS)

79 all: $(ALL_DEPS)

81 clean: $(CLEAN_DEPS)

83 clobber: $(CLOBBER_DEPS)

85 lint: $(LINT_DEPS)

87 modlintlib: $(MODLINTLIB_DEPS)

89 clean.lint: $(CLEAN_LINT_DEPS)

91 install: $(INSTALL_DEPS)

93 #
94 # Include common targets.
95 #
96 include $(UTBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/scsa2usb/Makefile

1

```
*****
2441 Mon Jan 7 13:09:39 2019
new/usr/src/uts/intel/scsa2usb/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
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11 # and limitations under the License.
12 #
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15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/scsa2usb/Makefile
23 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright 2014 Garrett D'Amore <garrett@damore.org>
27 #
28 # Copyright (c) 2018, Joyent, Inc.
29 # This makefile drives the production of the scsa2usb driver
30 # kernel module. intel architecture dependent
31 #
32 #
33 #
34 #
35 #
36 # Define the module and object file sets.
37 #
38 # MODULE = scsa2usb
39 # OBJECTS = $(SCSA2USB_OBJS:%=$(OBJDIR)/%)
40 # LINTS = $(SCSA2USB_OBJS:%.o=$(LINTSDIR)/%.ln)
41 # ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
42 # CONF_SRCDIR = $(UTSBASE)/common/io/usb/scsa2usb
43 #
44 #
45 # Include common rules.
46 #
47 # include $(UTSBASE)/intel/Makefile.intel
48 #
49 #
50 # Define targets
51 #
52 # ALL_TARGET = $(BINARY) $(SRC_CONFILE)
53 # LINT_TARGET = $(MODULE).lint
54 # INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
55 #
56 #
57 # Override defaults to build a unique, local modstubs.o.
58 #
```

new/usr/src/uts/intel/scsa2usb/Makefile

2

```
59 MODSTUBS_DIR = $(OBJDIR)
60 CLEANFILES += $(MODSTUBS_O)
61 #
62 #
63 # depends on misc/usba misc/scsi
64 #
65 # LDFLAGS += -dy -Nmisc/usba -Nmisc/scsi
66 #
67 #
68 # For now, disable these lint checks; maintainers should endeavor
69 # to investigate and remove these for maximum lint coverage.
70 # Please do not carry these forward to new Makefiles.
71 #
72 # LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
73 #
74 # CERRWARN += -_gcc=-Wno-unused-label
75 # CERRWARN += -_gcc=-Wno-uninitialized
76 #
77 # needs work
78 $(OBJDIR)/scsa2usb.o := SMOFF += deref_check
79 #
80 #
81 # Default build targets.
82 #
83 # .KEEP_STATE:
84 #
85 # def: $(DEF_DEPS)
86 #
87 # all: $(ALL_DEPS)
88 #
89 # clean: $(CLEAN_DEPS)
90 #
91 # clobber: $(CLOBBER_DEPS)
92 #
93 # lint: $(LINT_DEPS)
94 #
95 # modlintlib: $(MODLINTLIB_DEPS)
96 #
97 # clean.lint: $(CLEAN_LINT_DEPS)
98 #
99 # install: $(INSTALL_DEPS)
100 #
101 #
102 # Include common targets.
103 #
104 # include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2334 Mon Jan 7 13:09:39 2019
new/usr/src/uts/intel/scsi/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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9 # or http://www.opensolaris.org/os/licensing.
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/io/scsi/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the scsi kernel module.
31 #
32 # intel architecture dependent
33 #

35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTBASE = ../../

40 #
41 # Define the module and object file sets.
42 #
43 MODULE = scsi
44 OBJECTS = $(SCSI_OBJS:%=$(OBJS_DIR)/%)
45 LINTS = $(SCSI_OBJS:%.o=$(LINTS_DIR)/%.ln)
46 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)

48 #
49 # Include common rules.
50 #
51 include $(UTBASE)/intel/Makefile.intel

53 #
54 # Define targets
55 #
56 ALL_TARGET = $(BINARY)
57 LINT_TARGET = $(MODULE).lint
58 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

60 #
61 # Overrides.

```

```

62 #
63 DEBUG_FLGS =
64 DEBUG_DEFS += $(DEBUG_FLGS)

66 #
67 # For now, disable these lint checks; maintainers should endeavor
68 # to investigate and remove these for maximum lint coverage.
69 # Please do not carry these forward to new Makefiles.
70 #
71 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
72 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
73 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

75 CERRWARN += -_gcc=-Wno-parentheses
76 CERRWARN += -_gcc=-Wno-switch
77 CERRWARN += -_gcc=-Wno-uninitialized

79 # needs work
80 SMATCH=off

82 #
83 # Default build targets.
84 #
85 .KEEP_STATE:

87 def: $(DEF_DEPS)

89 all: $(ALL_DEPS)

91 clean: $(CLEAN_DEPS)

93 clobber: $(CLOBBER_DEPS)

95 lint: $(LINT_DEPS)

97 modlintlib: $(MODLINTLIB_DEPS)

99 clean.lint: $(CLEAN_LINT_DEPS)

101 install: $(INSTALL_DEPS)

103 #
104 # Include common targets.
105 #
106 include $(UTBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/scsi\_vhci/Makefile

1

```
*****
2087 Mon Jan 7 13:09:39 2019
new/usr/src/uts/intel/scsi_vhci/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 #
28 #
29 # This makefile drives the production of SCSI vHCI Driver
30 # intel architecture dependent
31 #
32 #
33 #
34 # Paths to the base of the uts directory trees
35 #
36 UTSBASE = ../../
37 #
38 #
39 # Define the module and object file sets.
40 #
41 MODULE = scsi_vhci
42 OBJECTS = $(SCSI_VHCI_OBJS:%=$(OBJSDIR)/%)
43 LINTS = $(SCSI_VHCI_OBJS:%.o=$(LINTSDIR)/%.ln)
44 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
45 CONF_SRCDIR = $(UTSBASE)/common/io/scsi/adapters/scsi_vhci
46 #
47 #
48 # Include common rules.
49 #
50 include $(UTSBASE)/intel/Makefile.intel
51 #
52 CERRWARN += _gcc=-Wno-parentheses
53 CERRWARN += _gcc=-Wno-uninitialized
54 CERRWARN += _gcc=-Wno-unused-label
55 #
56 # needs work
57 SMATCH=off
58 #
59 #
60 # Define targets.
61 #
```

new/usr/src/uts/intel/scsi\_vhci/Makefile

2

```
62 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
63 LINT_TARGET = $(MODULE).lint
64 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
65 #
66 #
67 # Note dependancy on misc/scsi.
68 #
69 LDPLAGS += -dy -N"misc/scsi"
70 #
71 #
72 # Default build targets.
73 #
74 .KEEP_STATE:
75 #
76 def: $(DEF_DEPS)
77 #
78 all: $(ALL_DEPS)
79 #
80 clean: $(CLEAN_DEPS)
81 #
82 clobber: $(CLOBBER_DEPS)
83 #
84 lint: $(LINT_DEPS)
85 #
86 modlintlib: $(MODLINTLIB_DEPS)
87 #
88 clean.lint: $(CLEAN_LINT_DEPS)
89 #
90 install: $(INSTALL_DEPS)
91 #
92 #
93 # Include common targets.
94 #
95 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/sd/Makefile

1

```
*****
2493 Mon Jan 7 13:09:39 2019
new/usr/src/uts/intel/sd/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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9 # or http://www.opensolaris.org/os/licensing.
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11 # and limitations under the License.
12 #
13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 # uts/intel/io/sd/Makefile
22 #
23 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 # Copyright (c) 2011 Bayard G. Bell. All rights reserved.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the sd
28 # kernel module.
29 #
30 #
29 #
30 # Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 UTSBASE = ../../
34 #
35 # Define the module and object file sets.
36 #
37 MODULE = sd
38 OBJECTS = ${SD_OBJS:%=${OBJSDIR}/%}
39 LINTS = ${SD_OBJS:%.o=${LINTSDIR}/%.lint}
40 ROOTMODULE = ${ROOT_DRV_DIR}/${MODULE}
41 CONF_SRCDIR = ${UTSBASE}/intel/io/scsi/targets
43 #
44 # Include common rules.
45 #
46 include ${UTSBASE}/intel/Makefile.intel
48 #
49 # Define targets
50 #
51 ALL_TARGET = ${BINARY} ${CONFMOD}
52 LINT_TARGET = ${MODULE}.lint
53 INSTALL_TARGET = ${BINARY} ${ROOTMODULE} ${ROOT_CONFFILE}
55 #
56 # Overrides.
57 #
```

new/usr/src/uts/intel/sd/Makefile

2

```
58 DEBUG_FLGS =
59 DEBUG_DEFS += $(DEBUG_FLGS)
61 INC_PATH += -I$(UTSBASE)/intel/io/scsi/targets
63 #
64 # For now, disable these lint checks; maintainers should endeavor
65 # to investigate and remove these for maximum lint coverage.
66 # Please do not carry these forward to new Makefiles.
67 #
68 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
69 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
70 LINTTAGS += -erroff=E_STATIC_UNUSED
71 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
72 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
74 # needs work
75 SMOFF += index_overflow,shift_to_zero,signed
77 #
78 # Depends on scsi and cmlb
79 #
80 LDFFLAGS += -dy -N misc/scsi -N misc/cmlb
82 #
83 # Default build targets.
84 #
85 .KEEP_STATE:
87 def: $(DEF_DEPS)
89 all: $(ALL_DEPS)
91 clean: $(CLEAN_DEPS)
93 clobber: $(CLOBBER_DEPS)
95 lint: $(LINT_DEPS)
97 modlintlib: $(MODLINTLIB_DEPS)
99 clean.lint: $(CLEAN_LINT_DEPS)
101 install: $(INSTALL_DEPS)
103 #
104 # Include common targets.
105 #
106 include ${UTSBASE}/intel/Makefile.targ
```

```

*****
2224 Mon Jan 7 13:09:39 2019
new/usr/src/uts/intel/sda/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
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19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright (c) 2008, 2010, Oracle and/or its affiliates. All rights reserved.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
26 #
27 #
28 #
29 # uts/intel/sda/Makefile
30 #
31 # This makefile drives the production of the sda "misc"
32 # kernel module.
33 #
34 # intel architecture dependent
35 #
36 #
37 #
38 # Path to the base of the uts directory tree (usually /usr/src/uts).
39 #
40 UTSBASE = ../../
41 #
42 #
43 # Define the module and object file sets.
44 #
45 MODULE = sda
46 OBJECTS = $(SDA_OBJS:=$(OBJS_DIR)/%)
47 LINTS = $(SDA_OBJS:%.o=$(LINTS_DIR)/%.ln)
48 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
49 #
50 #
51 # Include common rules.
52 #
53 include $(UTSBASE)/intel/Makefile.intel
54 #
55 #
56 # Define targets
57 #
58 ALL_TARGET = $(BINARY)
59 LINT_TARGET = $(MODULE).lint
60 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

```

```

62 #
63 # Overrides.
64 #
65 DEBUG_FLGS =
66 DEBUG_DEFS += $(DEBUG_FLGS)
67 #
68 #
69 # lint pass one enforcement
70 #
71 CFLAGS += $(CCVERBOSE)
72 #
73 #
74 # dependency on blkdev module, scope limiting mapfile
75 # Note that we have to allow CTFMERGE to use fuzzy matching
76 #
77 MAPFILE = $(UTSBASE)/common/io/sdcard/impl/mapfile
78 LD_FLAGS += -dy -Ndrv/blkdev -B reduce -M $(MAPFILE)
79 CTFMRG_FLAGS += -f
80 #
81 # needs work
82 SMOFF += all_func_returns
83 #
84 #
85 # Default build targets.
86 #
87 .KEEP_STATE:
88 #
89 def: $(DEF_DEPS)
90 #
91 all: $(ALL_DEPS)
92 #
93 clean: $(CLEAN_DEPS)
94 #
95 clobber: $(CLOBBER_DEPS)
96 #
97 lint: $(LINT_DEPS)
98 #
99 modlintlib: $(MODLINTLIB_DEPS)
100 #
101 clean.lint: $(CLEAN_LINT_DEPS)
102 #
103 install: $(INSTALL_DEPS)
104 #
105 #
106 # Include common targets.
107 #
108 include $(UTSBASE)/intel/Makefile.targ

```



new/usr/src/uts/intel/sfe/Makefile

1

```
*****
2686 Mon Jan 7 13:09:40 2019
new/usr/src/uts/intel/sfe/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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25 # Copyright (c) 2018, Joyent, Inc.

28 #
29 # This makefile drives the production of NS/SiS
30 # Fast Ethernet (SFE) driver module in intel systems
31 #

33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTBASE = ../../

38 #
39 # Define the module and object file sets.
40 #
41 MODULE = sfe
42 OBJECTS = $(SFE_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(SFE_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

46 #
47 # Include common rules.
48 #
49 include $(UTBASE)/intel/Makefile.intel

51 #
52 # Define targets
53 #
54 ALL_TARGET = $(BINARY)
55 LINT_TARGET = $(MODULE).lint
56 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

58 #
59 # GENERAL PURPOSE GEM FLAGS: Tuning GEM for Solaris specific modes
60 #
61 VFLAGS = -DVERSION=' "2.6.1"'
```

new/usr/src/uts/intel/sfe/Makefile

2

```
62 AFLAGS = -Di86pc
63 DFLAGS = -D"__INLINE__="
64 CFGFLAGS = -DGEM_CONFIG_POLLING -DGEM_CONFIG_GLDv3 -DGEM_CONFIG_VLAN \
65 -DGEM_CONFIG_CKSUM_OFFLOAD -DGEM_CONFIG_ND \
66 -DCONFIG_DP83815 -DCONFIG_SIS900 -DCONFIG_SIS7016 \
67 -DCONFIG_MAC_ADDR_SIS630E -DCONFIG_OPT_IO -UCONFIG_OO \
68 -DCONFIG_PATTERN_MATCH_DP83815
69 #
70 # FAST PATH SECTION: Will activate usage of inlines as a regular functions
71 # on fast data path

73 CPPFLAGS += $(VFLAGS) $(AFLAGS) $(DFLAGS) $(CFGFLAGS) $(CCVERBOSE) \
74 -I$(UTSBASE)/common/io/sfe

76 CFLAGS += $(CPPFLAGS)

78 CERRWARN += -_gcc=-Wno-unused-label
79 CERRWARN += -_gcc=-Wno-switch
80 CERRWARN += -_gcc=-Wno-parentheses
81 CERRWARN += -_gcc=-Wno-uninitialized

83 # needs work
84 SMOFF += all_func_returns

86 #
87 # Driver depends on MAC & IP
88 #
89 LDFLAGS += -dy -N misc/mac -N drv/ip

91 #
92 # Default build targets.
93 #
94 .KEEP_STATE:

96 def: $(DEF_DEPS)

98 all: $(ALL_DEPS)

100 clean: $(CLEAN_DEPS)

102 clobber: $(CLOBBER_DEPS)

104 lint: $(LINT_DEPS)

106 modlintlib: $(MODLINTLIB_DEPS)

108 clean.lint: $(CLEAN_LINT_DEPS)

110 install: $(INSTALL_DEPS)

112 #
113 # Include common targets.
114 #
115 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
1700 Mon Jan 7 13:09:40 2019
new/usr/src/uts/intel/sfxge/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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5 # 1.0 of the CDDL.
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8 # source. A copy of the CDDL is also available via the Internet at
9 # http://www.illumos.org/license/CDDL.
10 #

12 #
13 # Copyright 2016 Garrett D'Amore <garrett@damore.org>
14 #
15 # Copyright (c) 2018, Joyent, Inc.

17 UTSBASE      = ../..

19 MODULE       = sfxge
20 OBJECTS      = $(SFXGE_OBJS:%=$(OBJSDIR)/%)
21 OBJECTS      += $(SFXGE_SF_OBJS:%=$(OBJSDIR)/%)
22 LINTS        = $(SFXGE_OBJS:%.o=$(LINTSDIR)/%.ln)
23 LINTS        += $(SFXGE_SF_OBJS:%.o=$(LINTSDIR)/%.ln)
24 ROOTMODULE   = $(ROOTDRVDIR)/$(MODULE)

26 include $(UTSBASE)/intel/Makefile.intel

28 #
29 # Targets
30 #
31 ALL_TARGET   = $(BINARY)
32 LINT_TARGET  = $(MODULE).lint
33 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

35 #
36 # Overrides
37 #

39 INC_PATH += -I$(UTSBASE)/common/io/sfxge -I$(UTSBASE)/common/io/sfxge/common

41 #
42 # TODO:
43 # These are specific to this driver. We will undef these out later.
44 # Some of them need further cleanup as well (e.g. we shouldn't bother with
45 # supporting NDD directly.)
46 #
47 CPPFLAGS += -U_USE_MTU_UPDATE

49 CFLAGS += $(CCVERBOSE)

51 #
52 # Driver depends on GLDv3 (mac)
53 #
54 LDFLAGS      += -dy -N misc/mac

56 # needs work
57 $(OBJSDIR)/sfxge_ev.o := SMOFF += index_overflow
58 SMOFF += all_func_returns

60 #
61 # Default build targets.

```

```

62 #
63 .KEEP_STATE:

65 def:          $(DEF_DEPS)

67 all:          $(ALL_DEPS)

69 clean:        $(CLEAN_DEPS)

71 clobber:      $(CLOBBER_DEPS)

73 lint:         $(LINT_DEPS)

75 modlintlib:  $(MODLINTLIB_DEPS)

77 clean.lint:   $(CLEAN_LINT_DEPS)

79 install:      $(INSTALL_DEPS)

81 #
82 #          Include common targets.
83 #
84 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/smbfs/Makefile

1

```
*****
2416 Mon Jan 7 13:09:40 2019
new/usr/src/uts/intel/smbfs/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
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20 #
21 #
22 # uts/intel/smbfs/Makefile
23 #
24 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
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26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the smbfs (Server
28 # message block file system) kernel module.
29 #
30 # intel architecture dependent
31 #
32 #
33 # Path to the base of the uts directory tree (usually /usr/src/uts).
34 #
35 # Define the module and object file sets.
36 #
37 MODULE = smbfs
38 OBJECTS = ${SMBFS_OBJS:%=${OBJDIR}/%}
39 LINTS = ${SMBFS_OBJS:%.o=${LINTSDIR}/%.ln}
40 ROOTMODULE = ${USR_FS_DIR}/${MODULE}
41 #
42 #
43 # Include common rules.
44 #
45 include ${UTSBASE}/intel/Makefile.intel
46 #
47 #
48 # Define targets
49 #
50 ALL_TARGET = ${BINARY}
51 LINT_TARGET = ${MODULE}.lint
52 INSTALL_TARGET = ${BINARY} ${ROOTMODULE}
53 #
54 #
55 # Overrides.
56 #
```

new/usr/src/uts/intel/smbfs/Makefile

2

```
57 MODSTUBS_DIR = ${OBJDIR}
58 ${MODSTUBS_O} := AS_CPPFLAGS += -DSMBFS_MODULE
59 CLEANFILES += ${MODSTUBS_O}
60 INC_PATH += -I${UTSBASE}/common/fs/smbclnt
61 INC_PATH += -I${COMMONBASE}/smbclnt
62 LDFLAGS += -dy -Ndrv/nsmb
63 #
64 # Until CR 4994570 is fixed...
65 LINTTAGS += -erroff=E_BAD_FORMAT_ARG_TYPE2
66 CERRWARN += _gcc=-Wno-parentheses
67 CERRWARN += _gcc=-Wno-uninitialized
68 # needs work
69 SMOFF += all_func_returns,signed,deref_check
70 #
71 #
72 # The mb_put/md_get functions are intentionally used with and without
73 # return value checks, so filter those out like LGREP.2 does.
74 LGREP.nsm = egrep -v ' ignored: (mb_init|mb_put|md_get)'
75 LTAIL += 2>&l | ${LGREP.2} | ${LGREP.nsm} || true
76 #
77 #
78 # Default build targets.
79 #
80 .KEEP_STATE:
81 #
82 def: ${DEF_DEPS}
83 #
84 all: ${ALL_DEPS}
85 #
86 clean: ${CLEAN_DEPS}
87 #
88 clobber: ${CLOBBER_DEPS}
89 #
90 lint: ${LINT_DEPS}
91 #
92 modlintlib: ${MODLINTLIB_DEPS}
93 #
94 clean.lint: ${CLEAN_LINT_DEPS}
95 #
96 install: ${INSTALL_DEPS}
97 #
98 #
99 # Include common targets.
100 #
101 include ${UTSBASE}/intel/Makefile.targ
```

new/usr/src/uts/intel/smbios/Makefile

1

\*\*\*\*\*

1596 Mon Jan 7 13:09:40 2019

new/usr/src/uts/intel/smbios/Makefile

10077 usr/src/uts/ Makefile changes for smatch

\*\*\*\*\*

```
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20 # CDDL HEADER END
21 #
22 #
23 #
24 # Copyright 2005 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 #ident "%Z%M% %I% %E% SMI"
```

```
30 UTSBASE = ../../
```

```
32 MODULE = smbios
33 OBJECTS = $(OBJS_DIR)/smbios.o
34 LINTS = $(LINTS_DIR)/smbios.ln
35 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
36 CONF_SRCDIR = $(UTSBASE)/common/io
```

```
38 include $(UTSBASE)/intel/Makefile.intel
```

```
40 ALL_TARGET = $(BINARY) $(SRC_CONFILE)
41 LINT_TARGET = $(MODULE).lint
42 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
```

```
44 # needs work
45 SMOFF += signed_integer_overflow_check
```

```
47 .KEEP_STATE:
```

```
49 def: $(DEF_DEPS)
```

```
51 all: $(ALL_DEPS)
```

```
53 clean: $(CLEAN_DEPS)
```

```
55 clobber: $(CLOBBER_DEPS)
```

```
57 lint: $(LINT_DEPS)
```

```
59 modlintlib: $(MODLINTLIB_DEPS)
```

new/usr/src/uts/intel/smbios/Makefile

2

```
61 clean.lint: $(CLEAN_LINT_DEPS)
```

```
63 install: $(INSTALL_DEPS)
```

```
65 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/smsrv/Makefile

1

```
*****
2336 Mon Jan 7 13:09:40 2019
new/usr/src/uts/intel/smsrv/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
```

```
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25 # Copyright 2014 Nexenta Systems, Inc. All rights reserved.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the cifs server file system
31 # kernel module.
32 #

34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../../

39 #
40 # Define the module and object file sets.
41 #
42 MODULE = smsrv
43 OBJECTS = ${SMSRV_OBJS:%=${OBJSDIR}/%}
44 LINTS = ${SMSRV_OBJS:%.o=${LINTSDIR}/%.ln}
45 ROOTMODULE = ${USR_DRV_DIR}/${MODULE}
46 CONF_SRCDIR = ${UTSBASE}/common/fs/smsrv

48 #
49 # Include common rules.
50 #
51 include ${UTSBASE}/intel/Makefile.intel

53 #
54 # Module dependencies
55 #
56 #
57 LDFLAGS += -dy -Nfs/sockfs -Nmisc/ksocket -Ndrv/ip -Nstrmod/rpcmod -Nsys/doorfs
58 LDFLAGS += -Nmisc/kcf

60 #
61 # Define targets
```

new/usr/src/uts/intel/smsrv/Makefile

2

```
62 #
63 ALL_TARGET = $(BINARY)
64 LINT_TARGET = $(MODULE).lint
65 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTLINK) $(ROOT_CONFFILE)

67 #
68 # Overrides.
69 #
70 MODSTUBS_DIR = $(OBJSDIR)
71 CLEANFILES += $(MODSTUBS_O)

73 INC_PATH += -I$(SRC)/common

75 CERRWARN += -_gcc=-Wno-parentheses
76 CERRWARN += -_gcc=-Wno-switch

78 # needs work
79 SMATCH=off

81 #
82 # Default build targets.
83 #
84 .KEEP_STATE:

86 def: $(DEF_DEPS)

88 all: $(ALL_DEPS)

90 clean: $(CLEAN_DEPS)

92 clobber: $(CLOBBER_DEPS)

94 lint: $(LINT_DEPS)

96 modlintlib: $(MODLINTLIB_DEPS)

98 clean.lint: $(CLEAN_LINT_DEPS)

100 install: $(INSTALL_DEPS)

102 $(ROOTLINK): $(ROOT_SYS_DIR) $(ROOTMODULE)
103 -$(RM) $@; ln $(ROOTMODULE) $@

105 #
106 # Include common targets.
107 #
108 include ${UTSBASE}/intel/Makefile.targ
```

new/usr/src/uts/intel/sockfs/Makefile

1

```
*****
3144 Mon Jan 7 13:09:40 2019
new/usr/src/uts/intel/sockfs/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/sockfs/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the sockfs file system
31 # kernel module.
32 #
33 # intel implementation architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTSBASE = ../../

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = sockfs
45 OBJECTS = $(SOCK_OBJS:%=$(OBJS_DIR)/%)
46 LINTS = $(SOCK_OBJS:%.o=$(LINTS_DIR)/%.ln)
47 ROOTMODULE = $(ROOT_FS_DIR)/$(MODULE)

49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

61 #
```

new/usr/src/uts/intel/sockfs/Makefile

2

```
62 # Overrides.
63 #
64 MODSTUBS_DIR = $(OBJS_DIR)
65 $(MODSTUBS_O) := AS_CPPFLAGS += -DSOCK_MODULE
66 CLEANFILES += $(MODSTUBS_O)
67 LDFLAGS += -dy -Ndrv/ip

69 #
70 # Derived file "nl7ctokgen.h" defines.
71 #
72 SRCDIR = $(UTSBASE)/common/fs/sockfs
73 TOKGEN = $(SRCDIR)/nl7ctokgen
74 DERIVED_FILES = nl7ctokgen.h
75 CFLAGS += -I.
76 LINTFLAGS += -I.

78 #
79 # For now, disable these lint checks; maintainers should endeavor
80 # to investigate and remove these for maximum lint coverage.
81 # Please do not carry these forward to new Makefiles.
82 #
83 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
84 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
85 LINTTAGS += -erroff=E_STATIC_UNUSED
86 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
87 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
88 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

90 CERRWARN += -_gcc=-Wno-unused-label
91 CERRWARN += -_gcc=-Wno-unused-value
92 CERRWARN += -_gcc=-Wno-unused-variable
93 CERRWARN += -_gcc=-Wno-unused-function
94 CERRWARN += -_gcc=-Wno-parentheses
95 CERRWARN += -_gcc=-Wno-uninitialized

97 # needs work
98 SMATCH=off

100 #
101 # Default build targets.
102 #
103 .KEEP_STATE:

105 def: $(DEF_DEPS)

107 all: $(ALL_DEPS)

109 clean: $(CLEAN_DEPS)
110 $(RM) $(DERIVED_FILES)

112 clobber: $(CLOBBER_DEPS)
113 $(RM) $(DERIVED_FILES)

115 lint: $(LINT_DEPS)

117 modlintlib: $(MODLINTLIB_DEPS)

119 clean.lint: $(CLEAN_LINT_DEPS)

121 install: $(INSTALL_DEPS)

123 #
124 # Include common targets.
125 #
126 include $(UTSBASE)/intel/Makefile.targ
```

**new/usr/src/uts/intel/sockfs/Makefile**

**3**

```
128 #
129 #     Derived file gen.
130 #
131 $(SRCDIR)/nl7chttp.c: nl7ctokgen.h

133 nl7ctokgen.h: $(TOKGEN) $(SRCDIR)/nl7ctokreq.txt $(SRCDIR)/nl7ctokres.txt
134     /bin/ksh $(TOKGEN) $(SRCDIR)/nl7ctokreq.txt $(SRCDIR)/nl7ctokres.txt >${@}
```

new/usr/src/uts/intel/sockpfp/Makefile

1

```
*****
2305 Mon Jan 7 13:09:41 2019
new/usr/src/uts/intel/sockpfp/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
26 # This makefile drives the production of the nca driver
27 # kernel module.
28 # intel architecture dependent
29 #
30 #
31 # Path to the base of the uts directory tree (usually /usr/src/uts).
32 #
33 # Define the module and object file sets.
34 #
35 MODULE = sockpfp
36 OBJECTS = $(PFP_SOCK_MOD_OBJS:%=$(OBJS_DIR)/%)
37 LINTS = $(PFP_SOCK_MOD_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(USR_SOCK_DIR)/$(MODULE)
39 #
40 #
41 # Include common rules.
42 #
43 include $(UTSBASE)/intel/Makefile.intel
44 #
45 # Define targets
46 #
47 #
48 ALL_TARGET = $(BINARY)
49 LINT_TARGET = $(MODULE).lint
50 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
51 #
52 #
53 # lint pass one enforcement and OS version
54 #
55 CFLAGS += $(CCVERBOSE)
```

new/usr/src/uts/intel/sockpfp/Makefile

2

```
57 LDFLAGS += -dy -Nfs/sockfs -Nmisc/dls -Nmisc/mac -Ndrv/bpf -Ndrv/ip
58 INC_PATH += -I$(UTSBASE)/common/inet/sockmods -I$(UTSBASE)/common/io/bpf
59 #
60 #
61 # For now, disable these lint checks; maintainers should endeavor
62 # to investigate and remove these for maximum lint coverage.
63 # Please do not carry these forward to new Makefiles.
64 #
65 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW -erroff=E_BAD_PTR_CAST_ALIGN
66 #
67 CERRWARN += -_gcc=-Wno-uninitialized
68 CERRWARN += -_gcc=-Wno-unused-label
69 #
70 # needs work
71 SMOFF += all_func_returns
72 #
73 #
74 # Default build targets.
75 #
76 .KEEP_STATE:
77 #
78 def: $(DEF_DEPS)
79 #
80 all: $(ALL_DEPS)
81 #
82 clean: $(CLEAN_DEPS)
83 #
84 clobber: $(CLOBBER_DEPS)
85 #
86 lint: $(LINT_DEPS)
87 #
88 modlintlib: $(MODLINTLIB_DEPS)
89 #
90 clean.lint: $(CLEAN_LINT_DEPS)
91 #
92 install: $(INSTALL_DEPS)
93 #
94 #
95 # Include common targets.
96 #
97 include $(UTSBASE)/intel/Makefile.targ
```



new/usr/src/uts/intel/sol\_ofs/Makefile

1

```
*****
2289 Mon Jan 7 13:09:41 2019
new/usr/src/uts/intel/sol_ofs/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
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19 # CDDL HEADER END
20 #
21 #
22 # Copyright (c) 2010, Oracle and/or its affiliates. All rights reserved.
23 #
24 # Copyright (c) 2018, Joyent, Inc.
25 #

27 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTSBASE = ../../

32 #
33 # Define the module and object file sets.
34 #
35 MODULE = sol_ofs
36 OBJECTS = $(SOL_OFS_OBJS:%=$(OBJS_DIR)/%)
37 LINTS = $(SOL_OFS_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
39 LDFFLAGS += -dy -Nmisc/ibt1 -Nmisc/ibcm

41 #
42 # Include common rules.
43 #
44 include $(UTSBASE)/intel/Makefile.intel

46 #
47 # Define targets
48 #
49 ALL_TARGET = $(BINARY)
50 LINT_TARGET = $(MODULE).lint
51 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

53 #
54 # For now, disable these lint checks; maintainers should endeavor
55 # to investigate and remove these for maximum lint coverage.
56 # Please do not carry these forward to new Makefiles.
57 #
58 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
59 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
60 LINTTAGS += -erroff=E_STATIC_UNUSED
```

new/usr/src/uts/intel/sol\_ofs/Makefile

2

```
61 LINTTAGS += -erroff=E_CONST_TRUNCATED_BY_ASSIGN
63 CERRWARN += -_gcc=-Wno-uninitialized
64 CERRWARN += -_gcc=-Wno-parentheses
65 CERRWARN += -_gcc=-Wno-type-limits
66 CERRWARN += -_gcc=-Wno-unused-variable

68 # needs work
69 $(OBJS_DIR)/sol_cma.o := SMOFF += deref_check

71 #
72 # Default build targets.
73 #
74 .KEEP_STATE:

76 def: $(DEF_DEPS)

78 all: $(ALL_DEPS)

80 clean: $(CLEAN_DEPS)

82 clobber: $(CLOBBER_DEPS)

84 lint: $(LINT_DEPS)

86 modlintlib: $(MODLINTLIB_DEPS)

88 clean.lint: $(CLEAN_LINT_DEPS)

90 install: $(INSTALL_DEPS)

92 #
93 # Include common targets.
94 #
95 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/sol\_ucma/Makefile

1

```
*****
2276 Mon Jan 7 13:09:41 2019
new/usr/src/uts/intel/sol_ucma/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
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19 # CDDL HEADER END
20 #
21 #
22 # Copyright (c) 2010, Oracle and/or its affiliates. All rights reserved.
23 #
24 # Copyright (c) 2018, Joyent, Inc.
24 #

26 #
27 # Path to the base of the uts directory tree (usually /usr/src/uts).
28 #
29 UTBASE = ../../

31 #
32 # Define the module and object file sets.
33 #
34 MODULE = sol_ucma
35 OBJECTS = $(SOL_UCMA_OBJS:%=$(OBJDIR)/%)
36 LINTS = $(SOL_UCMA_OBJS:%.o=$(LINTDIR)/%.ln)
37 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
38 LDPLAGS += -dy -Nmisc/sol_ofs
39 CONF_SRCDIR = $(UTBASE)/common/io/ib/clients/of/sol_ucma

41 #
42 # Include common rules.
43 #
44 include $(UTBASE)/intel/Makefile.intel

46 #
47 # Define targets
48 #
49 ALL_TARGET = $(BINARY) $(SRC_CONFILE)
50 LINT_TARGET = $(MODULE).lint
51 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFILE)

53 #
54 # path for header files
55 #
56 #
57 INCLUDE_PATH += -I$(UTBASE)/common/sys/ib/clients/ofv/sol_ucma

59 # For now, disable these lint checks; maintainers should endeavor
60 # to investigate and remove these for maximum lint coverage.
```

new/usr/src/uts/intel/sol\_ucma/Makefile

2

```
61 # Please do not carry these forward to new Makefiles.
62 #
63 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
64 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
65 LINTTAGS += -erroff=E_STATIC_UNUSED

67 # needs work
68 SMOFF += all_func_returns,logical_instead_of_bitwise

70 #
71 # Default build targets.
72 #
73 .KEEP_STATE:

75 def: $(DEF_DEPS)

77 all: $(ALL_DEPS)

79 clean: $(CLEAN_DEPS)

81 clobber: $(CLOBBER_DEPS)

83 lint: $(LINT_DEPS)

85 modlintlib: $(MODLINTLIB_DEPS)

87 clean.lint: $(CLEAN_LINT_DEPS)

89 install: $(INSTALL_DEPS)

91 #
92 # Include common targets.
93 #
94 include $(UTBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/sol\_uverbs/Makefile

1

```
*****
2349 Mon Jan 7 13:09:43 2019
new/usr/src/uts/intel/sol_uverbs/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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11 # and limitations under the License.
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright (c) 2010, Oracle and/or its affiliates. All rights reserved.
23 #
24 # Copyright (c) 2018, Joyent, Inc.
25 #
26 #
27 # Path to the base of the uts directory tree (usually /usr/src/uts).
28 #
29 UTSBASE = ../../
30 #
31 #
32 # Define the module and object file sets.
33 #
34 MODULE = sol_uverbs
35 OBJECTS = $(SOL_UVERBS_OBJS:%=$(OBJS_DIR)/%)
36 LINTS = $(SOL_UVERBS_OBJS:%.o=$(LINTS_DIR)/%.ln)
37 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
38 LDFLAGS += -dy -Nmisc/ibtl -Nmisc/sol_ofs
39 CONF_SRCDIR = $(UTSBASE)/common/io/ib/clients/of/sol_uverbs
40 #
41 #
42 # Include common rules.
43 #
44 include $(UTSBASE)/intel/Makefile.intel
45 #
46 #
47 # Define targets
48 #
49 ALL_TARGET = $(BINARY) $(SRC_CONFILE)
50 LINT_TARGET = $(MODULE).lint
51 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
52 #
53 #
54 # For now, disable these lint checks; maintainers should endeavor
55 # to investigate and remove these for maximum lint coverage.
56 # Please do not carry these forward to new Makefiles.
57 #
58 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
59 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
60 LINTTAGS += -erroff=E_STATIC_UNUSED
```

new/usr/src/uts/intel/sol\_uverbs/Makefile

2

```
61 LINTTAGS += -erroff=E_CONST_TRUNCATED_BY_ASSIGN
62 #
63 CERRWARN += -_gcc=-Wno-type-limits
64 CERRWARN += -_gcc=-Wno-switch
65 CERRWARN += -_gcc=-Wno-unused-label
66 #
67 # really broken
68 SMOFF += logical_instead_of_bitwise,or_vs_and
69 #
70 #
71 # Default build targets.
72 #
73 .KEEP_STATE:
74 #
75 def: $(DEF_DEPS)
76 #
77 all: $(ALL_DEPS)
78 #
79 clean: $(CLEAN_DEPS)
80 #
81 clobber: $(CLOBBER_DEPS)
82 #
83 lint: $(LINT_DEPS)
84 #
85 modlintlib: $(MODLINTLIB_DEPS)
86 #
87 clean.lint: $(CLEAN_LINT_DEPS)
88 #
89 install: $(INSTALL_DEPS)
90 #
91 #
92 # Include common targets.
93 #
94 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/spdsock/Makefile

1

```
*****
2433 Mon Jan 7 13:09:44 2019
new/usr/src/uts/intel/spdsock/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/spdsock/Makefile
23 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the spdsock driver
28 # kernel module.
29 # intel implementation architecture dependent
30 #
31 #
32 #
33 # Path to the base of the uts directory tree (usually /usr/src/uts).
34 #
35 #
36 # UTSBASE = ../../
37 #
38 # Define the module and object file sets.
39 #
40 #
41 #
42 #
43 #
44 #
45 # include $(UTSBASE)/intel/Makefile.intel
46 #
47 #
48 # Define targets
49 #
50 #
51 #
52 #
53 #
54 #
55 # Linkage dependencies
56 #
```

new/usr/src/uts/intel/spdsock/Makefile

2

```
57 LDFLAGS += -dy -Ndrv/ip
58 #
59 INC_PATH += -I$(UTSBASE)/common/io/bpf
60 #
61 #
62 # For now, disable these lint checks; maintainers should endeavor
63 # to investigate and remove these for maximum lint coverage.
64 # Please do not carry these forward to new Makefiles.
65 #
66 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
67 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
68 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
69 #
70 CERRWARN += -_gcc=-Wno-uninitialized
71 #
72 # needs work
73 $(OBJS_DIR)/spdsockddi.o := SMOFF += index_overflow
74 $(OBJS_DIR)/spdsock.o := SMOFF += no_if_block
75 #
76 #
77 # Default build targets.
78 #
79 .KEEP_STATE:
80 #
81 def: $(DEF_DEPS)
82 #
83 all: $(ALL_DEPS) $(SISCHECK_DEPS)
84 #
85 clean: $(CLEAN_DEPS) $(SISCLEAN_DEPS)
86 #
87 clobber: $(CLOBBER_DEPS) $(SISCLEAN_DEPS)
88 #
89 lint: $(LINT_DEPS)
90 #
91 modlintlib: $(MODLINTLIB_DEPS)
92 #
93 clean.lint: $(CLEAN_LINT_DEPS)
94 #
95 install: $(INSTALL_DEPS) $(SISCHECK_DEPS)
96 #
97 #
98 # Include common targets.
99 #
100 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2407 Mon Jan 7 13:09:44 2019
new/usr/src/uts/intel/specfs/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/specfs/Makefile
23 #
24 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the specfs file system
31 # kernel module.
32 #
33 # intel architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTBASE = ../..

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = specfs
45 OBJECTS = $(SPEC_OBJS:%=$(OBJS_DIR)/%)
46 LINTS = $(SPEC_OBJS:%.o=$(LINTS_DIR)/%.ln)
47 ROOTMODULE = $(ROOT_FS_DIR)/$(MODULE)

49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

61 #

```

```

62 # Override defaults to build a unique, local modstubs.o.
63 #
64 MODSTUBS_DIR = $(OBJS_DIR)
65 $(MODSTUBS_O) := AS_CPPFLAGS += -DSPEC_MODULE
66 CLEANFILES += $(MODSTUBS_O)
67 LDFLAGS += -dy -Nfs/fifofs

69 #
70 # For now, disable these lint checks; maintainers should endeavor
71 # to investigate and remove these for maximum lint coverage.
72 # Please do not carry these forward to new Makefiles.
73 #
74 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON

76 CERRWARN += -_gcc=-Wno-parentheses
77 CERRWARN += -_gcc=-Wno-uninitialized

79 # needs work
80 $(OBJS_DIR)/specvnops.o := SMOFF += signed

82 #
83 # Default build targets.
84 #
85 .KEEP_STATE:

87 def: $(DEF_DEPS)

89 all: $(ALL_DEPS)

91 clean: $(CLEAN_DEPS)

93 clobber: $(CLOBBER_DEPS)

95 lint: $(LINT_DEPS)

97 modlintlib: $(MODLINTLIB_DEPS)

99 clean.lint: $(CLEAN_LINT_DEPS)

101 install: $(INSTALL_DEPS)

103 #
104 # Include common targets.
105 #
106 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/spppcomp/Makefile

1

```
*****
2653 Mon Jan 7 13:09:44 2019
new/usr/src/uts/intel/spppcomp/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/spppcomp/Makefile
23 #
24 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 # Copyright (c) 2011 Bayard G. Bell. All rights reserved.
27 #
28 # Copyright (c) 2018, Joyent, Inc.
29 # This makefile drives the production of the spppcomp STREAMS
30 # kernel module.
31 # intel architecture dependent
32 #
33 #
34 #
35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../
39 #
40 # Define the module and object file sets.
41 #
42 #
43 # MODULE = spppcomp
44 # OBJECTS = $(SPPPCOMP_OBJS:%=$(OBJS_DIR)/%)
45 # LINTS = $(SPPPCOMP_OBJS:%.o=$(LINTS_DIR)/%.ln)
46 # ROOTMODULE = $(USR_STRMOD_DIR)/$(MODULE)
47 #
48 #
49 # Include common rules.
50 #
51 include $(UTSBASE)/intel/Makefile.intel
52 #
53 # Define targets
54 #
55 # ALL_TARGET = $(BINARY)
56 # LINT_TARGET = $(MODULE).lint
57 # INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
58 #
59 #
60 # Internal build definitions
```

new/usr/src/uts/intel/spppcomp/Makefile

2

```
57 #
58 CPPFLAGS += -DINTERNAL_BUILD -DSOL2 -DMUX_FRAME
59 #
60 # Additional compiler definitions
61 #
62 #
63 INC_PATH += -I$(UTSBASE)/common/io/ppp/common
64 #
65 #
66 # For now, disable these lint checks; maintainers should endeavor
67 # to investigate and remove these for maximum lint coverage.
68 # Please do not carry these forward to new Makefiles.
69 #
70 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
71 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
72 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
73 #
74 CERRWARN += -_gcc=-Wno-parentheses
75 CERRWARN += -_gcc=-Wno-uninitialized
76 #
77 # needs work
78 SMOFF += indenting
79 #
80 #
81 # Depends on sppp
82 #
83 LDFLAGS += -dy -N drv/sppp
84 #
85 #
86 # Default build targets.
87 #
88 .KEEP_STATE:
89 #
90 def: $(DEF_DEPS)
91 #
92 all: $(ALL_DEPS)
93 #
94 clean: $(CLEAN_DEPS)
95 #
96 clobber: $(CLOBBER_DEPS)
97 #
98 lint: $(LINT_DEPS)
99 #
100 modlintlib: $(MODLINTLIB_DEPS)
101 #
102 clean.lint: $(CLEAN_LINT_DEPS)
103 #
104 install: $(INSTALL_DEPS)
105 #
106 $(LINTS_DIR)/%.ln: $(UTSBASE)/common/io/ppp/spppcomp/%.c
107 @($(LHEAD) $(LINT.c) $< $(LTAIL))
108 #
109 $(OBJS_DIR)/%.o: $(UTSBASE)/common/io/ppp/spppcomp/%.c
110 $(COMPILE.c) -o $@ $<
111 $(CTFCONVERT_O)
112 #
113 #
114 # Include common targets.
115 #
116 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2023 Mon Jan 7 13:09:45 2019
new/usr/src/uts/intel/srpt/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright (c) 2009, 2010, Oracle and/or its affiliates. All rights reserved.
23 #
24 # Copyright (c) 2018, Joyent, Inc.
25 # This makefile drives the production of the srpt pseudo-driver for
    COMSTAR.

26 #
27 # Path to the base of the uts directory tree (usually /usr/src/uts).
28 #

31 UTSBASE = ../..

33 ARCHDIR:sh = cd ../; basename `pwd`

35 #
36 # Define the module and object file sets.
37 #
38 MODULE = srpt
39 OBJECTS = $(SRPT_OBJS:%=$(OBJS_DIR)/%)
40 LINTS = $(SRPT_OBJS:%.o=$(LINTS_DIR)/%.ln)
41 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
42 CONF_SRCDIR = $(UTSBASE)/common/io/comstar/port/srpt

44 #
45 # Include common rules.
46 #
47 include ../Makefile.$(ARCHDIR)

49 #
50 # Define targets
51 #
52 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
53 LINT_TARGET = $(MODULE).lint
54 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

56 #
57 # Overrides and depends_on
58 #
59 MODSTUBS_DIR = $(OBJS_DIR)

```

```

60 LDFLAGS += -dy -Ndrv/stmf -Nmisc/ibt1 -Nmisc/ibcm
62 C99LMODE= -Xc99=%all
64 CERRWARN += -_gcc=-Wno-unused-label

66 # needs work
67 $(OBJS_DIR)/srpt_stp.o := SMOFF += all_func_returns

69 #
70 # Default build targets.
71 #
72 .KEEP_STATE:

74 def: $(DEF_DEPS)

76 all: $(ALL_DEPS)

78 clean: $(CLEAN_DEPS)

80 clobber: $(CLOBBER_DEPS)

82 lint: $(LINT_DEPS)

84 modlintlib: $(MODLINTLIB_DEPS)

86 clean.lint: $(CLEAN_LINT_DEPS)

88 install: $(INSTALL_DEPS)

90 #
91 # Include common targets.
92 #
93 include ../Makefile.targ

```

new/usr/src/uts/intel/st/Makefile

1

```
*****
2418 Mon Jan 7 13:09:45 2019
new/usr/src/uts/intel/st/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/st/Makefile
23 # Copyright 2007 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 # Copyright (c) 2011 Bayard G. Bell. All rights reserved.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the st driver
28 # kernel module.
29 #
30 # intel implementation architecture independent
31 #
29 #
30 # Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 UTSBASE = ../../

34 #
35 # Define the module and object file sets.
36 #
37 MODULE = st
38 OBJECTS = $(ST_OBJS:%=$(OBJDIR)/%)
39 LINTS = $(ST_OBJS:%.o=$(LINTSDIR)/%.ln)
40 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
41 CONF_SRCDIR = $(UTSBASE)/intel/io/scsi/targets

43 #
44 # Include common rules.
45 #
46 include $(UTSBASE)/intel/Makefile.intel

48 #
49 # Define targets
50 #
51 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
52 LINT_TARGET = $(MODULE).lint
53 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

55 #
56 # Overrides.
```

new/usr/src/uts/intel/st/Makefile

2

```
57 #
58 DEBUG_FLGS =
59 DEBUG_DEFS += $(DEBUG_FLGS)

61 #
62 # For now, disable these lint checks; maintainers should endeavor
63 # to investigate and remove these for maximum lint coverage.
64 # Please do not carry these forward to new Makefiles.
65 #
66 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
67 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
68 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

70 CERRWARN += -_gcc=-Wno-parentheses
71 CERRWARN += -_gcc=-Wno-switch
72 CERRWARN += -_gcc=-Wno-uninitialized

74 # needs work
75 SMATCH=off

77 #
78 # Depends on scsi
79 #
80 LDFFLAGS += -dy -N misc/scsi

82 #
83 # Default build targets.
84 #
85 .KEEP_STATE:

87 def: $(DEF_DEPS)

89 all: $(ALL_DEPS)

91 clean: $(CLEAN_DEPS)

93 clobber: $(CLOBBER_DEPS)

95 lint: $(LINT_DEPS)

97 modlintlib: $(MODLINTLIB_DEPS)

99 clean.lint: $(CLEAN_LINT_DEPS)

101 install: $(INSTALL_DEPS)

103 #
104 # Include common targets.
105 #
106 include $(UTSBASE)/intel/Makefile.targ
```



```

*****
2247 Mon Jan 7 13:09:46 2019
new/usr/src/uts/intel/stmf/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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7 #
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9 # or http://www.opensolaris.org/os/licensing.
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15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright (c) 2008, 2010, Oracle and/or its affiliates. All rights reserved.
23 #
24 # Copyright (c) 2018, Joyent, Inc.
25 # This makefile drives the production of the stmf driver for
    COMSTAR.

26 #
27 # Path to the base of the uts directory tree (usually /usr/src/uts).
28 #
29 UTSBASE = ../../

31 ARCHDIR:sh = cd ../; basename `pwd`

33 #
34 # Define the module and object file sets.
35 #
36 MODULE = stmf
37 OBJECTS = $(STMF_OBJS:%=$(OBJDIR)/%)
38 LINTS = $(STMF_OBJS:%.o=$(LINTS_DIR)/%.ln)
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
40 CONF_SRCDIR = $(UTSBASE)/common/io/comstar/stmf

42 #
43 # Include common rules.
44 #
45 include ../Makefile.$(ARCHDIR)

47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY) $(SRC_CONFILE)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

54 #
55 # Overrides and depends_on
56 #
57 MODSTUBS_DIR = $(OBJDIR)
58 CERRWARN += -erroff=E_STATEMENT_NOT_REACHED

```

```

60 # needs work
61 SMATCH=off

63 C99LMODE= -Xc99=%all

65 #
66 # For now, disable these lint checks; maintainers should endeavor
67 # to investigate and remove these for maximum lint coverage.
68 #
69 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
70 LINTTAGS += -erroff=E_STATIC_UNUSED

72 CERRWARN += -_gcc=-Wno-uninitialized
73 CERRWARN += -_gcc=-Wno-switch
74 CERRWARN += -_gcc=-Wno-unused-variable

76 #
77 # Default build targets.
78 #
79 .KEEP_STATE:

81 def: $(DEF_DEPS)

83 all: $(ALL_DEPS)

85 clean: $(CLEAN_DEPS)

87 clobber: $(CLOBBER_DEPS)

89 lint: $(LINT_DEPS)

91 modlintlib: $(MODLINTLIB_DEPS)

93 clean.lint: $(CLEAN_LINT_DEPS)

95 install: $(INSTALL_DEPS)

97 #
98 # Include common targets.
99 #
100 include ../Makefile.targ

```

new/usr/src/uts/intel/strategy/Makefile

1

```
*****
2048 Mon Jan 7 13:09:46 2019
new/usr/src/uts/intel/strategy/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License, Version 1.0 only
6 # (the "License"). You may not use this file except in compliance
7 # with the License.
8 #
9 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
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17 # fields enclosed by brackets "[]" replaced with your own identifying
18 # information: Portions Copyright [yyyy] [name of copyright owner]
19 #
20 # CDDL HEADER END
21 #
22 #
23 # uts/intel/strategy/Makefile
24 #
25 # Copyright 2005 Sun Microsystems, Inc. All rights reserved.
26 # Use is subject to license terms.
27 #
28 # Copyright (c) 2018, Joyent, Inc.

30 #
31 # This makefile drives the production of the strategy "misc"
32 # kernel module.
33 #
34 # intel architecture dependent
35 #

37 #
38 # Path to the base of the uts directory tree (usually /usr/src/uts).
39 #
40 UTSBASE = ../../

42 #
43 # Define the module and object file sets.
44 #
45 MODULE = strategy
46 OBJECTS = $(STRATEGY_OBJS:%=$(OBJS_DIR)/%)
47 LINTS = $(STRATEGY_OBJS:%.o=$(LINTS_DIR)/%.ln)
48 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)

50 #
51 # Include common rules.
52 #
53 include $(UTSBASE)/intel/Makefile.intel

55 CERRWARN += -_gcc=-Wno-parentheses

57 # needs work
58 $(OBJS_DIR)/strategy.o := SMOFF += all_func_returns

60 #
61 # Define targets
```

new/usr/src/uts/intel/strategy/Makefile

2

```
62 #
63 ALL_TARGET = $(BINARY)
64 LINT_TARGET = $(MODULE).lint
65 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

67 #
68 # Overrides.
69 #
70 DEBUG_FLGS =
71 DEBUG_DEFS += $(DEBUG_FLGS)

73 #
74 # Default build targets.
75 #
76 .KEEP_STATE:

78 def: $(DEF_DEPS)

80 all: $(ALL_DEPS)

82 clean: $(CLEAN_DEPS)

84 clobber: $(CLOBBER_DEPS)

86 lint: $(LINT_DEPS)

88 modlintlib: $(MODLINTLIB_DEPS)

90 clean.lint: $(CLEAN_LINT_DEPS)

92 install: $(INSTALL_DEPS)

94 #
95 # Include common targets.
96 #
97 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/tavor/Makefile

1

```
*****
2505 Mon Jan 7 13:09:47 2019
new/usr/src/uts/intel/tavor/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the tavor InfiniBand module
28 # intel architecture dependent
29 #
30 # uts/intel/tavor/Makefile
31 #
32 #
33 #
34 # Define the module and object file sets.
35 #
36 MODULE = tavor
37 OBJECTS = $(TAVOR_OBJS:%=$(OBJS_DIR)/%)
38 LINTS = $(TAVOR_OBJS:%.o=$(LINTS_DIR)/%.ln)
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
40 CONF_SRCDIR = $(UTSBASE)/common/io/ib/adapters/tavor
41 LDFLAGS += -dy -Ndrv/ib -Nmisc/ibt1 -Nmisc/ibmf
42 #
43 # Include common rules.
44 #
45 #
46 include $(UTSBASE)/intel/Makefile.intel
47 #
48 # Define targets
49 #
50 #
51 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
52 LINT_TARGET = $(MODULE).lint
53 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
54 #
55 #
```

new/usr/src/uts/intel/tavor/Makefile

2

```
56 # Overrides
57 #
58 #
59 #
60 # Build with no debug TNF probes enabled (by default)
61 #
62 #DEBUG_DEFS_DBG32 += -DTNF_DEBUG
63 #DEBUG_DEFS_DBG64 += -DTNF_DEBUG
64 #
65 #
66 # lint pass one enforcement
67 #
68 CFLAGS += $(CCVERBOSE)
69 #
70 #
71 # For now, disable these lint checks; maintainers should endeavor
72 # to investigate and remove these for maximum lint coverage.
73 # Please do not carry these forward to new Makefiles.
74 #
75 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
76 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
77 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
78 #
79 CERRWARN += -_gcc=-Wno-unused-value
80 CERRWARN += -_gcc=-Wno-unused-label
81 CERRWARN += -_gcc=-Wno-uninitialized
82 CERRWARN += -_gcc=-Wno-type-limits
83 #
84 # needs work
85 SMATCH=off
86 #
87 #
88 # Default build targets.
89 #
90 .KEEP_STATE:
91 #
92 def: $(DEF_DEPS)
93 #
94 all: $(ALL_DEPS)
95 #
96 clean: $(CLEAN_DEPS)
97 #
98 clobber: $(CLOBBER_DEPS)
99 #
100 lint: $(LINT_DEPS)
101 #
102 modlintlib: $(MODLINTLIB_DEPS)
103 #
104 clean.lint: $(CLEAN_LINT_DEPS)
105 #
106 install: $(INSTALL_DEPS)
107 #
108 #
109 # Include common targets.
110 #
111 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/tcp/Makefile

1

```
*****
2502 Mon Jan 7 13:09:47 2019
new/usr/src/uts/intel/tcp/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/tcp/Makefile
23 #
24 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 #
28 # This makefile drives the production of the tcp driver kernel module.
29 #
30 # intel implementation architecture dependent
31 #
32 #
33 #
34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../..
38 #
39 # Define the module and object file sets.
40 #
41 MODULE = tcp
42 OBJECTS = $(TCP_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(TCP_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
45 ROOTLINK = $(ROOT_STRMOD_DIR)/$(MODULE) $(ROOT SOCK_DIR)/$(MODULE)
46 CONF_SRCDIR = $(UTSBASE)/common/inet/tcp
47 #
48 # Extra for $(MODULE).check target
49 #
50 # Need to remove ipddi.o since it has non-static defines for _init etc.
51 IP_CHECK_OBJS = $(IP_OBJS:ipddi.o=ip.o)
52 EXTRA_CHECK_OBJS = $(IP_CHECK_OBJS:%=../ip/$(OBJS_DIR)/%)
53 #
54 # Include common rules.
55 #
56 include $(UTSBASE)/intel/Makefile.intel
57 #
```

new/usr/src/uts/intel/tcp/Makefile

2

```
57 # Define targets
58 #
59 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
60 LINT_TARGET = $(MODULE).lint
61 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTLINK) $(ROOT_CONFFILE)
62 #
63 #
64 # Inline functions, this probably needs to be changed when compiling with
65 # -xO4.
66 #
67 CINLINES = -xinline=tcp_set_ws_value,tcp_fill_header
68 #
69 CFLAGS += $(CINLINES)
70 #
71 #
72 # depends on ip, md5 and sockfs
73 #
74 LDFLAGS += -dy -Ndrv/ip -Ncrypto/md5 -Nfs/sockfs
75 #
76 # needs work
77 $(OBJS_DIR)/tcpddi.o := SMOFF += index_overflow
78 #
79 #
80 # Default build targets.
81 #
82 .KEEP_STATE:
83 #
84 def: $(DEF_DEPS)
85 #
86 all: $(ALL_DEPS)
87 #
88 clean: $(CLEAN_DEPS)
89 #
90 clobber: $(CLOBBER_DEPS)
91 #
92 lint: $(LINT_DEPS)
93 #
94 modlintlib: $(MODLINTLIB_DEPS)
95 #
96 clean.lint: $(CLEAN_LINT_DEPS)
97 #
98 install: $(INSTALL_DEPS)
99 #
100 $(ROOTLINK): $(ROOT_STRMOD_DIR) $(ROOT SOCK_DIR) $(ROOTMODULE)
101 -$(RM) $@; ln $(ROOTMODULE) $@
102 #
103 #
104 # Include common targets.
105 #
106 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/tcp6/Makefile

1

```
*****
2126 Mon Jan 7 13:09:47 2019
new/usr/src/uts/intel/tcp6/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 # information: Portions Copyright [yyyy] [name of copyright owner]
19 #
20 # CDDL HEADER END
21 #
22 #
23 # Copyright 2004 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
26 # ident "%Z%M% %I% %E% SMI"
27 #
28 # This makefile drives the production of the tcp6 driver kernel module.
29 #
30 # intel implementation architecture dependent
31 #

28 #
29 # Path to the base of the uts directory tree (usually /usr/src/uts).
30 #
31 UTSBASE = ../../

33 #
34 # Define the module and object file sets.
35 #
36 MODULE = tcp6
37 OBJECTS = $(TCP6_OBJS:%=$(OBJS_DIR)/%)
38 LINTS = $(TCP6_OBJS:%.o=$(LINTS_DIR)/%.ln)
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
40 CONF_SRCDIR = $(UTSBASE)/common/inet/tcp

42 #
43 # Extra for $(MODULE).check target
44 #
45 # Need to remove ipddi.o since it has non-static defines for _init etc.
46 IP_CHECK_OBJS = $(IP_OBJS:ipddi.o=ip.o)
47 EXTRA_CHECK_OBJS = $(IP_CHECK_OBJS:%=../ip/$(OBJS_DIR)/%)

49 # needs work
50 SMOFF += index_overflow

52 #
53 # Include common rules.
54 #
55 include $(UTSBASE)/intel/Makefile.intel
```

new/usr/src/uts/intel/tcp6/Makefile

2

```
57 #
58 # Define targets
59 #
60 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
61 LINT_TARGET = $(MODULE).lint
62 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

64 #
65 # depends on tcp ip and ip6
66 #
67 LDFLAGS += -dy -Ndrv/tcp -Ndrv/ip -Ndrv/ip6

69 #
70 # Default build targets.
71 #
72 .KEEP_STATE:

74 def: $(DEF_DEPS)

76 all: $(ALL_DEPS)

78 clean: $(CLEAN_DEPS)

80 clobber: $(CLOBBER_DEPS)

82 lint: $(LINT_DEPS)

84 modlintlib: $(MODLINTLIB_DEPS)

86 clean.lint: $(CLEAN_LINT_DEPS)

88 install: $(INSTALL_DEPS)

90 #
91 # Include common targets.
92 #
93 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/timerfd/Makefile

1

\*\*\*\*\*

1363 Mon Jan 7 13:09:48 2019

new/usr/src/uts/intel/timerfd/Makefile

10077 usr/src/uts/ Makefile changes for smatch

\*\*\*\*\*

```
1 #
2 # This file and its contents are supplied under the terms of the
3 # Common Development and Distribution License ("CDDL"), version 1.0.
4 # You may only use this file in accordance with the terms of version
5 # 1.0 of the CDDL.
6 #
7 # A full copy of the text of the CDDL should have accompanied this
8 # source. A copy of the CDDL is also available via the Internet at
9 # http://www.illumos.org/license/CDDL.
10 #
11 #
12 #
13 # Copyright (c) 2018, Joyent, Inc.
14 # Copyright (c) 2015 Joyent, Inc. All rights reserved.
15 #
16 #
17 # Path to the base of the uts directory tree (usually /usr/src/uts).
18 #
19 UTSBASE = ../../
20 #
21 #
22 # Define the module and object file sets.
23 #
24 MODULE = timerfd
25 OBJECTS = $(TIMERFD_OBJS:%=$(OBJDIR)/%)
26 LINTS = $(TIMERFD_OBJS:%.o=$(LINTSDIR)/%.ln)
27 ROOTMODULE = $(USR_DRV_DIR)/$(MODULE)
28 CONF_SRCDIR = $(UTSBASE)/common/io
29 #
30 #
31 # Include common rules.
32 #
33 include $(UTSBASE)/intel/Makefile.intel
34 #
35 CERRWARN += -_gcc=-Wno-parentheses
36 #
37 # needs work
38 SMOFF += all_func_returns
39 #
40 #
41 # Define targets
42 #
43 ALL_TARGET = $(BINARY) $(SRC_CONFILE)
44 LINT_TARGET = $(MODULE).lint
45 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
46 #
47 #
48 # Default build targets.
49 #
50 .KEEP_STATE:
51 #
52 def: $(DEF_DEPS)
53 #
54 all: $(ALL_DEPS)
55 #
56 clean: $(CLEAN_DEPS)
57 #
58 clobber: $(CLOBBER_DEPS)
59 #
60 lint: $(LINT_DEPS)
```

new/usr/src/uts/intel/timerfd/Makefile

2

```
62 modlintlib: $(MODLINTLIB_DEPS)
63 #
64 clean.lint: $(CLEAN_LINT_DEPS)
65 #
66 install: $(INSTALL_DEPS)
67 #
68 #
69 # Include common targets.
70 #
71 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/timod/Makefile

1

```
*****
2039 Mon Jan 7 13:09:48 2019
new/usr/src/uts/intel/timod/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/timod/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 #ident "%Z%M% %I% %E% SMI"
28 #
29 # This makefile drives the production of the timod streams kernel
30 # module.
31 #
32 # intel architecture dependent
33 #

29 #
30 # Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 UTSBASE = ../../

34 #
35 # Define the module and object file sets.
36 #
37 MODULE = timod
38 OBJECTS = $(TIMOD_OBJS:%=$(OBJS_DIR)/%)
39 LINTS = $(TIMOD_OBJS:%.o=$(LINTS_DIR)/%.ln)
40 ROOTMODULE = $(ROOT_STRMOD_DIR)/$(MODULE)

42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel

47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

54 #
```

new/usr/src/uts/intel/timod/Makefile

2

```
55 # For now, disable these lint checks; maintainers should endeavor
56 # to investigate and remove these for maximum lint coverage.
57 # Please do not carry these forward to new Makefiles.
58 #
59 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
60 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW

62 # needs work
63 SMOFF += indenting

65 #
66 # Default build targets.
67 #
68 .KEEP_STATE:

70 def: $(DEF_DEPS)

72 all: $(ALL_DEPS)

74 clean: $(CLEAN_DEPS)

76 clobber: $(CLOBBER_DEPS)

78 lint: $(LINT_DEPS)

80 modlintlib: $(MODLINTLIB_DEPS)

82 clean.lint: $(CLEAN_LINT_DEPS)

84 install: $(INSTALL_DEPS)

86 #
87 # Include common targets.
88 #
89 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
2522 Mon Jan 7 13:09:49 2019
new/usr/src/uts/intel/tl/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/tl/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the tl driver
31 #
32 # intel architecture dependent
33 #

35 #
36 # Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../

40 #
41 # Define the module and object file sets.
42 #
43 MODULE = tl
44 OBJECTS = $(TL_OBJS:%=$(OBJS_DIR)/%)
45 LINTS = $(TL_OBJS:%.o=$(LINTS_DIR)/%.lint)
46 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
47 CONF_SRCDIR = $(UTSBASE)/common/io

49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY) $(SRC_CONFILE)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFILE)

61 #

```

```

62 # Depends on ip; this is for sharing common option management code
63 # which happens to live in "ip" kernel module only. Needed to do
64 # AF_UNIX socket options.
65 #
66 LDFLAGS += -dy -Ndrv/ip

68 #
69 # For now, disable these lint checks; maintainers should endeavor
70 # to investigate and remove these for maximum lint coverage.
71 # Please do not carry these forward to new Makefiles.
72 #
73 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
74 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
75 LINTTAGS += -erroff=E_STATIC_UNUSED

77 CERRWARN += -_gcc=-Wno-unused-function
78 CERRWARN += -_gcc=-Wno-parentheses
79 CERRWARN += -_gcc=-Wno-uninitialized

81 # needs work
82 SMOFF += deref_check

84 #
85 # Default build targets.
86 #
87 .KEEP_STATE:

89 def: $(DEF_DEPS)

91 all: $(ALL_DEPS)

93 clean: $(CLEAN_DEPS)

95 clobber: $(CLOBBER_DEPS)

97 lint: $(LINT_DEPS)

99 modlintlib: $(MODLINTLIB_DEPS)

101 clean.lint: $(CLEAN_LINT_DEPS)

103 install: $(INSTALL_DEPS)

105 #
106 # Include common targets.
107 #
108 include $(UTSBASE)/intel/Makefile.targ

```



```

*****
1775 Mon Jan 7 13:09:49 2019
new/usr/src/uts/intel/tlimod/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
12 #
13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/tlimod/Makefile
23 #
24 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 #ident "%Z%M% %I% %E% SMI"
28 #
29 # This makefile drives the production of the tlimod
30 # kernel module.
31 #
32 # intel architecture dependent
33 #

29 #
30 # Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 UTSBASE = ../../

34 #
35 # Define the module and object file sets.
36 #
37 MODULE = tlimod
38 OBJECTS = $(TLIMOD_OBJS:%=$(OBJDIR)/%)
39 LINTS = $(TLIMOD_OBJS:%.o=$(LINTS_DIR)/%.ln)
40 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)

42 #
43 # Include common rules.
44 #
45 include $(UTSBASE)/intel/Makefile.intel

47 #
48 # Define targets
49 #
50 ALL_TARGET = $(BINARY)
51 LINT_TARGET = $(MODULE).lint
52 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

54 # needs work

```

```

55 SMOFF += indenting

57 #
58 # Default build targets.
59 #
60 .KEEP_STATE:

62 def: $(DEF_DEPS)

64 all: $(ALL_DEPS)

66 clean: $(CLEAN_DEPS)

68 clobber: $(CLOBBER_DEPS)

70 lint: $(LINT_DEPS)

72 modlintlib: $(MODLINTLIB_DEPS)

74 clean.lint: $(CLEAN_LINT_DEPS)

76 install: $(INSTALL_DEPS)

78 #
79 # Include common targets.
80 #
81 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/tmpfs/Makefile

1

```
*****
2288 Mon Jan 7 13:09:49 2019
new/usr/src/uts/intel/tmpfs/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
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14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/tmpfs/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the tmpfs file system
31 # kernel module.
32 #
33 # intel architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTSBASE = ../..

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = tmpfs
45 OBJECTS = $(TMPFS_OBJS:%=$(OBJS_DIR)/%)
46 LINTS = $(TMPFS_OBJS:%.o=$(LINTS_DIR)/%.ln)
47 ROOTMODULE = $(ROOT_FS_DIR)/$(MODULE)

49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

61 #
```

new/usr/src/uts/intel/tmpfs/Makefile

2

```
62 # For now, disable these lint checks; maintainers should endeavor
63 # to investigate and remove these for maximum lint coverage.
64 # Please do not carry these forward to new Makefiles.
65 #
66 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
67 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

69 CERRWARN += -_gcc=-Wno-parentheses
70 CERRWARN += -_gcc=-Wno-uninitialized
71 CERRWARN += -_gcc=-Wno-unused-value

73 # needs work
74 $(OBJS_DIR)/tmp_vnops.o := SMOFF += signed

76 #
77 # Default build targets.
78 #
79 .KEEP_STATE:

81 def: $(DEF_DEPS)

83 all: $(ALL_DEPS)

85 clean: $(CLEAN_DEPS)

87 clobber: $(CLOBBER_DEPS)

89 lint: $(LINT_DEPS)

91 modlintlib: $(MODLINTLIB_DEPS)

93 clean.lint: $(CLEAN_LINT_DEPS)

95 install: $(INSTALL_DEPS)

97 #
98 # Include common targets.
99 #
100 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/tnf/Makefile

1

```
*****
2329 Mon Jan 7 13:09:50 2019
new/usr/src/uts/intel/tnf/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
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11 # and limitations under the License.
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/tnf/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 #         tnf (kernel trace driver) Makefile
31 #
32 #         intel architecture dependent
33 #

35 #
36 #         Path to the base of the uts directory tree (usually /usr/src/uts).
37 #
38 UTSBASE = ../../

40 #
41 #         Define the module and object file sets.
42 #
43 MODULE         = tnf
44 OBJECTS        = $(TNF_OBJS:%=$(OBJS_DIR)/%)
45 LINTS          = $(TNF_OBJS:%.o=$(LINTS_DIR)/%.ln)
46 ROOTMODULE    = $(USR_DRV_DIR)/$(MODULE)
47 CONF_SRCDIR   = $(UTSBASE)/common/tnf

49 #
50 #         Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 #
55 #         Define targets
56 #
57 ALL_TARGET     = $(BINARY) $(SRC_CONFILE)
58 LINT_TARGET    = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFILE)

61 #
```

new/usr/src/uts/intel/tnf/Makefile

2

```
62 # For now, disable these lint checks; maintainers should endeavor
63 # to investigate and remove these for maximum lint coverage.
64 # Please do not carry these forward to new Makefiles.
65 #
66 LINTTAGS      += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
67 LINTTAGS      += -erroff=E_PTRDIFF_OVERFLOW
68 LINTTAGS      += -erroff=E_ASSIGN_NARROW_CONV

70 CERRWARN      += -_gcc=-Wno-parentheses
71 CERRWARN      += -_gcc=-Wno-uninitialized

73 # needs work
74 $(OBJS_DIR)/tnf_writer.o := SMOFF += all_func_returns

76 #
77 #         Default build targets.
78 #
79 .KEEP_STATE:

81 def:         $(DEF_DEPS)

83 all:         $(ALL_DEPS)

85 clean:       $(CLEAN_DEPS)

87 clobber:     $(CLOBBER_DEPS)

89 lint:        $(LINT_DEPS)

91 modlintlib:  $(MODLINTLIB_DEPS)

93 clean.lint:  $(CLEAN_LINT_DEPS)

95 install:     $(INSTALL_DEPS)

97 #
98 #         Include common targets.
99 #
100 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/tpm/Makefile

1

```
*****
2068 Mon Jan 7 13:09:50 2019
new/usr/src/uts/intel/tpm/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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6 # You may not use this file except in compliance with the License.
7 #
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9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
12 #
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15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
22 # Use is subject to license terms.
23 #
24 # Copyright (c) 2018, Joyent, Inc.
25 # This makefile drives the production of the TPM driver kernel
26 # module.
26 UTSBASE = $(SRC)/uts
28 #
29 # Define the module and object file sets.
30 #
31 MODULE = tpm
32 OBJECTS = $(TPM_OBJS:%=$(OBJS_DIR)/%)
33 LINTS = $(TPM_OBJS:%.o=$(LINTS_DIR)/%.ln)
34 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
36 #
37 # Include common rules.
38 #
39 # For now, TPM only delivers on x86/64 platforms, so only build
40 # with the Intel rules.
41 #
42 include $(UTSBASE)/intel/Makefile.intel
44 #
45 # Define targets
46 #
47 ALL_TARGET = $(BINARY)
48 LINT_TARGET = $(MODULE).lint
49 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
51 #
52 # Dependency
53 #
54 LDFLAGS += -dy
56 CFLAGS += -xCC
58 # This is for everything except /usr/include/tss/
```

new/usr/src/uts/intel/tpm/Makefile

2

```
59 CPPFLAGS += -I$(ROOT)/usr/include
61 # This is for /usr/include/tss/, which is not built in the ON consolidation
62 CPPFLAGS += -I$(ADJUNCT_PROTO)/usr/include
64 CERRWARN += -_gcc=-Wno-parentheses
66 # needs work
67 $(OBJS_DIR)/tpm.o := SMOFF += index_overflow
69 #
70 # Default build targets.
71 #
72 .KEEP_STATE:
74 def: $(DEF_DEPS)
76 all: $(ALL_DEPS)
78 clean: $(CLEAN_DEPS)
80 clobber: $(CLOBBER_DEPS)
82 lint: $(LINT_DEPS)
84 modlintlib: $(MODLINTLIB_DEPS)
86 clean.lint: $(CLEAN_LINT_DEPS)
88 install: $(INSTALL_DEPS)
90 include $(UTSBASE)/intel/Makefile.targ
92 $(OBJECTS): $(OBJS_DIR)
```

new/usr/src/uts/intel/trill/Makefile

1

```
*****
1921 Mon Jan 7 13:09:51 2019
new/usr/src/uts/intel/trill/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/trill/Makefile
23 #
24 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the TRILL socket kernel
28 # module.
29 #
30 # intel architecture dependent
31 #
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 #
37 # UTSBASE = ../../
38 #
39 # Define the module and object file sets.
40 #
41 #
42 #
43 # MODULE = trill
44 # OBJECTS = $(TRILL_OBJS:%=$(OBJS_DIR)/%)
45 # LINTS = $(TRILL_OBJS:%.o=$(LINTS_DIR)/%.ln)
46 # ROOTMODULE = $(ROOT SOCK_DIR)/$(MODULE)
47 #
48 # Include common rules.
49 #
50 # include $(UTSBASE)/intel/Makefile.intel
51 #
52 # CERRWARN += _gcc=-Wno-uninitialized
53 #
54 #
55 # needs work
56 # SMOFF += all_func_returns
57 #
58 # Define targets
59 #
60 #
61 # ALL_TARGET = $(BINARY)
62 # LINT_TARGET = $(MODULE).lint
```

new/usr/src/uts/intel/trill/Makefile

2

```
57 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
58 #
59 # Overrides.
60 #
61 #
62 CFLAGS += $(CCVERBOSE)
63 LDFLAGS += -dy -Ndrv/bridge -Nmisc/mac -Nmisc/dls -Nfs/sockfs
64 #
65 #
66 # Default build targets.
67 #
68 .KEEP_STATE:
69 #
70 def: $(DEF_DEPS)
71 #
72 all: $(ALL_DEPS)
73 #
74 clean: $(CLEAN_DEPS)
75 #
76 clobber: $(CLOBBER_DEPS)
77 #
78 lint: $(LINT_DEPS)
79 #
80 modlintlib: $(MODLINTLIB_DEPS)
81 #
82 clean.lint: $(CLEAN_LINT_DEPS)
83 #
84 install: $(INSTALL_DEPS)
85 #
86 #
87 # Include common targets.
88 #
89 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/udfs/Makefile

1

```
*****
2552 Mon Jan 7 13:09:51 2019
new/usr/src/uts/intel/udfs/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 # Copyright (c) 2011 Bayard G. Bell. All rights reserved.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the udfs file system
28 # kernel module.
29 # intel implementation architecture dependent
30 #
31 #
32 #
33 # Path to the base of the uts directory tree (usually /usr/src/uts).
34 #
35 # UTSBASE = ../../
36 #
37 # Define the module and object file sets.
38 #
39 # MODULE = udfs
40 # OBJECTS = $(UDFS_OBJS:%=$(OBJS_DIR)/%)
41 # LINTS = $(UDFS_OBJS:%.o=$(LINTS_DIR)/%.ln)
42 # ROOTMODULE = $(ROOT_FS_DIR)/$(MODULE)
43 #
44 # Include common rules.
45 #
46 # include $(UTSBASE)/intel/Makefile.intel
47 #
48 # Define targets
49 #
50 # ALL_TARGET = $(BINARY)
51 # LINT_TARGET = $(MODULE).lint
52 # INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
53 #
54 # Overrides.
55 #
56 # MODSTUBS_DIR = $(OBJS_DIR)
```

new/usr/src/uts/intel/udfs/Makefile

2

```
57 $(MODSTUBS_O) := AS_CPPFLAGS += -DUDFS_MODULE
58 CLEANFILES += $(MODSTUBS_O)
59 #
60 #
61 # Define dependency on specfs
62 #
63 # LDFLAGS += -dy -N fs/specfs
64 #
65 #
66 # For now, disable these lint checks; maintainers should endeavor
67 # to investigate and remove these for maximum lint coverage.
68 # Please do not carry these forward to new Makefiles.
69 #
70 # LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
71 # LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
72 # LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
73 # LINTTAGS += -erroff=E_STATIC_UNUSED
74 # LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
75 # LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
76 #
77 # CERRWARN += _gcc=-Wno-parentheses
78 # CERRWARN += _gcc=-Wno-unused-label
79 # CERRWARN += _gcc=-Wno-uninitialized
80 # CERRWARN += _gcc=-Wno-type-limits
81 #
82 # needs work
83 # SMATCH=off
84 #
85 #
86 # Default build targets.
87 #
88 # .KEEP_STATE:
89 #
90 # def: $(DEF_DEPS)
91 #
92 # all: $(ALL_DEPS)
93 #
94 # clean: $(CLEAN_DEPS)
95 #
96 # clobber: $(CLOBBER_DEPS)
97 #
98 # lint: $(LINT_DEPS)
99 #
100 # modlintlib: $(MODLINTLIB_DEPS)
101 #
102 # clean.lint: $(CLEAN_LINT_DEPS)
103 #
104 # install: $(INSTALL_DEPS)
105 #
106 #
107 # Include common targets.
108 #
109 # include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/udmf/Makefile

1

```
*****
1796 Mon Jan 7 13:09:51 2019
new/usr/src/uts/intel/udmf/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # This file and its contents are supplied under the terms of the
3 # Common Development and Distribution License ("CDDL"), version 1.0.
4 # You may only use this file in accordance with the terms of version
5 # 1.0 of the CDDL.
6 #
7 # A full copy of the text of the CDDL should have accompanied this
8 # source. A copy of the CDDL is also available via the Internet at
9 # http://www.illumos.org/license/CDDL.
10 #
11 #
12 #
13 # Copyright (c) 2018, Joyent, Inc.
14 # Copyright (c) 2014 Joyent, Inc. All rights reserved.
15 #
16 #
17 # Path to the base of the uts directory tree (usually /usr/src/uts).
18 #
19 UTSBASE = ../..
20 #
21 #
22 # Define the module and object file sets.
23 #
24 MODULE = udmf
25 OBJECTS = $(UDMF_OBJS:%=$(OBJDIR)/%)
26 LINTS = $(UDMF_OBJS:%.o=$(LINTSDIR)/%.ln)
27 ROOTMODULE = $(ROOTDRVDIR)/$(MODULE)
28 #
29 #
30 # Include common rules.
31 #
32 include $(UTSBASE)/intel/Makefile.intel
33 #
34 CPPFLAGS += -I$(UTSBASE)/common/io/usbgem
35 CPPFLAGS += -DVERSION=\"2.0.0\"
36 CPPFLAGS += -DUSBGEM_CONFIG_GLDv3
37 LDFLAGS += -dy -N misc/mac -N drv/ip -N misc/usba -N misc/usbgem
38 #
39 CERRWARN += _gcc=-Wno-unused-value
40 CERRWARN += _gcc=-Wno-unused-function
41 CERRWARN += _gcc=-Wno-unused-variable
42 CERRWARN += _gcc=-Wno-unused-label
43 #
44 LINTTAGS += -erroff=E_FUNC_VAR_UNUSED
45 LINTTAGS += -erroff=E_FUNC_ARG_UNUSED
46 LINTTAGS += -erroff=E_STATIC_UNUSED
47 LINTTAGS += -erroff=E_FUNC_RET_ALWAYS_IGNORE2
48 LINTTAGS += -erroff=E_FUNC_SET_NOT_USED
49 #
50 # needs work
51 $(OBJDIR)/udmf_usbgem.o := SMOFF += indenting
52 #
53 #
54 # Define targets
55 #
56 ALL_TARGET = $(BINARY)
57 LINT_TARGET = $(MODULE).lint
58 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
59 #
60 #
```

new/usr/src/uts/intel/udmf/Makefile

2

```
61 # Default build targets.
62 #
63 .KEEP_STATE:
64 #
65 def: $(DEF_DEPS)
66 #
67 all: $(ALL_DEPS)
68 #
69 clean: $(CLEAN_DEPS)
70 #
71 clobber: $(CLOBBER_DEPS)
72 #
73 lint: $(LINT_DEPS)
74 #
75 modlintlib: $(MODLINTLIB_DEPS)
76 #
77 clean.lint: $(CLEAN_LINT_DEPS)
78 #
79 install: $(INSTALL_DEPS)
80 #
81 # Include common targets.
82 #
83 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/udp/Makefile

1

```
*****
2318 Mon Jan 7 13:09:51 2019
new/usr/src/uts/intel/udp/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/udp/Makefile
23 #
24 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.
28 # This makefile drives the production of the udp driver kernel module.
29 # intel implementation architecture dependent
30 #
31 #
32 # Path to the base of the uts directory tree (usually /usr/src/uts).
33 #
34 # UTSBASE = ../../
35 #
36 # Define the module and object file sets.
37 #
38 # MODULE = udp
39 # OBJECTS = $(UDP_OBJS:%=$(OBJS_DIR)/%)
40 # LINTS = $(UDP_OBJS:%.o=$(LINTS_DIR)/%.ln)
41 # ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
42 # ROOTLINK = $(ROOT_STRMOD_DIR)/$(MODULE) $(ROOT SOCK_DIR)/$(MODULE)
43 # CONF_SRC_DIR = $(UTSBASE)/common/inet/udp
44 #
45 # Extra for $(MODULE).check target
46 #
47 # Need to remove ipddi.o since it has non-static defines for _init etc.
48 # IP_CHECK_OBJS = $(IP_OBJS:ipddi.o=ip.o)
49 # EXTRA_CHECK_OBJS = $(IP_CHECK_OBJS:%=../ip/$(OBJS_DIR)/%)
50 #
51 #
52 # Include common rules.
53 #
54 # include $(UTSBASE)/intel/Makefile.intel
55 #
56 #
57 # Define targets
```

new/usr/src/uts/intel/udp/Makefile

2

```
58 #
59 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
60 LINT_TARGET = $(MODULE).lint
61 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTLINK) $(ROOT_CONFFILE)
62 #
63 #
64 # depends on ip and sockfs
65 #
66 LDFLAGS += -dy -Ndrv/ip -Nfs/sockfs
67 #
68 # needs work
69 $(OBJS_DIR)/udpddi.o := SMOFF += index_overflow
70 #
71 #
72 # Default build targets.
73 #
74 .KEEP_STATE:
75 #
76 def: $(DEF_DEPS)
77 #
78 all: $(ALL_DEPS)
79 #
80 clean: $(CLEAN_DEPS)
81 #
82 clobber: $(CLOBBER_DEPS)
83 #
84 lint: $(LINT_DEPS)
85 #
86 modlintlib: $(MODLINTLIB_DEPS)
87 #
88 clean.lint: $(CLEAN_LINT_DEPS)
89 #
90 install: $(INSTALL_DEPS)
91 #
92 $(ROOTLINK): $(ROOT_STRMOD_DIR) $(ROOT SOCK_DIR) $(ROOTMODULE)
93 -$(RM) $@; ln $(ROOTMODULE) $@
94 #
95 #
96 # Include common targets.
97 #
98 include $(UTSBASE)/intel/Makefile.targ
```



new/usr/src/uts/intel/udp6/Makefile

1

```
*****
1916 Mon Jan 7 13:09:52 2019
new/usr/src/uts/intel/udp6/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License, Version 1.0 only
6 # (the "License"). You may not use this file except in compliance
7 # with the License.
8 #
9 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
10 # or http://www.opensolaris.org/os/licensing.
11 # See the License for the specific language governing permissions
12 # and limitations under the License.
13 #
14 # When distributing Covered Code, include this CDDL HEADER in each
15 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
16 # If applicable, add the following below this CDDL HEADER, with the
17 # fields enclosed by brackets "[]" replaced with your own identifying
18 # information: Portions Copyright [yyyy] [name of copyright owner]
19 #
20 # CDDL HEADER END
21 #
22 #
23 # Copyright 2004 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # ident "%Z%M% %I% %E% SMI"
28 #
29 # This makefile drives the production of the udp6 driver kernel module.
30 #
31 # intel implementation architecture dependent
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../
37 #
38 # Define the module and object file sets.
39 #
40 MODULE = udp6
41 OBJECTS = $(UDP6_OBJS:%=$(OBJS_DIR)/%)
42 LINTS = $(UDP6_OBJS:%.o=$(LINTS_DIR)/%.ln)
43 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
44 CONF_SRCDIR = $(UTSBASE)/common/inet/udp
45 #
46 #
47 # Include common rules.
48 #
49 #
50 include $(UTSBASE)/intel/Makefile.intel
51 #
52 # Define targets
53 #
54 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
55 LINT_TARGET = $(MODULE).lint
56 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)
57 #
58 # depends on udp ip and ip6
```

new/usr/src/uts/intel/udp6/Makefile

2

```
56 #
57 LDFLAGS += -dy -Ndrv/udp -Ndrv/ip -Ndrv/ip6
58 #
59 # needs work
60 SMOFF += index_overflow
61 #
62 #
63 # Default build targets.
64 #
65 .KEEP_STATE:
66 #
67 def: $(DEF_DEPS)
68 #
69 all: $(ALL_DEPS)
70 #
71 clean: $(CLEAN_DEPS)
72 #
73 clobber: $(CLOBBER_DEPS)
74 #
75 lint: $(LINT_DEPS)
76 #
77 modlintlib: $(MODLINTLIB_DEPS)
78 #
79 clean.lint: $(CLEAN_LINT_DEPS)
80 #
81 install: $(INSTALL_DEPS)
82 #
83 #
84 # Include common targets.
85 #
86 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/ufs/Makefile

1

```
*****
2659 Mon Jan 7 13:09:53 2019
new/usr/src/uts/intel/ufs/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
12 #
13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/ufs/Makefile
23 #
24 # Copyright 2006 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the ufs file system
31 # kernel module.
32 #
33 # intel architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTBASE = ../../

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = ufs
45 OBJECTS = $(UFS_OBJS:%=$(OBJS_DIR)/%)
46 LINTS = $(UFS_OBJS:%.o=$(LINTS_DIR)/%.ln)
47 ROOTMODULE = $(ROOT_FS_DIR)/$(MODULE)

49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

61 #
```

new/usr/src/uts/intel/ufs/Makefile

2

```
62 # Override defaults to build a unique, local modstubs.o.
63 #
64 MODSTUBS_DIR = $(OBJS_DIR)
65 $(MODSTUBS_O) := AS_CPPFLAGS += -DUPS_MODULE
66 CLEANFILES += $(MODSTUBS_O)
67 LDFLAGS += -dy -Nfs/specfs -Nmisc/fssnap_if

69 #
70 # For now, disable these lint checks; maintainers should endeavor
71 # to investigate and remove these for maximum lint coverage.
72 # Please do not carry these forward to new Makefiles.
73 #
74 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
75 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
76 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
77 LINTTAGS += -erroff=E_STATIC_UNUSED
78 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
79 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

81 CERRWARN += -_gcc=-Wno-parentheses
82 CERRWARN += -_gcc=-Wno-uninitialized
83 CERRWARN += -_gcc=-Wno-type-limits
84 CERRWARN += -_gcc=-Wno-unused-label

86 # needs work
87 SMATCH=off

89 #
90 # Default build targets.
91 #
92 .KEEP_STATE:

94 def: $(DEF_DEPS)

96 all: $(ALL_DEPS)

98 clean: $(CLEAN_DEPS)

100 clobber: $(CLOBBER_DEPS)

102 lint: $(LINT_DEPS)

104 modlintlib: $(MODLINTLIB_DEPS)

106 clean.lint: $(CLEAN_LINT_DEPS)

108 install: $(INSTALL_DEPS)

110 #
111 # Include common targets.
112 #
113 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/upf/Makefile

1

```
*****
1842 Mon Jan 7 13:09:53 2019
new/usr/src/uts/intel/upf/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # This file and its contents are supplied under the terms of the
3 # Common Development and Distribution License ("CDDL"), version 1.0.
4 # You may only use this file in accordance with the terms of version
5 # 1.0 of the CDDL.
6 #
7 # A full copy of the text of the CDDL should have accompanied this
8 # source. A copy of the CDDL is also available via the Internet at
9 # http://www.illumos.org/license/CDDL.
10 #
11 #
12 #
13 # Copyright (c) 2018, Joyent, Inc.
14 # Copyright (c) 2014 Joyent, Inc. All rights reserved.
15 #
16 #
17 # Path to the base of the uts directory tree (usually /usr/src/uts).
18 #
19 UTSBASE = ../..
20 #
21 #
22 # Define the module and object file sets.
23 #
24 MODULE = upf
25 OBJECTS = $(UPF_OBJS:%=$(OBJS_DIR)/%)
26 LINTS = $(UPF_OBJS:%.o=$(LINTS_DIR)/%.ln)
27 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
28 #
29 #
30 # Include common rules.
31 #
32 include $(UTSBASE)/intel/Makefile.intel
33 #
34 CPPFLAGS += -I$(UTSBASE)/common/io/usbgem
35 CPPFLAGS += -DVERSION=\"2.0.1\"
36 CPPFLAGS += -DUSBGEM_CONFIG_GLDv3
37 LDFLAGS += -dy -N misc/mac -N drv/ip -N misc/usba -N misc/usbgem
38 #
39 CERRWARN += _gcc=-Wno-type-limits
40 CERRWARN += _gcc=-Wno-unused-function
41 CERRWARN += _gcc=-Wno-unused-variable
42 CERRWARN += _gcc=-Wno-unused-label
43 #
44 # needs work
45 $(OBJS_DIR)/upf_usbgem.o := SMOFF += all_func_returns
46 #
47 LINTTAGS += -erroff=E_FUNC_VAR_UNUSED
48 LINTTAGS += -erroff=E_FUNC_ARG_UNUSED
49 LINTTAGS += -erroff=E_STATIC_UNUSED
50 LINTTAGS += -erroff=E_FUNC_RET_ALWAYS_IGNORE2
51 LINTTAGS += -erroff=E_FUNC_SET_NOT_USED
52 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
53 #
54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
```

new/usr/src/uts/intel/upf/Makefile

2

```
61 #
62 # Default build targets.
63 #
64 .KEEP_STATE:
65 #
66 def: $(DEF_DEPS)
67 #
68 all: $(ALL_DEPS)
69 #
70 clean: $(CLEAN_DEPS)
71 #
72 clobber: $(CLOBBER_DEPS)
73 #
74 lint: $(LINT_DEPS)
75 #
76 modlintlib: $(MODLINTLIB_DEPS)
77 #
78 clean.lint: $(CLEAN_LINT_DEPS)
79 #
80 install: $(INSTALL_DEPS)
81 #
82 # Include common targets.
83 #
84 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
1975 Mon Jan 7 13:09:54 2019
new/usr/src/uts/intel/ural/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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9 # or http://www.opensolaris.org/os/licensing.
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15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

26 # ident "%Z%M% %I% %E% SMI"
27 #
28 # This makefile drives the production of the ural driver kernel module.
29 #
30 # i86pc architecture dependent
31 #

33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../

37 #
38 # Define the module and object file sets.
39 #
40 MODULE = ural
41 OBJECTS = $(URAL_OBJS:%=$(OBJS_DIR)/%)
42 LINTS = $(URAL_OBJS:%.o=$(LINTS_DIR)/%.ln)
43 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

44 #
45 # Include common rules.
46 #
47 include $(UTSBASE)/intel/Makefile.intel

48 #
49 # Define targets
50 #
51 ALL_TARGET = $(BINARY)
52 LINT_TARGET = $(MODULE).lint
53 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

54 #
55 # Driver depends on GLDv3 & wifi kernel support module.
56 #

```

```

55 LDFLAGS += -dy -Nmisc/mac -Nmisc/net80211 -Nmisc/usba

57 # needs work
58 SMOFF += deref_check

60 #
61 # Default build targets.
62 #
63 .KEEP_STATE:

65 def: $(DEF_DEPS)

67 all: $(ALL_DEPS)

69 clean: $(CLEAN_DEPS)

71 clobber: $(CLOBBER_DEPS)

73 lint: $(LINT_DEPS)

75 modlintlib: $(MODLINTLIB_DEPS)

77 clean.lint: $(CLEAN_LINT_DEPS)

79 install: $(INSTALL_DEPS)

81 #
82 # Include common targets.
83 #
84 include $(UTSBASE)/intel/Makefile.targ

86 #
87 # If you have any special case that general
88 # Makefile rules don't serve for you, just do
89 # it yourself.
90 #

```

```

*****
2256 Mon Jan 7 13:09:54 2019
new/usr/src/uts/intel/urtw/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # This makefile drives the production of the urtw driver kernel module.
29 #
30 # i86pc architecture dependent
31 #

33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTSBASE = ../../

38 #
39 # Define the module and object file sets.
40 #
41 MODULE = urtw
42 OBJECTS = $(URTW_OBJS:%=$(OBJS_DIR)/%)
43 LINTS = $(URTW_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

46 #
47 # Include common rules.
48 #
49 include $(UTSBASE)/intel/Makefile.intel

51 #
52 # Define targets
53 #
54 ALL_TARGET = $(BINARY)
55 LINT_TARGET = $(MODULE).lint
56 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

58 #
59 # Driver depends on GLDv3 & wifi kernel support module.
60 #
61 LDFLAGS += -dy -Nmisc/mac -Nmisc/net80211 -Nmisc/usba

```

```

63 CERRWARN += -_gcc=-Wno-parentheses
64 CERRWARN += -_gcc=-Wno-switch
65 CERRWARN += -_gcc=-Wno-char-subscripts
66 CERRWARN += -_gcc=-Wno-uninitialized

68 # needs work
69 SMOFF += deref_check,no_if_block,all_func_returns

71 #
72 # Default build targets.
73 #
74 .KEEP_STATE:

76 def: $(DEF_DEPS)

78 all: $(ALL_DEPS)

80 clean: $(CLEAN_DEPS)

82 clobber: $(CLOBBER_DEPS)

84 lint: $(LINT_DEPS)

86 modlintlib: $(MODLINTLIB_DEPS)

88 clean.lint: $(CLEAN_LINT_DEPS)

90 install: $(INSTALL_DEPS)

92 #
93 # Include common targets.
94 #
95 include $(UTSBASE)/intel/Makefile.targ

97 #
98 # If you have any special case that general
99 # Makefile rules don't serve for you, just do
100 # it yourself.
101 #

```

new/usr/src/uts/intel/usb\_ac/Makefile

1

```
*****
2277 Mon Jan 7 13:09:55 2019
new/usr/src/uts/intel/usb_ac/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright 2014 Garrett D'Amore <garrett@damore.org>
26 #
27 # Copyright (c) 2018, Joyent, Inc.
27 # uts/intel/usb_ac/Makefile
28 #
29 # This makefile drives the production of the usb_ac driver
30 # kernel module. Intel architecture dependent
31 #

30 #
31 # Path to the base of the uts directory tree (usually /usr/src/uts).
32 #
33 UTSBASE = ../../

35 #
36 # Define the module and object file sets.
37 #
38 MODULE = usb_ac
39 OBJECTS = $(USB_AC_OBJS:%=$(OBJS_DIR)/%)
40 LINTS = $(USB_AC_OBJS:%.o=$(LINTS_DIR)/%.ln)
41 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
42 CONF_SRCDIR = $(UTSBASE)/common/io/usb/clients/audio/usb_ac

44 #
45 # Include common rules.
46 #
47 include $(UTSBASE)/intel/Makefile.intel

49 #
50 # Define targets
51 #
52 ALL_TARGET = $(BINARY) $(SRC_CONFILE)
53 LINT_TARGET = $(MODULE).lint
54 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFILE)

56 #
```

new/usr/src/uts/intel/usb\_ac/Makefile

2

```
57 # Override defaults to build a unique, local modstubs.o.
58 #
59 MODSTUBS_DIR = $(OBJS_DIR)
60 CLEANFILES += $(MODSTUBS_O)

62 CERRWARN += -_gcc=-Wno-switch
63 CERRWARN += -_gcc=-Wno-type-limits
64 CERRWARN += -_gcc=-Wno-unused-label
65 CERRWARN += -_gcc=-Wno-uninitialized

67 # needs work
68 $(OBJS_DIR)/usb_ac.o := SMOFF += deref_check,indenting,testing_index_after_use

70 #
71 # depends on misc/usba
72 #
73 LDFLAGS += -dy -Nmisc/usba -Ndrv/audio

75 #
76 # Default build targets.
77 #
78 .KEEP_STATE:

80 def: $(DEF_DEPS)

82 all: $(ALL_DEPS)

84 clean: $(CLEAN_DEPS)

86 clobber: $(CLOBBER_DEPS)

88 lint: $(LINT_DEPS)

90 modlintlib: $(MODLINTLIB_DEPS)

92 clean.lint: $(CLEAN_LINT_DEPS)

94 install: $(INSTALL_DEPS)

96 #
97 # Include common targets.
98 #
99 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
1998 Mon Jan 7 13:09:55 2019
new/usr/src/uts/intel/usba/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
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2 # CDDL HEADER START
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16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/usba/Makefile
23 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 #
28 #
29 #
30 #
31 # Path to the base of the uts directory tree (usually /usr/src/uts).
32 #
33 #
34 # Define the module and object file sets.
35 #
36 # MODULE = usba
37 # OBJECTS = $(USBA_OBJS:%=$(OBJS_DIR)/%)
38 # LINTS = $(USBA_OBJS:%.o=$(LINTS_DIR)/%.ln)
39 # ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
40 #
41 #
42 # Include common rules.
43 #
44 # include $(UTSBASE)/intel/Makefile.intel
45 #
46 # CERRWARN += -_gcc=-Wno-uninitialized
47 # CERRWARN += -_gcc=-Wno-parentheses
48 # CERRWARN += -_gcc=-Wno-switch
49 # CERRWARN += -_gcc=-Wno-unused-label
50 # CERRWARN += -_gcc=-Wno-unused-value
51 # CERRWARN += -_gcc=-Wno-unused-variable
52 #
53 # needs work
54 # SMOFF += all_func_returns,deref_check
55 #
56 #
57 # Define targets
58 #

```

```

59 ALL_TARGET = $(BINARY)
60 LINT_TARGET = $(MODULE).lint
61 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
62 #
63 #
64 # Default build targets.
65 #
66 .KEEP_STATE:
67 #
68 def: $(DEF_DEPS)
69 #
70 all: $(ALL_DEPS)
71 #
72 clean: $(CLEAN_DEPS)
73 #
74 clobber: $(CLOBBER_DEPS)
75 #
76 lint: $(LINT_DEPS)
77 #
78 modlintlib: $(MODLINTLIB_DEPS)
79 #
80 clean.lint: $(CLEAN_LINT_DEPS)
81 #
82 install: $(INSTALL_DEPS)
83 #
84 #
85 # Include common targets.
86 #
87 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/usbecm/Makefile

1

\*\*\*\*\*

1848 Mon Jan 7 13:09:56 2019

new/usr/src/uts/intel/usbecm/Makefile

10077 usr/src/uts/ Makefile changes for smatch

\*\*\*\*\*

```
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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10 # See the License for the specific language governing permissions
11 # and limitations under the License.
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15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/usbecm/Makefile
23 # Copyright 2010 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright 2014 Garrett D'Amore <garrett@damore.org>
27 #
28 # Copyright (c) 2018, Joyent, Inc.

28 # This makefile drives the production of Abstract Control Model of
29 # USB Communication Devices Class driver.
30 #
31 # Path to the base of the uts directory tree (usually /usr/src/uts).
32 #
33 UTSBASE = ../../

35 #
36 # Define the module and object file sets.
37 #
38 MODULE = usbecm
39 OBJECTS = $(USBECM_OBJS:%=$(OBJS_DIR)/%)
40 LINTS = $(USBECM_OBJS:%.o=$(LINTS_DIR)/%.ln)
41 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

43 #
44 # Include common rules.
45 #
46 include $(UTSBASE)/intel/Makefile.intel

48 LDFLAGS += -dy -Nmisc/usba -Nmisc/mac

50 #
51 # Define targets
52 #
53 ALL_TARGET = $(BINARY)
54 LINT_TARGET = $(MODULE).lint
55 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

57 # needs work
58 SMOFF += deref_check
```

new/usr/src/uts/intel/usbecm/Makefile

2

```
60 .KEEP_STATE:

62 all: $(ALL_DEPS)

64 def: $(DEF_DEPS)

66 clean: $(CLEAN_DEPS)

68 clobber: $(CLOBBER_DEPS)

70 lint: $(LINT_DEPS)

72 modlintlib: $(MODLINTLIB_DEPS)

74 clean.lint: $(CLEAN_LINT_DEPS)

76 install: $(INSTALL_DEPS)

78 #
79 # Include common targets.
80 #
81 include $(UTSBASE)/intel/Makefile.targ
```



```

*****
2108 Mon Jan 7 13:09:56 2019
new/usr/src/uts/intel/usbgeom/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # This file and its contents are supplied under the terms of the
3 # Common Development and Distribution License ("CDDL"), version 1.0.
4 # You may only use this file in accordance with the terms of version
5 # 1.0 of the CDDL.
6 #
7 # A full copy of the text of the CDDL should have accompanied this
8 # source. A copy of the CDDL is also available via the Internet at
9 # http://www.illumos.org/license/CDDL.
10 #
11 #
12 #
13 # Copyright (c) 2018, Joyent, Inc.
14 # Copyright (c) 2014 Joyent, Inc. All rights reserved.
15 #
16 #
17 # Path to the base of the uts directory tree (usually /usr/src/uts).
18 #
19 UTSBASE = ../..
20 #
21 #
22 # Define the module and object file sets.
23 #
24 MODULE = usbgeom
25 OBJECTS = $(USBGEM_OBJS:%=$(OBJS_DIR)/%)
26 LINTS = $(USBGEM_OBJS:%.o=$(LINTS_DIR)/%.ln)
27 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
28 #
29 #
30 # Include common rules.
31 #
32 include $(UTSBASE)/intel/Makefile.intel
33 #
34 #
35 # The USBGEM has support for various different features. We use
36 # these pre-processor macros to define the set we care about.
37 #
38 CPPFLAGS += \
39 -DUSBGEM_CONFIG_GLDv3 \
40 -DMODULE \
41 -DGEN_CONFIG_FMA \
42 -DUSBGEM_CONFIG_MAC_PROP \
43 -DVERSION="1.6"
44 #
45 CERRWARN += -_gcc=-Wno-uninitialized
46 CERRWARN += -_gcc=-Wno-parentheses
47 CERRWARN += -_gcc=-Wno-switch
48 CERRWARN += -_gcc=-Wno-unused-variable
49 CERRWARN += -_gcc=-Wno-unused-label
50 CERRWARN += -_gcc=-Wno-unused-function
51 #
52 # needs work
53 SMOFF += all_func_returns
54 #
55 LINTTAGS += -erroff=E_FUNC_VAR_UNUSED
56 LINTTAGS += -erroff=E_FUNC_ARG_UNUSED
57 LINTTAGS += -erroff=E_STATIC_UNUSED
58 LINTTAGS += -erroff=E_FUNC_SET_NOT_USED
59 LINTTAGS += -erroff=E_FUNC_RET_MAYBE_IGNORED2
60 LINTTAGS += -erroff=E_FUNC_RET_ALWAYS_IGNORE2

```

```

61 LINTTAGS += -erroff=E_BAD_FORMAT_ARG_TYPE2
62 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV
63 #
64 LDFLAGS += -dy -N misc/mac -N drv/ip -N misc/usba
65 #
66 #
67 # Define targets
68 #
69 ALL_TARGET = $(BINARY)
70 LINT_TARGET = $(MODULE).lint
71 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
72 #
73 #
74 # Default build targets.
75 #
76 .KEEP_STATE:
77 #
78 def: $(DEF_DEPS)
79 #
80 all: $(ALL_DEPS)
81 #
82 clean: $(CLEAN_DEPS)
83 #
84 clobber: $(CLOBBER_DEPS)
85 #
86 lint: $(LINT_DEPS)
87 #
88 modlintlib: $(MODLINTLIB_DEPS)
89 #
90 clean.lint: $(CLEAN_LINT_DEPS)
91 #
92 install: $(INSTALL_DEPS)
93 #
94 # Include common targets.
95 #
96 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/usbms/Makefile

1

```
*****
2272 Mon Jan 7 13:09:57 2019
new/usr/src/uts/intel/usbms/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
7 #
8 # You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
9 # or http://www.opensolaris.org/os/licensing.
10 # See the License for the specific language governing permissions
11 # and limitations under the License.
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13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/usbms/Makefile
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the usbms driver kernel module.
28 #
29 #
30 # Path to the base of the uts directory tree (usually /usr/src/uts).
31 #
32 #
33 #
34 # Define the module and object file sets.
35 #
36 MODULE = usbms
37 OBJECTS = $(USBMS_OBJS:%=$(OBJS_DIR)/%)
38 LINTS = $(USBMS_OBJS:%.o=$(LINTS_DIR)/%.ln)
39 ROOTMODULE = $(ROOT_STRMOD_DIR)/$(MODULE)
40 #
41 #
42 # Include common rules.
43 #
44 include $(UTSBASE)/intel/Makefile.intel
45 #
46 #
47 # Define targets
48 #
49 ALL_TARGET = $(BINARY)
50 LINT_TARGET = $(MODULE).lint
51 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
52 #
53 #
54 # Override defaults to build a unique, local modstubs.o.
55 #
56 MODSTUBS_DIR = $(OBJS_DIR)
57 CLEANFILES += $(MODSTUBS_O)
```

new/usr/src/uts/intel/usbms/Makefile

2

```
59 #
60 # depends on misc/usba
61 #
62 LDFLAGS += -dy -Nmisc/usba -Nmisc/hidparser
63 #
64 #
65 # For now, disable these lint checks; maintainers should endeavor
66 # to investigate and remove these for maximum lint coverage.
67 # Please do not carry these forward to new Makefiles.
68 #
69 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
70 #
71 CERRWARN += -_gcc=-Wno-parentheses
72 CERRWARN += -_gcc=-Wno-uninitialized
73 #
74 # needs work
75 SMOFF += deref_check
76 #
77 #
78 # Default build targets.
79 #
80 .KEEP_STATE:
81 #
82 def: $(DEF_DEPS)
83 #
84 all: $(ALL_DEPS)
85 #
86 clean: $(CLEAN_DEPS)
87 #
88 clobber: $(CLOBBER_DEPS)
89 #
90 lint: $(LINT_DEPS)
91 #
92 modlintlib: $(MODLINTLIB_DEPS)
93 #
94 clean.lint: $(CLEAN_LINT_DEPS)
95 #
96 install: $(INSTALL_DEPS)
97 #
98 #
99 # Include common targets.
100 #
101 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
1837 Mon Jan 7 13:09:58 2019
new/usr/src/uts/intel/usbsacm/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/usbsacm/Makefile
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.

28 #
27 # This makefile drives the production of Abstract Control Model of
28 # USB Communication Devices Class driver.
29 #
29 # Path to the base of the uts directory tree (usually /usr/src/uts).
30 #
31 UTSBASE = ../../

33 #
34 # Define the module and object file sets.
35 #
36 MODULE = usbsacm
37 OBJECTS = $(USBSACM_OBJS:%=$(OBJDIR)/%)
38 LINTS = $(USBSACM_OBJS:%.o=$(LINTSDIR)/%.ln)
39 ROOTMODULE = $(ROOTDRVDIR)/$(MODULE)

41 #
42 # Include common rules.
43 #
44 include $(UTSBASE)/intel/Makefile.intel

46 LDFLAGS += -dy -Nmisc/usba -Nmisc/usbser

48 CERRWARN += -_gcc=-Wno-uninitialized

50 # needs work
51 SMOFF += deref_check

53 #
54 # Define targets
55 #
56 ALL_TARGET = $(BINARY)
57 LINT_TARGET = $(MODULE).lint
58 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

```

```

60 .KEEP_STATE:

62 all: $(ALL_DEPS)

64 def: $(DEF_DEPS)

66 clean: $(CLEAN_DEPS)

68 clobber: $(CLOBBER_DEPS)

70 lint: $(LINT_DEPS)

72 modlintlib: $(MODLINTLIB_DEPS)

74 clean.lint: $(CLEAN_LINT_DEPS)

76 install: $(INSTALL_DEPS)

78 #
79 # Include common targets.
80 #
81 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/usbser/Makefile

1

\*\*\*\*\*

2085 Mon Jan 7 13:09:59 2019

new/usr/src/uts/intel/usbser/Makefile

10077 usr/src/uts/Makefile changes for smatch

\*\*\*\*\*

```
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.
25 # uts/intel/usbser/Makefile
```

```
28 #
29 # This makefile drives the production of the usbser kernel module.
30 #
31 #
32 # Path to the base of the uts directory tree (usually /usr/src/uts).
33 #
34 UTSBASE = ../..
```

```
36 #
37 # Define the module and object file sets.
38 #
39 MODULE = usbser
40 OBJECTS = $(USBSER_OBJS:%=$(OBJS_DIR)/%)
41 LINTS = $(USBSER_OBJS:%.o=$(LINTS_DIR)/%.ln)
42 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
```

```
44 #
45 # Include common rules.
46 #
47 include $(UTSBASE)/intel/Makefile.intel
```

```
49 #
50 # depends on misc/usba
51 #
52 LDFLAGS += -dy -Nmisc/usba
```

```
54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
```

new/usr/src/uts/intel/usbser/Makefile

2

```
61 #
62 # For now, disable these lint checks; maintainers should endeavor
63 # to investigate and remove these for maximum lint coverage.
64 # Please do not carry these forward to new Makefiles.
65 #
66 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
```

```
68 # needs work
69 SMOFF += all_func_returns
```

```
71 .KEEP_STATE:
```

```
73 all: $(ALL_DEPS)
```

```
75 def: $(DEF_DEPS)
```

```
77 clean: $(CLEAN_DEPS)
```

```
79 clobber: $(CLOBBER_DEPS)
```

```
81 lint: $(LINT_DEPS)
```

```
83 modlintlib: $(MODLINTLIB_DEPS)
```

```
85 clean.lint: $(CLEAN_LINT_DEPS)
```

```
87 install: $(INSTALL_DEPS)
```

```
89 #
90 # Include common targets.
91 #
```

```
92 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/usbvc/Makefile

1

```
*****
2215 Mon Jan 7 13:10:00 2019
new/usr/src/uts/intel/usbvc/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright 2014 Garrett D'Amore <garrett@damore.org>
27 #
28 # Copyright (c) 2018, Joyent, Inc.
29 #
30 #
31 # This makefile drives the production of the usbvc driver kernel module.
32 #
33 #
34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../../
38 #
39 #
40 # Define the module and object file sets.
41 #
42 MODULE = usbvc
43 OBJECTS = ${USBVC_OBJS}=${(OBJS_DIR)}/%
44 LINTS = ${USBVC_OBJS}:%.o=${(LINTS_DIR)}/%.ln
45 ROOTMODULE = ${(ROOT_DRV_DIR)}/${MODULE}
46 #
47 #
48 # Include common rules.
49 #
50 include ${UTSBASE}/intel/Makefile.intel
51 #
52 #
53 # Define targets
54 #
55 ALL_TARGET = ${(BINARY)}
56 LINT_TARGET = ${(MODULE)}.lint
57 INSTALL_TARGET = ${(BINARY)} ${(ROOTMODULE)}
58 #
59 #
60 # Override defaults to build a unique, local modstubs.o.
61 #
```

new/usr/src/uts/intel/usbvc/Makefile

2

```
62 MODSTUBS_DIR = ${(OBJS_DIR)}
63 CLEANFILES += ${(MODSTUBS_O)}
64 #
65 #
66 # depends on misc/usba
67 #
68 LDFLAGS += -dy -Nmisc/usba
69 #
70 #
71 # Disable this because it is necessary for this driver
72 # and it is surely safe
73 #
74 LINTTAGS += -errorf=E_BAD_PTR_CAST_ALIGN
75 #
76 CERRWARN += -_gcc=-Wno-uninitialized
77 #
78 # needs work
79 SMOFF += deref_check
80 #
81 #
82 # Default build targets.
83 #
84 .KEEP_STATE:
85 #
86 def: ${(DEF_DEPS)}
87 #
88 all: ${(ALL_DEPS)}
89 #
90 clean: ${(CLEAN_DEPS)}
91 #
92 clobber: ${(CLOBBER_DEPS)}
93 #
94 lint: ${(LINT_DEPS)}
95 #
96 modlintlib: ${(MODLINTLIB_DEPS)}
97 #
98 clean.lint: ${(CLEAN_LINT_DEPS)}
99 #
100 install: ${(INSTALL_DEPS)}
101 #
102 #
103 # Include common targets.
104 #
105 include ${UTSBASE}/intel/Makefile.targ
```

```

*****
1837 Mon Jan 7 13:10:00 2019
new/usr/src/uts/intel/virtio/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright (c) 2012 Nexenta Systems, Inc. All rights reserved.
24 # Copyright (c) 2018, Joyent, Inc.
25 #
26 #
27 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTSBASE = ../../
31 #
32 #
33 # Define the module and object file sets.
34 #
35 MODULE = virtio
36 OBJECTS = $(VIRTIO_OBJS:%=$(OBJS_DIR)/%)
37 LINTS = $(VIRTIO_OBJS:%.o=$(LINTS_DIR)/%.ln)
38 ROOTMODULE = $(ROOT_MISC_DIR)/$(MODULE)
39 #
40 #
41 # Include common rules.
42 #
43 include $(UTSBASE)/intel/Makefile.intel
44 #
45 #
46 # Define targets
47 #
48 ALL_TARGET = $(BINARY)
49 LINT_TARGET = $(MODULE).lint
50 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
51 #
52 #
53 # Overrides
54 #
55 #
56 INC_PATH += -I$(UTSBASE)/common/io/virtio
57 #
58 #
59 # lint pass one enforcement
60 #
61 CFLAGS += $(CCVERBOSE)

```

```

63 # needs work
64 SMOFF += all_func_returns
65 #
66 #
67 # Default build targets.
68 #
69 .KEEP_STATE:
70 #
71 def: $(DEF_DEPS)
72 #
73 all: $(ALL_DEPS)
74 #
75 clean: $(CLEAN_DEPS)
76 #
77 clobber: $(CLOBBER_DEPS)
78 #
79 lint: $(LINT_DEPS)
80 #
81 modlintlib: $(MODLINTLIB_DEPS)
82 #
83 clean.lint: $(CLEAN_LINT_DEPS)
84 #
85 install: $(INSTALL_DEPS)
86 #
87 #
88 # Include common targets.
89 #
90 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
1969 Mon Jan 7 13:10:01 2019
new/usr/src/uts/intel/vnic/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 #
28 # Path to the base of the uts directory tree (usually /usr/src/uts).
29 #
30 UTSBASE = ../../

32 #
33 # Define the module and object file sets.
34 #
35 MODULE          = vnic
36 OBJECTS         = $(VNIC_OBJS:%=$(OBJDIR)/%)
37 LINTS           = $(VNIC_OBJS:%.o=$(LINTDIR)/%.ln)
38 ROOTMODULE     = $(ROOT_DRV_DIR)/$(MODULE)
39 CONF_SRCDIR    = $(UTSBASE)/common/io/vnic

41 #
42 # Include common rules.
43 #
44 include $(UTSBASE)/intel/Makefile.intel

46 #
47 # Define targets
48 #
49 ALL_TARGET      = $(BINARY) $(SRC_CONFILE)
50 LINT_TARGET     = $(MODULE).lint
51 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONFFILE)

53 #
54 # Overrides
55 #
56 CFLAGS          += $(CCVERBOSE)
57 LDFLAGS         += -dy -Ndrv/dld -Nmisc/mac -Nmisc/dls

59 CERRWARN        += -_gcc=-Wno-switch
60 CERRWARN        += -_gcc=-Wno-uninitialized

```

```

62 # needs work
63 SMOFF += deref_check

65 #
66 #     Default build targets.
67 #
68 .KEEP_STATE:

70 def:          $(DEF_DEPS)

72 all:         $(ALL_DEPS)

74 clean:       $(CLEAN_DEPS)

76 clobber:     $(CLOBBER_DEPS)

78 lint:        $(LINT_DEPS)

80 modlintlib:  $(MODLINTLIB_DEPS)

82 clean.lint:  $(CLEAN_LINT_DEPS)

84 install:     $(INSTALL_DEPS)

86 #
87 #     Include common targets.
88 #
89 include $(UTSBASE)/intel/Makefile.targ

```

```

*****
1992 Mon Jan 7 13:10:02 2019
new/usr/src/uts/intel/vr/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
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7 #
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9 # or http://www.opensolaris.org/os/licensing.
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11 # and limitations under the License.
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13 # When distributing Covered Code, include this CDDL HEADER in each
14 # file and include the License file at usr/src/OPENSOLARIS.LICENSE.
15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # Copyright (c) 2018, Joyent, Inc.

27 # This makefile drives the production of the VIA Rhine Ethernet
28 # (vr) driver module in intel systems
29 #

31 #
32 # Path to the base of the uts directory tree (usually /usr/src/uts).
33 #
34 UTBASE = ../../

36 #
37 # Define the module and object file sets.
38 #
39 MODULE = vr
40 OBJECTS = ${VR_OBJS:%=${OBJDIR}/%}
41 LINTS = ${VR_OBJS:%.o=${LINTSDIR}/%.ln}
42 ROOTMODULE = ${ROOT_DRV_DIR}/${MODULE}

44 #
45 # Include common rules.
46 #
47 include ${UTBASE}/intel/Makefile.intel

49 #
50 # Define targets
51 #
52 ALL_TARGET = ${BINARY}
53 LINT_TARGET = ${MODULE}.lint
54 INSTALL_TARGET = ${BINARY} ${ROOTMODULE}

56 #
57 # Overrides
58 #

60 CERRWARN += -_gcc=-Wno-unused-label
61 CERRWARN += -_gcc=-Wno-uninitialized

```

```

63 # needs work
64 SMOFF += array_condition

66 #
67 # Driver depends on GLD
68 #
69 LDPLAGS += -dy -N misc/mac

71 #
72 # Default build targets.
73 #
74 .KEEP_STATE:

76 def:          ${DEF_DEPS}

78 all:          ${ALL_DEPS}

80 clean:        ${CLEAN_DEPS}

82 clobber:      ${CLOBBER_DEPS}

84 lint:         ${LINT_DEPS}

86 modlintlib:   ${MODLINTLIB_DEPS}

88 clean.lint:   ${CLEAN_LINT_DEPS}

90 install:      ${INSTALL_DEPS}

92 #
93 # Include common targets.
94 #
95 include ${UTBASE}/intel/Makefile.targ

```



new/usr/src/uts/intel/vuid2ps2/Makefile

1

```
*****
2016 Mon Jan 7 13:10:03 2019
new/usr/src/uts/intel/vuid2ps2/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/vuid2ps2/Makefile
23 #
24 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the vuid2ps2 streams kernel
31 # module.
32 #
33 # intel architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTSBASE = ../../

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = vuid2ps2
45 OBJECTS = $(VUIDPS2_OBJS:=$(OBJDIR)/%)
46 LINTS = $(VUIDPS2_OBJS:%.o=$(LINTSDIR)/%.ln)
47 ROOTMODULE = $(ROOTSTRMODDIR)/$(MODULE)

49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 CERRWARN += -_gcc=-Wno-uninitialized
55 CERRWARN += -_gcc=-Wno-parentheses

57 # needs work
58 SMOFF += deref_check

60 #
61 # Define targets
```

new/usr/src/uts/intel/vuid2ps2/Makefile

2

```
62 #
63 ALL_TARGET = $(BINARY)
64 LINT_TARGET = $(MODULE).lint
65 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

67 #
68 # Overrides.
69 #
70 ALL_DEFS += -DVUID2PS2

72 #
73 # Default build targets.
74 #
75 .KEEP_STATE:

77 def: $(DEF_DEPS)

79 all: $(ALL_DEPS)

81 clean: $(CLEAN_DEPS)

83 clobber: $(CLOBBER_DEPS)

85 lint: $(LINT_DEPS)

87 modlintlib: $(MODLINTLIB_DEPS)

89 clean.lint: $(CLEAN_LINT_DEPS)

91 install: $(INSTALL_DEPS)

93 #
94 # Include common targets.
95 #
96 include $(UTSBASE)/intel/Makefile.targ
```

new/usr/src/uts/intel/vuid3ps2/Makefile

1

```
*****
2016 Mon Jan 7 13:10:04 2019
new/usr/src/uts/intel/vuid3ps2/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/vuid3ps2/Makefile
23 #
24 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the vuid3ps2 streams kernel
31 # module.
32 #
33 # intel architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTSBASE = ../../

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = vuid3ps2
45 OBJECTS = $(VUIDPS2_OBJS:=$(OBJDIR)/%)
46 LINTS = $(VUIDPS2_OBJS:%.o=$(LINTSDIR)/%.ln)
47 ROOTMODULE = $(ROOTSTRMODDIR)/$(MODULE)

49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 CERRWARN += -_gcc=-Wno-uninitialized
55 CERRWARN += -_gcc=-Wno-parentheses

57 # needs work
58 SMOFF += deref_check

60 #
61 # Define targets
```

new/usr/src/uts/intel/vuid3ps2/Makefile

2

```
62 #
63 ALL_TARGET = $(BINARY)
64 LINT_TARGET = $(MODULE).lint
65 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

67 #
68 # Overrides.
69 #
70 ALL_DEFS += -DVUID3PS2

72 #
73 # Default build targets.
74 #
75 .KEEP_STATE:

77 def: $(DEF_DEPS)

79 all: $(ALL_DEPS)

81 clean: $(CLEAN_DEPS)

83 clobber: $(CLOBBER_DEPS)

85 lint: $(LINT_DEPS)

87 modlintlib: $(MODLINTLIB_DEPS)

89 clean.lint: $(CLEAN_LINT_DEPS)

91 install: $(INSTALL_DEPS)

93 #
94 # Include common targets.
95 #
96 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
1977 Mon Jan 7 13:10:05 2019
new/usr/src/uts/intel/vuidm3p/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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21 #
22 # uts/intel/vuidm3p/Makefile
23 #
24 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
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26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the vuidm3p streams kernel
31 # module.
32 #
33 # intel architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTSBASE = ../../

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = vuidm3p
45 OBJECTS = $(VUIDM3P_OBJS:%=$(OBJDIR)/%)
46 LINTS = $(VUIDM3P_OBJS:%.o=$(LINTSDIR)/%.ln)
47 ROOTMODULE = $(ROOT_STRMOD_DIR)/$(MODULE)

49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

61 #

```

```

62 # Overrides.
63 #
64 ALL_DEFS += -DVUIDM3P

66 CERRWARN += -_gcc=-Wno-uninitialized

68 # needs work
69 SMOFF += deref_check

71 #
72 # Default build targets.
73 #
74 .KEEP_STATE:

76 def: $(DEF_DEPS)

78 all: $(ALL_DEPS)

80 clean: $(CLEAN_DEPS)

82 clobber: $(CLOBBER_DEPS)

84 lint: $(LINT_DEPS)

86 modlintlib: $(MODLINTLIB_DEPS)

88 clean.lint: $(CLEAN_LINT_DEPS)

90 install: $(INSTALL_DEPS)

92 #
93 # Include common targets.
94 #
95 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/vuidm4p/Makefile

1

```
*****
1977 Mon Jan 7 13:10:05 2019
new/usr/src/uts/intel/vuidm4p/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
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19 # CDDL HEADER END
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21 #
22 # uts/intel/vuidm4p/Makefile
23 #
24 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the vuidm4p streams kernel
31 # module.
32 #
33 # intel architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTSBASE = ../../

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = vuidm4p
45 OBJECTS = $(VUIDM4P_OBJS:%=$(OBJDIR)/%)
46 LINTS = $(VUIDM4P_OBJS:%.o=$(LINTSDIR)/%.ln)
47 ROOTMODULE = $(ROOTSTRMODDIR)/$(MODULE)

49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

61 #
```

new/usr/src/uts/intel/vuidm4p/Makefile

2

```
62 # Overrides.
63 #
64 ALL_DEFS += -DVUIDM4P

66 CERRWARN += -_gcc=-Wno-uninitialized

68 # needs work
69 SMOFF += deref_check

71 #
72 # Default build targets.
73 #
74 .KEEP_STATE:

76 def: $(DEF_DEPS)

78 all: $(ALL_DEPS)

80 clean: $(CLEAN_DEPS)

82 clobber: $(CLOBBER_DEPS)

84 lint: $(LINT_DEPS)

86 modlintlib: $(MODLINTLIB_DEPS)

88 clean.lint: $(CLEAN_LINT_DEPS)

90 install: $(INSTALL_DEPS)

92 #
93 # Include common targets.
94 #
95 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
1960 Mon Jan 7 13:10:06 2019
new/usr/src/uts/intel/vuidm5p/Makefile
10077 usr/src/uts/ Makefile changes for smatch
*****
1 #
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19 # CDDL HEADER END
20 #
21 #
22 # uts/intel/vuidm5p/Makefile
23 #
24 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
25 # Use is subject to license terms.
26 #
27 # Copyright (c) 2018, Joyent, Inc.

29 #
30 # This makefile drives the production of the vuidm5p streams kernel
31 # module.
32 #
33 # intel architecture dependent
34 #

36 #
37 # Path to the base of the uts directory tree (usually /usr/src/uts).
38 #
39 UTSBASE = ../../

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = vuidm5p
45 OBJECTS = $(VUIDM5P_OBJS:%=$(OBJDIR)/%)
46 LINTS = $(VUIDM5P_OBJS:%.o=$(LINTSDIR)/%.ln)
47 ROOTMODULE = $(ROOTSTRMODDIR)/$(MODULE)

49 #
50 # Include common rules.
51 #
52 include $(UTSBASE)/intel/Makefile.intel

54 #
55 # Define targets
56 #
57 ALL_TARGET = $(BINARY)
58 LINT_TARGET = $(MODULE).lint
59 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

61 ALL_DEFS += -DVUIDM5P

```

```

63 CERWARN += -_gcc=-Wno-uninitialized

65 # needs work
66 SMOFF += deref_check

68 #
69 # Default build targets.
70 #
71 .KEEP_STATE:

73 def: $(DEF_DEPS)

75 all: $(ALL_DEPS)

77 clean: $(CLEAN_DEPS)

79 clobber: $(CLOBBER_DEPS)

81 lint: $(LINT_DEPS)

83 modlintlib: $(MODLINTLIB_DEPS)

85 clean.lint: $(CLEAN_LINT_DEPS)

87 install: $(INSTALL_DEPS)

89 #
90 # Include common targets.
91 #
92 include $(UTSBASE)/intel/Makefile.targ

```

new/usr/src/uts/intel/wc/Makefile

1

```
*****
1908 Mon Jan 7 13:10:07 2019
new/usr/src/uts/intel/wc/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
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18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the wc driver
28 # kernel module.
29 #
30 # intel implementation architecture independent
31 #
32 #
33 # Path to the base of the uts directory tree (usually /usr/src/uts).
34 #
35 # UTSBASE = ../../
36 #
37 # Define the module and object file sets.
38 #
39 MODULE = wc
40 OBJECTS = $(WC_OBJS:%=$(OBJDIR)/%)
41 LINTS = $(WC_OBJS:%.o=$(LINTSDIR)/%.ln)
42 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
43 CONF_SRCDIR = $(UTSBASE)/common/io
44 #
45 # Include common rules.
46 #
47 include $(UTSBASE)/intel/Makefile.intel
48 #
49 # Define targets
50 #
51 ALL_TARGET = $(BINARY) $(SRC_CONF_FILE)
52 LINT_TARGET = $(MODULE).lint
53 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOT_CONF_FILE)
54 #
55 # Overrides.
```

new/usr/src/uts/intel/wc/Makefile

2

```
56 #
57 LD_FLAGS += -dy -Nmisc/tem -Ndacf/consconfig_dacf
58 #
59 CERR_WARN += _gcc=-Wno-uninitialized
60 #
61 # needs work
62 SMOFF += deref_check
63 #
64 #
65 # Default build targets.
66 #
67 .KEEP_STATE:
68 #
69 def: $(DEF_DEPS)
70 #
71 all: $(ALL_DEPS)
72 #
73 clean: $(CLEAN_DEPS)
74 #
75 clobber: $(CLOBBER_DEPS)
76 #
77 lint: $(LINT_DEPS)
78 #
79 modlintlib: $(MODLINTLIB_DEPS)
80 #
81 clean.lint: $(CLEAN_LINT_DEPS)
82 #
83 install: $(INSTALL_DEPS)
84 #
85 #
86 # Include common targets.
87 #
88 include $(UTSBASE)/intel/Makefile.targ
```

```

*****
3538 Mon Jan 7 13:10:08 2019
new/usr/src/uts/intel/xge/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
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18 #
19 # CDDL HEADER END
20 #
21 #
22 #
23 # Copyright 2008 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 # Copyright (c) 2018, Joyent, Inc.
27 # This makefile drives the production of the Neterion Xframe
28 # 10G Ethernet (XGE) driver module in x86 systems

28 #
29 # Paths to the base of the uts directory trees
30 #
31 UTSBASE = ../../

33 #
34 # Define the module and object file sets.
35 #
36 MODULE = xge
37 OBJECTS = $(XGE_HAL_OBJS:%=$(OBJS_DIR)/%) $(XGE_OBJS:%=$(OBJS_DIR)/%)
38 LINTS = $(XGE_HAL_OBJS:%.o=$(LINTS_DIR)/%.ln) $(XGE_OBJS:%.o=$(LINTS_D
39 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)

41 #
42 # Include common rules.
43 #
44 include $(UTSBASE)/intel/Makefile.intel

46 #
47 # Define targets
48 #
49 ALL_TARGET = $(BINARY)
50 LINT_TARGET = $(MODULE).lint
51 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)

53 #
54 # GENERAL PURPOSE HAL FLAGS: Tuning HAL for Solaris specific modes
55 #
56 HAL_CFLAGS = -DXGE_HAL_USE_MGMT_AUX

58 #

```

```

59 # TRACE SECTION: Possible values for MODULE, TRACE and ERR masks:
60 #
61 # XGE_COMPONENT_HAL_CONFIG 0x1
62 # XGE_COMPONENT_HAL_FIFO 0x2
63 # XGE_COMPONENT_HAL_RING 0x4
64 # XGE_COMPONENT_HAL_CHANNEL 0x8
65 # XGE_COMPONENT_HAL_DEVICE 0x10
66 # XGE_COMPONENT_HAL_MM 0x20
67 # XGE_COMPONENT_HAL_QUEUE 0x40
68 # XGE_COMPONENT_HAL_STATS 0x100
69 # XGE_COMPONENT_OSDEP 0x1000
70 # XGE_COMPONENT_LL 0x2000
71 # XGE_COMPONENT_TOE 0x4000
72 # XGE_COMPONENT_RDMA 0x8000
73 # XGE_COMPONENT_ALL 0xffffffff
74 #TRACE_CFLAGS = -DXGE_DEBUG_MODULE_MASK=0xffffffff \
75 # -DXGE_DEBUG_TRACE_MASK=0xffffffff \
76 # -DXGE_DEBUG_ERR_MASK=0xffffffff
77 TRACE_CFLAGS = -DXGE_DEBUG_MODULE_MASK=0x00003010 \
78 -DXGE_DEBUG_TRACE_MASK=0x00000000 \
79 -DXGE_DEBUG_ERR_MASK=0x00003010

81 XGE_CFLAGS = $(HAL_CFLAGS) $(TRACE_CFLAGS) $(CCVERBOSE) \
82 -I$(UTSBASE)/common/io/xge/hal/include \
83 -I$(UTSBASE)/common/io/xge/hal/xgehal \
84 -I$(UTSBASE)/common/io/xge/drv -DSOLARIS

86 CFLAGS += $(XGE_CFLAGS) -xO4 -xcrossfile
87 CFLAGS64 += $(XGE_CFLAGS) -xO4 -xcrossfile

89 #
90 # Driver depends on MAC & IP
91 #
92 LDFLAGS += -dy -N misc/mac -N drv/ip

94 # Lint flag
95 #
96 LINTFLAGS += $(XGE_CFLAGS) -Xc99=%all

98 #
99 # For now, disable these lint checks; maintainers should endeavor
100 # to investigate and remove these for maximum lint coverage.
101 # Please do not carry these forward to new Makefiles.
102 #
103 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
104 LINTTAGS += -erroff=E_STATIC_UNUSED
105 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW

107 CERRWARN += -_gcc=-Wno-parentheses
108 CERRWARN += -_gcc=-Wno-unused-variable
109 CERRWARN += -_gcc=-Wno-unused-label
110 CERRWARN += -_gcc=-Wno-empty-body
111 CERRWARN += -_gcc=-Wno-uninitialized

113 # needs work
114 SMOFF += indenting,all_func_returns,no_if_block

116 #
117 #
118 # Default build targets.
119 #
120 .KEEP_STATE:

122 def: $(DEF_DEPS)

124 all: $(ALL_DEPS)

```

```
126 clean:          $(CLEAN_DEPS)
128 clobber:        $(CLOBBER_DEPS)
130 lint:           $(LINT_DEPS)
132 modlintlib:     $(MODLINTLIB_DEPS)
134 clean.lint:     $(CLEAN_LINT_DEPS)
136 install:        $(INSTALL_DEPS)

138 #
139 #       Include common targets.
140 #
141 include $(UTSBASE)/intel/Makefile.targ
```



```

*****
3525 Mon Jan 7 13:10:09 2019
new/usr/src/uts/intel/zfs/Makefile
10077 usr/src/uts/Makefile changes for smatch
*****
1 #
2 # CDDL HEADER START
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18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
23 # Use is subject to license terms.
24 #
25 # This makefile drives the production of the zfs file system
26 # kernel module.
27 #
28 # Copyright 2013 Saso Kiselkov. All rights reserved.
29 #
30 # Copyright (c) 2016 by Delphix. All rights reserved.
31 #
32 # Copyright (c) 2018, Joyent, Inc.

34 #
35 # Path to the base of the uts directory tree (usually /usr/src/uts).
36 #
37 UTSBASE = ../../

39 ARCHDIR:sh = cd ../; basename `pwd`

41 #
42 # Define the module and object file sets.
43 #
44 MODULE = zfs
45 OBJECTS = $(ZFS_OBJS:%=$(OBJS_DIR)/%) $(LUA_OBJS:%=$(OBJS_DIR)/%)
46 # LUA_OBJS are intentionally omitted from LINTS
47 LINTS = $(ZFS_OBJS:%.o=$(LINTS_DIR)/%.ln)
48 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
49 ROOTLINK = $(ROOT_FS_DIR)/$(MODULE)
50 CONF_SRCDIR = $(UTSBASE)/common/fs/zfs

52 #
53 # Include common rules.
54 #
55 include ../Makefile.$(ARCHDIR)

57 #
58 # Define targets
59 #
60 ALL_TARGET = $(BINARY) $(SRC_CONFFILE)
61 LINT_TARGET = $(MODULE).lint

```

```

62 INSTALL_TARGET = $(BINARY) $(ROOTMODULE) $(ROOTLINK) $(ROOT_CONFFILE)

64 #
65 # Overrides and depends_on
66 #
67 MODSTUBS_DIR = $(OBJS_DIR)
68 LDFLAGS += -dy -Nfs/specfs -Ncrypto/swrand -Nmisc/idmap -Nmisc/sha2 \
69 -Nmisc/skein -Nmisc/edonr

71 INC_PATH += -I$(UTSBASE)/common/fs/zfs
72 INC_PATH += -I$(UTSBASE)/common/fs/zfs/lua
73 INC_PATH += -I$(SRC)/common
74 INC_PATH += -I$(COMMONBASE)/zfs

76 C99LMODE= -Xc99=%all

78 #
79 # For now, disable these lint checks; maintainers should endeavor
80 # to investigate and remove these for maximum lint coverage.
81 # Please do not carry these forward to new Makefiles.
82 #
83 LINTTAGS += -erroff=E_SUSPICIOUS_COMPARISON
84 LINTTAGS += -erroff=E_BAD_PTR_CAST_ALIGN
85 LINTTAGS += -erroff=E_SUPPRESSION_DIRECTIVE_UNUSED
86 LINTTAGS += -erroff=E_STATIC_UNUSED
87 LINTTAGS += -erroff=E_PTRDIFF_OVERFLOW
88 LINTTAGS += -erroff=E_ASSIGN_NARROW_CONV

90 CERRWARN += -_gcc=-Wno-type-limits
91 CERRWARN += -_gcc=-Wno-switch
92 CERRWARN += -_gcc=-Wno-parentheses
93 CERRWARN += -_gcc=-Wno-unused-variable
94 CERRWARN += -_gcc=-Wno-unused-function
95 CERRWARN += -_gcc=-Wno-unused-label

97 # needs work
98 SMOFF += all_func_returns,indenting
99 $(OBJS_DIR)/llex.o := SMOFF += index_overflow
100 $(OBJS_DIR)/metaslab.o := SMOFF += no_if_block
101 $(OBJS_DIR)/zfs_vnops.o := SMOFF += signed
102 # needs work
103 $(OBJS_DIR)/zvol.o := SMOFF += deref_check,signed

105 # false positives
106 $(OBJS_DIR)/zfs_ctldir.o := SMOFF += strcpy_overflow
107 $(OBJS_DIR)/zfs_ioctl.o := SMOFF += strcpy_overflow

109 #
110 # Default build targets.
111 #
112 .KEEP_STATE:

114 def: $(DEF_DEPS)

116 all: $(ALL_DEPS)

118 clean: $(CLEAN_DEPS)

120 clobber: $(CLOBBER_DEPS)

122 lint: $(LINT_DEPS)

124 modlintlib: $(MODLINTLIB_DEPS)

126 clean.lint: $(CLEAN_LINT_DEPS)

```

**new/usr/src/uts/intel/zfs/Makefile**

3

```
128 install:          $(INSTALL_DEPS)
130 $(ROOTLINK):      $(ROOT_FS_DIR) $(ROOTMODULE)
131                  -$(RM) $@; ln $(ROOTMODULE) $@
133 #
134 #          Include common targets.
135 #
136 include ../Makefile.targ
```