

```

*****
2511 Fri Jul 7 20:18:15 2017
new/usr/src/uts/intel/rwn/Makefile
8465 aggressive-loop-optimizations error in rt2860.c
*****
1 #
2 # CDDL HEADER START
3 #
4 # The contents of this file are subject to the terms of the
5 # Common Development and Distribution License (the "License").
6 # You may not use this file except in compliance with the License.
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15 # If applicable, add the following below this CDDL HEADER, with the
16 # fields enclosed by brackets "[]" replaced with your own identifying
17 # information: Portions Copyright [yyyy] [name of copyright owner]
18 #
19 # CDDL HEADER END
20 #
21 #
22 # Copyright 2017 Gary Mills
23 # Copyright 2009 Sun Microsystems, Inc. All rights reserved.
24 # Use is subject to license terms.
25 #
26 #
27 #
28 # This makefile drives the production of the wpi driver kernel module.
29 #
30 # i86pc architecture dependent
31 #
32 #
33 #
34 # Path to the base of the uts directory tree (usually /usr/src/uts).
35 #
36 UTBASE = ../../
37 #
38 #
39 # Define the module and object file sets.
40 #
41 MODULE      = rwn
42 OBJECTS     = $(RWN_OBJS:%=$(OBJS_DIR)/%)
43 LINTS       = $(RWN_OBJS:%.o=$(LINTS_DIR)/%.ln)
44 ROOTMODULE = $(ROOT_DRV_DIR)/$(MODULE)
45 #
46 #
47 # Include common rules.
48 #
49 include $(UTSBASE)/intel/Makefile.intel
50 #
51 #
52 # Define targets
53 #
54 ALL_TARGET = $(BINARY)
55 LINT_TARGET = $(MODULE).lint
56 INSTALL_TARGET = $(BINARY) $(ROOTMODULE)
57 #
58 #
59 # Driver depends on GLDv3 & wifi kernel support module.
60 #
61 LDFLAGS    += -dy -Nmisc/mac -Nmisc/net80211

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63 #
64 # Overrides
65 #
66 LINTTAGS      += -erroff=E_BAD_PTR_CAST_ALIGN
67 #
68 CERRWARN      += -_gcc=-Wno-unused-label
69 CERRWARN      += -_gcc=-Wno-parentheses
70 CERRWARN      += -_gcc=-Wno-unused-value
71 #
72 # Prevent the error with gcc-4.9.4: iteration 35u invokes undefined behavior
73 GCC_VERS:sh = ${GCC_ROOT}/bin/gcc --version | \
74     head -1 | \
75     sed -e 's/([^\ ]*)//g' -e 's/^\.* //'
76 ADD.$(GCC_VERS) = -_gcc=-fno-aggressive-loop-optimizations
77 ADD.4.4.4 =
78 ADD.3.4.3 =
79 $(OBJS_DIR)/rt2860.o := CFLAGS += $(ADD.$(GCC_VERS))
80 #
81 #
82 # Default build targets.
83 #
84 .KEEP_STATE:
85 #
86 def:          $(DEF_DEPS)
87 #
88 all:          $(ALL_DEPS)
89 #
90 clean:        $(CLEAN_DEPS)
91 #
92 clobber:      $(CLOBBER_DEPS)
93 #
94 lint:         $(LINT_DEPS)
95 #
96 modlintlib:  $(MODLINTLIB_DEPS)
97 #
98 clean.lint:   $(CLEAN_LINT_DEPS)
99 #
100 install:     $(INSTALL_DEPS)
101 #
102 #
103 # Include common targets.
104 #
105 include $(UTSBASE)/intel/Makefile.targ
106 #
107 #
108 # If you have any special case that general
109 # Makefile rules don't serve for you, just do
110 # it yourself.
111 #

```