```
**********************************************************
    2645 Sun Dec 16 10:46:50 2012
new/usr/src/pkg/manifests/driver-serial-usbftdi.mf
3419 usbftdi needs to support the BeagleBone
**********************************************************
#
# CDDL HEADER START
#
# The contents of this file are subject to the terms of the
# Common Development and Distribution License (the "License").
# You may not use this file except in compliance with the License.
#
# You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
# or http://www.opensolaris.org/os/licensing.
# See the License for the specific language governing permissions
# and limitations under the License.
#
# When distributing Covered Code, include this CDDL HEADER in each
# file and include the License file at usr/src/OPENSOLARIS.LICENSE.
# If applicable, add the following below this CDDL HEADER, with the
# fields enclosed by brackets "[]" replaced with your own identifying
# information: Portions Copyright [yyyy] [name of copyright owner]
#
# CDDL HEADER END
#

#
# Copyright (c) 2010, Oracle and/or its affiliates. All rights reserved.
#

#
# The default for payload-bearing actions in this package is to appear in the
# global zone only.  See the include file for greater detail, as well as
# information about overriding the defaults.
#
<include global_zone_only_component>
set name=pkg.fmri value=pkg:/driver/serial/usbftdi@$(PKGVERS)
set name=pkg.description value="FTDI FT232R USB serial driver"
set name=pkg.summary value="FT232R USB UART"
set name=info.classification value=org.opensolaris.category.2008:Drivers/Ports
set name=variant.arch value=$(ARCH)
dir path=kernel group=sys
dir path=kernel/drv group=sys
dir path=kernel/drv/$(ARCH64) group=sys
dir path=usr/share/man
dir path=usr/share/man/man7d
driver name=usbftdi perms="* 0666 root sys" \
    alias=usb403,6001 \
    alias=usb403,cc48 \
    alias=usb403,cc49 \
    alias=usb403,cc4a \
    alias=usb403,e888 \
    alias=usb403,e889 \
    alias=usb403,e88b \
    alias=usb403,e88c \
    alias=usb403,fa00 \
    alias=usb403,fa01 \
    alias=usb403,fa02 \
    alias=usb403,fa03 \
    alias=usb403,fa04 \
    alias=usb403,fc08 \
    alias=usb403,fc09 \
    alias=usb403,fc0b \
    alias=usb403,fc0c \
    alias=usb403,fc0d \
    alias=usb403,fc82 \
```



```
    alias=usb411,00b3 \
    alias=usb7cc,0421 \
    alias=usb856,ac01 \
    alias=usb93c,0601 \
    alias=usb93c,0701 \
    alias=usbif403,6010.config1.1 \
    alias=usbif9e88,9e8f.config1.1
file path=kernel/drv/$(ARCH64)/usbftdi group=sys
$(i386_ONLY)file path=kernel/drv/usbftdi group=sys
file path=kernel/drv/usbftdi.conf group=sys \
    original_name=SUNWuftdi:kernel/drv/usbftdi.conf preserve=true
file path=usr/share/man/man7d/usbftdi.7d
legacy pkg=SUNWuftdi desc="FTDI FT232R USB serial driver" \
    name="FT232R USB UART"
license cr_Sun license=cr_Sun
license lic_CDDL license=lic_CDDL
```

```
**********************************************************
   48130 Sun Dec 16 10:46:51 2012
new/usr/src/uts/common/io/usb/clients/usbser/usbftdi/uftdi_dsd.c
3419 usbftdi needs to support the BeagleBone
**********************************************************
```

```
/*
 * CDDL HEADER START
 *
 * The contents of this file are subject to the terms of the
 * Common Development and Distribution License (the "License").
 * You may not use this file except in compliance with the License.
 *
 * You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
 * or http://www.opensolaris.org/os/licensing.
 * See the License for the specific language governing permissions
 * and limitations under the License.
 *
 * When distributing Covered Code, include this CDDL HEADER in each
 * file and include the License file at usr/src/OPENSOLARIS.LICENSE.
 * If applicable, add the following below this CDDL HEADER, with the
 * fields enclosed by brackets "[]" replaced with your own identifying
 * information: Portions Copyright [yyyy] [name of copyright owner]
 *
 * CDDL HEADER END
 */

/*
 * Copyright 2009 Sun Microsystems, Inc.  All rights reserved.
 * Use is subject to license terms.
 */

/*
 * Copyright 2012 Hans Rosenfeld <rosenfeld@grumpf.hope-2000.org>
 */

/*
 * FTDI FT232R USB UART device-specific driver
 *
 * May work on the (many) devices based on earlier versions of the chip.
 */

#include <sys/types.h>
#include <sys/param.h>
#include <sys/conf.h>
#include <sys/stream.h>
#include <sys/strsun.h>
#include <sys/termio.h>
#include <sys/termiox.h>
#include <sys/ddi.h>
#include <sys/sunddi.h>

#define USBDRV_MAJOR_VER        2
#define USBDRV_MINOR_VER        0

#include <sys/usb/usba.h>
#include <sys/usb/usba/usba_types.h>
#include <sys/usb/usba/usba_impl.h>

#include <sys/usb/clients/usbser/usbser_dsdi.h>
#include <sys/usb/clients/usbser/usbftdi/uftdi_var.h>
#include <sys/usb/clients/usbser/usbftdi/uftdi_reg.h>

#include <sys/usb/usbdevs.h>

/*
 * DSD operations
```



```
 */
static int      uftdi_attach(ds_attach_info_t *);
static void     uftdi_detach(ds_hdl_t);
static int      uftdi_register_cb(ds_hdl_t, uint_t, ds_cb_t *);
static void     uftdi_unregister_cb(ds_hdl_t, uint_t);
static int      uftdi_open_port(ds_hdl_t, uint_t);
static int      uftdi_close_port(ds_hdl_t, uint_t);

/* power management */
static int      uftdi_usb_power(ds_hdl_t, int, int, int *);
static int      uftdi_suspend(ds_hdl_t);
static int      uftdi_resume(ds_hdl_t);
static int      uftdi_disconnect(ds_hdl_t);
static int      uftdi_reconnect(ds_hdl_t);

/* standard UART operations */
static int      uftdi_set_port_params(ds_hdl_t, uint_t, ds_port_params_t *);
static int      uftdi_set_modem_ctl(ds_hdl_t, uint_t, int, int);
static int      uftdi_get_modem_ctl(ds_hdl_t, uint_t, int, int *);
static int      uftdi_break_ctl(ds_hdl_t, uint_t, int);

/* data xfer */
static int      uftdi_tx(ds_hdl_t, uint_t, mblk_t *);
static mblk_t   *uftdi_rx(ds_hdl_t, uint_t);
static void     uftdi_stop(ds_hdl_t, uint_t, int);
static void     uftdi_start(ds_hdl_t, uint_t, int);
static int      uftdi_fifo_flush(ds_hdl_t, uint_t, int);
static int      uftdi_fifo_drain(ds_hdl_t, uint_t, int);

/* polled I/O support */
static usb_pipe_handle_t uftdi_out_pipe(ds_hdl_t, uint_t);
static usb_pipe_handle_t uftdi_in_pipe(ds_hdl_t, uint_t);

/*
 * Sub-routines
 */

/* configuration routines */
static void     uftdi_cleanup(uftdi_state_t *, int);
static int      uftdi_dev_attach(uftdi_state_t *);
static int      uftdi_open_hw_port(uftdi_state_t *, int);

/* hotplug */
static int      uftdi_restore_device_state(uftdi_state_t *);
static int      uftdi_restore_port_state(uftdi_state_t *);

/* power management */
static int      uftdi_create_pm_components(uftdi_state_t *);
static void     uftdi_destroy_pm_components(uftdi_state_t *);
static int      uftdi_pm_set_busy(uftdi_state_t *);
static void     uftdi_pm_set_idle(uftdi_state_t *);
static int      uftdi_pwrlvl0(uftdi_state_t *);
static int      uftdi_pwrlvl1(uftdi_state_t *);
static int      uftdi_pwrlvl2(uftdi_state_t *);
static int      uftdi_pwrlvl3(uftdi_state_t *);

/* pipe operations */
static int      uftdi_open_pipes(uftdi_state_t *);
static void     uftdi_close_pipes(uftdi_state_t *);
static void     uftdi_disconnect_pipes(uftdi_state_t *);
static int      uftdi_reconnect_pipes(uftdi_state_t *);

/* pipe callbacks */
static void     uftdi_bulkin_cb(usb_pipe_handle_t, usb_bulk_req_t *);
static void     uftdi_bulkout_cb(usb_pipe_handle_t, usb_bulk_req_t *);
```

```
128 /* data transfer routines */
129 static int      uftdi_rx_start(uftdi_state_t *);
130 static void     uftdi_tx_start(uftdi_state_t *, int *);
131 static int      uftdi_send_data(uftdi_state_t *, mblk_t *);
132 static int      uftdi_wait_tx_drain(uftdi_state_t *, int);

134 /* vendor-specific commands */
135 static int      uftdi_cmd_vendor_write0(uftdi_state_t *,
136                     uint16_t, uint16_t, uint16_t);

138 /* misc */
139 static void     uftdi_put_tail(mblk_t **, mblk_t *);
140 static void     uftdi_put_head(mblk_t **, mblk_t *);


143 /*
144  * DSD ops structure
145  */
146 ds_ops_t uftdi_ds_ops = {
147         DS_OPS_VERSION,
148         uftdi_attach,
149         uftdi_detach,
150         uftdi_register_cb,
151         uftdi_unregister_cb,
152         uftdi_open_port,
153         uftdi_close_port,
154         uftdi_usb_power,
155         uftdi_suspend,
156         uftdi_resume,
157         uftdi_disconnect,
158         uftdi_reconnect,
159         uftdi_set_port_params,
160         uftdi_set_modem_ctl,
161         uftdi_get_modem_ctl,
162         uftdi_break_ctl,
163         NULL,                   /* no loopback support */
164         uftdi_tx,
165         uftdi_rx,
166         uftdi_stop,
167         uftdi_start,
168         uftdi_fifo_flush,
169         uftdi_fifo_drain,
170         uftdi_out_pipe,
171         uftdi_in_pipe
172 };

174 /* debug support */
175 static uint_t   uftdi_errlevel = USB_LOG_L4;
176 static uint_t   uftdi_errmask = DPRINT_MASK_ALL;
177 static uint_t   uftdi_instance_debug = (uint_t)-1;
178 static uint_t   uftdi_attach_unrecognized = B_FALSE;

180 /*
181  * ds_attach
182  */
183 static int
184 uftdi_attach(ds_attach_info_t *aip)
185 {
186         uftdi_state_t *uf;
187         usb_dev_descr_t *dd;
188         int recognized;

190         uf = kmem_zalloc(sizeof (*uf), KM_SLEEP);
191         uf->uf_dip = aip->ai_dip;
192         uf->uf_usb_events = aip->ai_usb_events;
193         *aip->ai_hdl = (ds_hdl_t)uf;
```



```
195         /* only one port */
196         *aip->ai_port_cnt = 1;

198         if (usb_client_attach(uf->uf_dip, USBDRV_VERSION, 0) != USB_SUCCESS) {
199                 uftdi_cleanup(uf, 1);
200                 return (USB_FAILURE);
201         }

203         if (usb_get_dev_data(uf->uf_dip,
204             &uf->uf_dev_data, USB_PARSE_LVL_IF, 0) != USB_SUCCESS) {
205                 uftdi_cleanup(uf, 2);
206                 return (USB_FAILURE);
207         }

209         uf->uf_hwport = FTDI_PIT_SIOA + uf->uf_dev_data->dev_curr_if;

211         mutex_init(&uf->uf_lock, NULL, MUTEX_DRIVER,
212             uf->uf_dev_data->dev_iblock_cookie);

214         cv_init(&uf->uf_tx_cv, NULL, CV_DRIVER, NULL);

216         uf->uf_lh = usb_alloc_log_hdl(uf->uf_dip, "uftdi",
217             &uftdi_errlevel, &uftdi_errmask, &uftdi_instance_debug, 0);

219         /*
220          * This device and its clones has numerous physical instantiations.
221          */
222         recognized = B_TRUE;
223         dd = uf->uf_dev_data->dev_descr;
224         switch (dd->idVendor) {
225         case USB_VENDOR_FTDI:
226                 switch (dd->idProduct) {
227                 case USB_PRODUCT_FTDI_SERIAL_2232C:
228                 case USB_PRODUCT_FTDI_SERIAL_8U232AM:
229                 case USB_PRODUCT_FTDI_SEMC_DSS20:
230                 case USB_PRODUCT_FTDI_CFA_631:
231                 case USB_PRODUCT_FTDI_CFA_632:
232                 case USB_PRODUCT_FTDI_CFA_633:
233                 case USB_PRODUCT_FTDI_CFA_634:
234                 case USB_PRODUCT_FTDI_CFA_635:
235                 case USB_PRODUCT_FTDI_USBSERIAL:
236                 case USB_PRODUCT_FTDI_MX2_3:
237                 case USB_PRODUCT_FTDI_MX4_5:
238                 case USB_PRODUCT_FTDI_LK202:
239                 case USB_PRODUCT_FTDI_LK204:
240                 case USB_PRODUCT_FTDI_TACTRIX_OPENPORT_13M:
241                 case USB_PRODUCT_FTDI_TACTRIX_OPENPORT_13S:
242                 case USB_PRODUCT_FTDI_TACTRIX_OPENPORT_13U:
243                 case USB_PRODUCT_FTDI_EISCOU:
244                 case USB_PRODUCT_FTDI_UOPTBR:
245                 case USB_PRODUCT_FTDI_EMCU2D:
246                 case USB_PRODUCT_FTDI_PCMSFU:
247                 case USB_PRODUCT_FTDI_EMCU2H:
248                         break;
249                 default:
250                         recognized = B_FALSE;
251                         break;
252                 }
253                 break;
254         case USB_VENDOR_SIIG2:
255                 switch (dd->idProduct) {
256                 case USB_PRODUCT_SIIG2_US2308:
257                         break;
258                 default:
259                         recognized = B_FALSE;
```

```
260                         break;
261                 }
262                 break;
263         case USB_VENDOR_INTREPIDCS:
264                 switch (dd->idProduct) {
265                 case USB_PRODUCT_INTREPIDCS_VALUECAN:
266                 case USB_PRODUCT_INTREPIDCS_NEOVI:
267                         break;
268                 default:
269                         recognized = B_FALSE;
270                         break;
271                 }
272                 break;
273         case USB_VENDOR_BBELECTRONICS:
274                 switch (dd->idProduct) {
275                 case USB_PRODUCT_BBELECTRONICS_USOTL4:
276                         break;
277                 default:
278                         recognized = B_FALSE;
279                         break;
280                 }
281                 break;
282         case USB_VENDOR_MELCO:
283                 switch (dd->idProduct) {
284                 case USB_PRODUCT_MELCO_PCOPRS1:
285                         break;
286                 default:
287                         recognized = B_FALSE;
288                         break;
289                 }
290                 break;
291         case USB_VENDOR_MARVELL:
292                 switch (dd->idProduct) {
293                 case USB_PRODUCT_MARVELL_SHEEVAPLUG_JTAG:
294                         break;
295                 default:
296                         recognized = B_FALSE;
297                         break;
298                 }
299                 break;
300         default:
301                 recognized = B_FALSE;
302                 break;
303         }

305         /*
306          * Set 'uftdi_attach_unrecognized' to non-zero to
307          * experiment with newer devices ..
308          */
309         if (!recognized && !uftdi_attach_unrecognized) {
310                 uftdi_cleanup(uf, 3);
311                 return (USB_FAILURE);
312         }

314         USB_DPRINTF_L3(DPRINT_ATTACH, uf->uf_lh,
315             "uftdi: matched vendor 0x%x product 0x%x port %d",
316             dd->idVendor, dd->idProduct, uf->uf_hwport);

318         uf->uf_def_ph = uf->uf_dev_data->dev_default_ph;

320         mutex_enter(&uf->uf_lock);
321         uf->uf_dev_state = USB_DEV_ONLINE;
322         uf->uf_port_state = UFTDI_PORT_CLOSED;
323         mutex_exit(&uf->uf_lock);

325         if (uftdi_create_pm_components(uf) != USB_SUCCESS) {
```



```
326                 uftdi_cleanup(uf, 3);
327                 return (USB_FAILURE);
328         }

330         if (usb_register_event_cbs(uf->uf_dip,
331             uf->uf_usb_events, 0) != USB_SUCCESS) {
332                 uftdi_cleanup(uf, 4);
333                 return (USB_FAILURE);
334         }

331         if (usb_pipe_get_max_bulk_transfer_size(uf->uf_dip,
332             &uf->uf_xfer_sz) != USB_SUCCESS) {
333                 uftdi_cleanup(uf, 5);
334                 return (USB_FAILURE);
335         }

337         /*
338          * TODO: modern ftdi devices have deeper (and asymmetric)
339          * fifos than this minimal 64 bytes .. but how to tell
340          * -safely- ?
341          */

343 #define FTDI_MAX_XFERSIZE       64

345         if (uf->uf_xfer_sz > FTDI_MAX_XFERSIZE)
346                 uf->uf_xfer_sz = FTDI_MAX_XFERSIZE;

336         if (uftdi_dev_attach(uf) != USB_SUCCESS) {
337                 uftdi_cleanup(uf, 5);
338                 return (USB_FAILURE);
339         }

341         return (USB_SUCCESS);
342 }
```

_____***unchanged_portion_omitted_***

```
1483 /*
1484  * pipe operations
1485  */
1486 static int
1487 uftdi_open_pipes(uftdi_state_t *uf)
1488 {
1489         int ifc, alt;
1490         usb_pipe_policy_t policy;
1491         usb_ep_data_t *in_data, *out_data;
1492         size_t max_xfer_sz;

1494         /* get max transfer size */
1495         if (usb_pipe_get_max_bulk_transfer_size(uf->uf_dip, &max_xfer_sz)
1496             != USB_SUCCESS)
1497                 return (USB_FAILURE);

1499         /* get ep data */
1500         ifc = uf->uf_dev_data->dev_curr_if;
1501         alt = 0;

1503         in_data = usb_lookup_ep_data(uf->uf_dip, uf->uf_dev_data, ifc, alt,
1504             0, USB_EP_ATTR_BULK, USB_EP_DIR_IN);

1506         out_data = usb_lookup_ep_data(uf->uf_dip, uf->uf_dev_data, ifc, alt,
1507             0, USB_EP_ATTR_BULK, USB_EP_DIR_OUT);

1509         if (in_data == NULL || out_data == NULL) {
1510                 USB_DPRINTF_L2(DPRINT_ATTACH, uf->uf_lh,
1511                     "uftdi_open_pipes: can't get ep data");
```

```
1512                return (USB_FAILURE);
1513        }

1515        /*
1516         * Set buffer sizes. Default to UFTDI_XFER_SZ_MAX.
1517         * Use wMaxPacketSize from endpoint descriptor if it is nonzero..
1518         * Cap at a max transfer size of host controller.
1519         */
1520        uf->uf_ibuf_sz = uf->uf_obuf_sz = UFTDI_XFER_SZ_MAX;

1522        if (in_data->ep_descr.wMaxPacketSize)
1523                uf->uf_ibuf_sz = in_data->ep_descr.wMaxPacketSize;
1524        uf->uf_ibuf_sz = min(uf->uf_ibuf_sz, max_xfer_sz);

1526        if (out_data->ep_descr.wMaxPacketSize)
1527                uf->uf_obuf_sz = out_data->ep_descr.wMaxPacketSize;
1528        uf->uf_obuf_sz = min(uf->uf_obuf_sz, max_xfer_sz);

1530        /* open pipes */
1531        policy.pp_max_async_reqs = 2;

1533        if (usb_pipe_open(uf->uf_dip, &in_data->ep_descr, &policy,
1534            USB_FLAGS_SLEEP, &uf->uf_bulkin_ph) != USB_SUCCESS)
1535                return (USB_FAILURE);

1537        if (usb_pipe_open(uf->uf_dip, &out_data->ep_descr, &policy,
1538            USB_FLAGS_SLEEP, &uf->uf_bulkout_ph) != USB_SUCCESS) {
1539                usb_pipe_close(uf->uf_dip, uf->uf_bulkin_ph, USB_FLAGS_SLEEP,
1540                    NULL, NULL);
1541                return (USB_FAILURE);
1542        }

1544        mutex_enter(&uf->uf_lock);
1545        uf->uf_bulkin_state = UFTDI_PIPE_IDLE;
1546        uf->uf_bulkout_state = UFTDI_PIPE_IDLE;
1547        mutex_exit(&uf->uf_lock);

1549        return (USB_SUCCESS);
1550 }
```

*unchanged_portion_omitted_*

```
1802 /*
1803  * start receiving data
1804  */
1805 static int
1806 uftdi_rx_start(uftdi_state_t *uf)
1807 {
1808        usb_bulk_req_t *br;
1809        int rval;

1811        USB_DPRINTF_L4(DPRINT_OUT_PIPE, uf->uf_lh, "uftdi_rx_start");

1813        ASSERT(mutex_owned(&uf->uf_lock));

1815        uf->uf_bulkin_state = UFTDI_PIPE_BUSY;
1816        mutex_exit(&uf->uf_lock);

1818        br = usb_alloc_bulk_req(uf->uf_dip, uf->uf_ibuf_sz, USB_FLAGS_SLEEP);
1819        br->bulk_len = uf->uf_ibuf_sz;
1809        br = usb_alloc_bulk_req(uf->uf_dip, uf->uf_xfer_sz, USB_FLAGS_SLEEP);
1810        br->bulk_len = uf->uf_xfer_sz;
1820        br->bulk_timeout = UFTDI_BULKIN_TIMEOUT;
1821        br->bulk_cb = uftdi_bulkin_cb;
1822        br->bulk_exc_cb = uftdi_bulkin_cb;
1823        br->bulk_client_private = (usb_opaque_t)uf;
```



```
1824        br->bulk_attributes = USB_ATTRS_AUTOCLEARING | USB_ATTRS_SHORT_XFER_OK;

1826        rval = usb_pipe_bulk_xfer(uf->uf_bulkin_ph, br, 0);

1828        if (rval != USB_SUCCESS) {
1829                USB_DPRINTF_L2(DPRINT_IN_PIPE, uf->uf_lh,
1830                    "uftdi_rx_start: xfer failed %d", rval);
1831                usb_free_bulk_req(br);
1832        }

1834        mutex_enter(&uf->uf_lock);
1835        if (rval != USB_SUCCESS)
1836                uf->uf_bulkin_state = UFTDI_PIPE_IDLE;

1838        return (rval);
1839 }


1842 /*
1843  * start data transmit
1844  */
1845 static void
1846 uftdi_tx_start(uftdi_state_t *uf, int *xferd)
1847 {
1848        int             len;            /* bytes we can transmit */
1849        mblk_t          *data;          /* data to be transmitted */
1850        int             data_len;       /* bytes in 'data' */
1851        mblk_t          *mp;            /* current msgblk */
1852        int             copylen;        /* bytes copy from 'mp' to 'data' */
1853        int             rval;

1855        USB_DPRINTF_L4(DPRINT_OUT_PIPE, uf->uf_lh, "uftdi_tx_start");
1856        ASSERT(mutex_owned(&uf->uf_lock));
1857        ASSERT(uf->uf_port_state != UFTDI_PORT_CLOSED);

1859        if (xferd)
1860                *xferd = 0;
1861        if ((uf->uf_port_flags & UFTDI_PORT_TX_STOPPED) ||
1862            uf->uf_tx_mp == NULL) {
1863                return;
1864        }
1865        if (uf->uf_bulkout_state != UFTDI_PIPE_IDLE) {
1866                USB_DPRINTF_L4(DPRINT_OUT_PIPE, uf->uf_lh,
1867                    "uftdi_tx_start: pipe busy");
1868                return;
1869        }
1870        ASSERT(MBLKL(uf->uf_tx_mp) > 0);

1872        /* send as much data as port can receive */
1873        len = min(msgdsize(uf->uf_tx_mp), uf->uf_obuf_sz);
1864        len = min(msgdsize(uf->uf_tx_mp), uf->uf_xfer_sz);

1875        if (len <= 0)
1876                return;
1877        if ((data = allocb(len, BPRI_LO)) == NULL)
1878                return;

1880        /*
1881         * copy no more than 'len' bytes from mblk chain to transmit mblk 'data'
1882         */
1883        data_len = 0;
1884        while (data_len < len && uf->uf_tx_mp) {
1885                mp = uf->uf_tx_mp;
1886                copylen = min(MBLKL(mp), len - data_len);
1887                bcopy(mp->b_rptr, data->b_wptr, copylen);
1888                mp->b_rptr += copylen;
```

```
1889                 data->b_wptr += copylen;
1890                 data_len += copylen;

1892                 if (MBLKL(mp) < 1) {
1893                         uf->uf_tx_mp = unlinkb(mp);
1894                         freeb(mp);
1895                 } else {
1896                         ASSERT(data_len == len);
1897                 }
1898         }

1900         ASSERT(data_len > 0);

1902         uf->uf_bulkout_state = UFTDI_PIPE_BUSY;
1903         mutex_exit(&uf->uf_lock);

1905         rval = uftdi_send_data(uf, data);
1906         mutex_enter(&uf->uf_lock);

1908         if (rval != USB_SUCCESS) {
1909                 uf->uf_bulkout_state = UFTDI_PIPE_IDLE;
1910                 uftdi_put_head(&uf->uf_tx_mp, data);
1911         } else {
1912                 if (xferd)
1913                         *xferd = data_len;
1914         }
1915 }
```

_____***unchanged_portion_omitted_***

```
**********************************************************
    4514 Sun Dec 16 10:46:52 2012
new/usr/src/uts/common/sys/usb/clients/usbser/usbftdi/uftdi_var.h
3419 usbftdi needs to support the BeagleBone
**********************************************************
```

```
/*
 * CDDL HEADER START
 *
 * The contents of this file are subject to the terms of the
 * Common Development and Distribution License (the "License").
 * You may not use this file except in compliance with the License.
 *
 * You can obtain a copy of the license at usr/src/OPENSOLARIS.LICENSE
 * or http://www.opensolaris.org/os/licensing.
 * See the License for the specific language governing permissions
 * and limitations under the License.
 *
 * When distributing Covered Code, include this CDDL HEADER in each
 * file and include the License file at usr/src/OPENSOLARIS.LICENSE.
 * If applicable, add the following below this CDDL HEADER, with the
 * fields enclosed by brackets "[]" replaced with your own identifying
 * information: Portions Copyright [yyyy] [name of copyright owner]
 *
 * CDDL HEADER END
 */
/*
 * Copyright 2009 Sun Microsystems, Inc.  All rights reserved.
 * Use is subject to license terms.
 */
/*
 * Copyright 2012 Hans Rosenfeld <rosenfeld@grumpf.hope-2000.org>
 */

#ifndef _USBSER_USBFTDI_UFTDI_VAR_H
#define _USBSER_USBFTDI_UFTDI_VAR_H

/*
 * USB UFTDI definitions
 */

#include <sys/types.h>
#include <sys/ddityp
#include <sys/note.h>

#include <sys/usb/clients/usbser/usbser_dsdi.h>

#ifdef  __cplusplus
extern "C" {
#endif

/*
 * PM support
 */
typedef struct uftdi_pm {
        uint8_t         pm_wakeup_enabled;      /* remote wakeup enabled */
        uint8_t         pm_pwr_states;  /* bit mask of power states */
        boolean_t       pm_raise_power; /* driver is about to raise power */
        uint8_t         pm_cur_power;   /* current power level */
        uint_t          pm_busy_cnt;    /* number of set_busy requests */
} uftdi_pm_t;
```

*_____unchanged_portion_omitted_*

```
_NOTE(SCHEME_PROTECTS_DATA("uftdi_regs", uftdi_regs))

/*
 * per device state structure
```



```
 */
typedef struct uftdi_state {
        kmutex_t                uf_lock;                /* structure lock */
        dev_info_t              *uf_dip;                /* device info */
        int                     uf_dev_flags;           /* device flags */
        int                     uf_hwport;              /* hw port number */
        int                     uf_port_state;          /* port state */
        int                     uf_port_flags;          /* port flags */
        ds_cb_t                 uf_cb;                  /* DSD callbacks */

        /*
         * USBA
         */
        usb_client_dev_data_t   *uf_dev_data;           /* registration data */
        usb_event_t             *uf_usb_events;         /* usb events */
        usb_pipe_handle_t       uf_def_ph;              /* default pipe hdl */
        usb_pipe_handle_t       uf_bulkin_ph;           /* in pipe hdl */
        int                     uf_bulkin_state;        /* in pipe state */
        usb_pipe_handle_t       uf_bulkout_ph;          /* in pipe hdl */
        int                     uf_bulkout_state;       /* out pipe state */
        usb_log_handle_t        uf_lh;                  /* USBA log handle */
        int                     uf_dev_state;           /* USB device state */
        size_t                  uf_ibuf_sz;             /* input buffer size */
        size_t                  uf_obuf_sz;             /* output buffer size */
        size_t                  uf_xfer_sz;             /* HCI bulk xfer size */

        uftdi_pm_t              *uf_pm;                 /* PM support */

        /*
         * data receive and transmit
         */
        mblk_t                  *uf_rx_mp;              /* rx data */
        mblk_t                  *uf_tx_mp;              /* tx data */
        kcondvar_t              uf_tx_cv;               /* tx completion */

        /*
         * soft registers
         */
        uftdi_regs_t            uf_softr;       /* config registers */
        uint16_t                uf_mctl;        /* modem control */
        uint8_t                 uf_msr;         /* modem status */
        uint8_t                 uf_lsr;         /* line status register */

} uftdi_state_t;

_NOTE(MUTEX_PROTECTS_DATA(uftdi_state::uf_lock, uftdi_state))
_NOTE(DATA_READABLE_WITHOUT_LOCK(uftdi_state::{
        uf_dip
        uf_dev_data
        uf_usb_events
        uf_def_ph
        uf_lh
        uf_ibuf_sz
        uf_obuf_sz
        uf_xfer_sz
        uf_pm
        uf_port_state
        uf_cb
        uf_bulkin_ph
        uf_bulkout_ph
        uf_hwport
}))
```

*_____unchanged_portion_omitted_*

```
#endif

#endif  /* _USBSER_USBFTDI_UFTDI_VAR_H */
```